

Ch 5 Small Signal Analysis of BJTs

→ The small signal means 'less amplitude' signal where 'small' indicates small amplitude (mV) only not small frequency. [milli Volt]

How Conservation of energy takes place in Amplifier?

The d.c. power supply (battery) is given in amplifier ckt. So there is an 'exchange' of d.c. power to the a.c. domain that permits establishing a higher output a.c. power.

→ The superposition theorem is applicable for analysis of a BJT network. It decomposes the d.c. and a.c. components of a BJT network. So one can make a complete d.c. analysis of a system before considering the a.c. response. Then independent a.c. analysis is done. The complete analysis is done by using the superposition theorem. ~~considering~~ these independent analysis in to account.

→ BJT transistor Modelling:-

→ The key to transistor small-signal analysis is use of equivalent circuits (models).

→ A model is a combination of ckt elements, properly chosen, that best approximates the actual behavior of a semiconductor device under specific operating conditions.

D.C analysis

Steps

1) Reduce the a.c sources to zero. This means that a voltage source is replaced by short ckt and current source is replaced by open ckt.

2) Make all the Capacitor open ckted.

$$(X_C = \frac{1}{2\pi f C}, \quad f=0, \quad X_C = \infty)$$

A.C Analysis:-

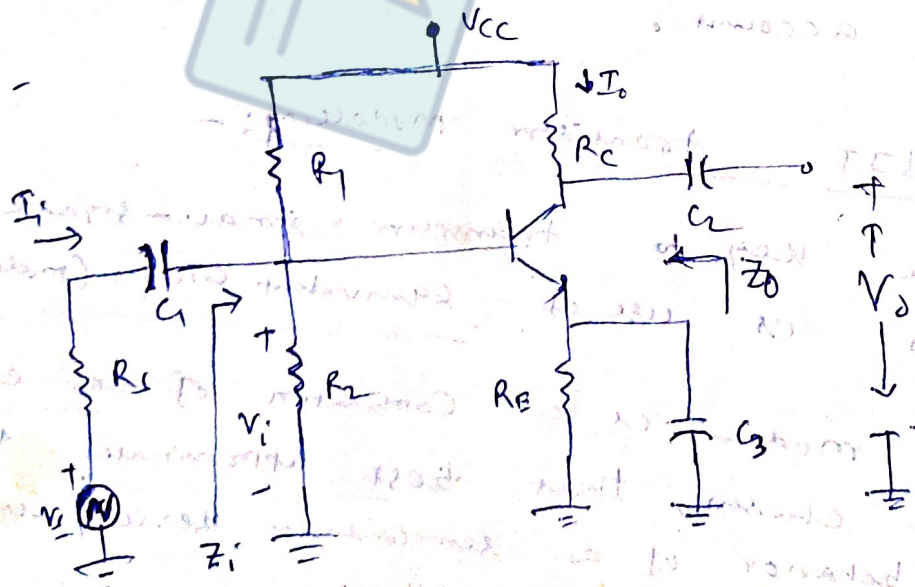
1) Set all d.c sources to zero and replace them by short-ckt equivalent.

2) Replace all capacitors by short-ckt equivalent.

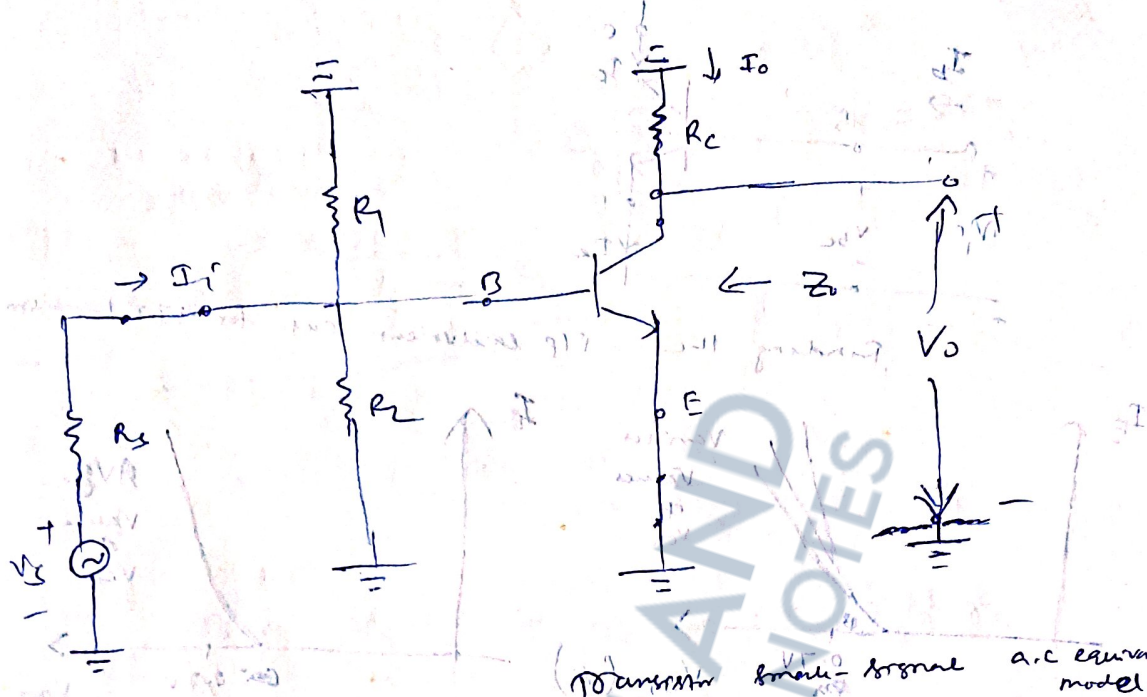
3) Remove all the elements bypassed by the short-ckt equivalent introduced by step 1 and 2.

4) Redraw the n/w in more convenient and logical form.

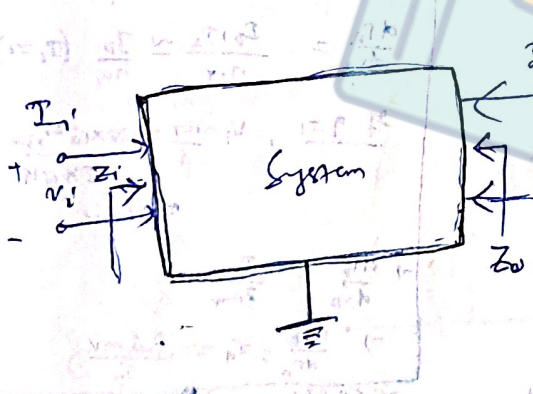
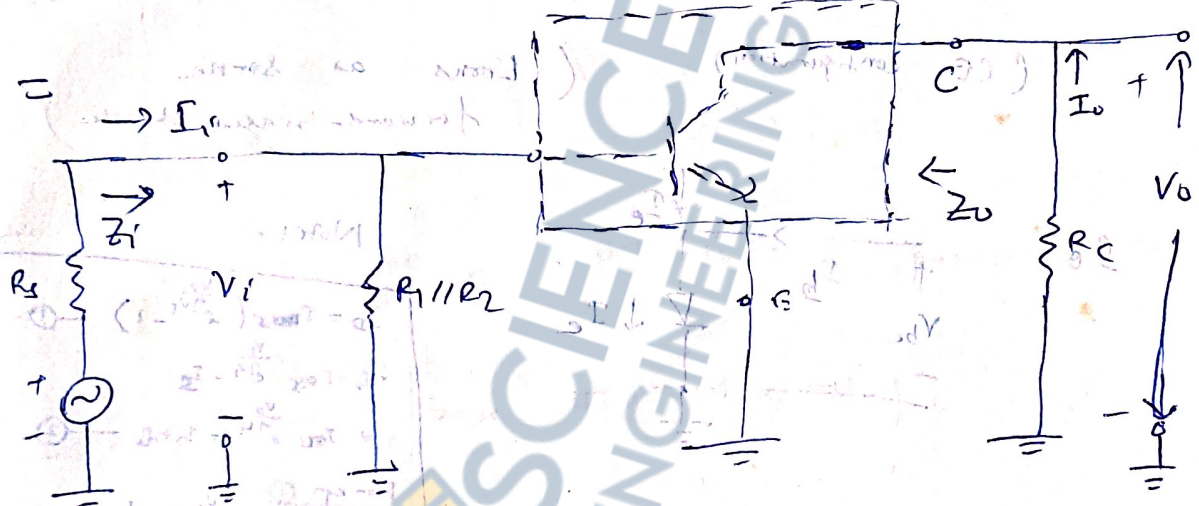
Ex:-



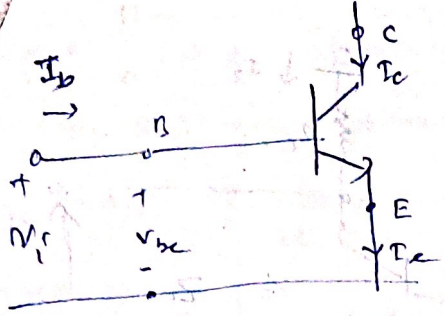
Now, for A-C analysis, d.c. sources should be removed, capacitors short circuited.



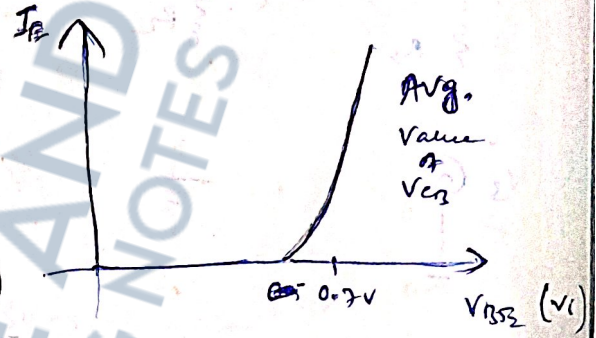
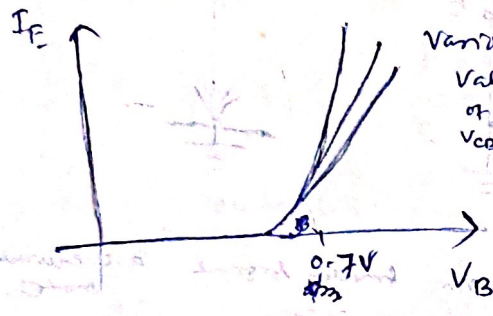
Common-emitter small-signal a.c. equivalent model.



The r_e transistor model :-

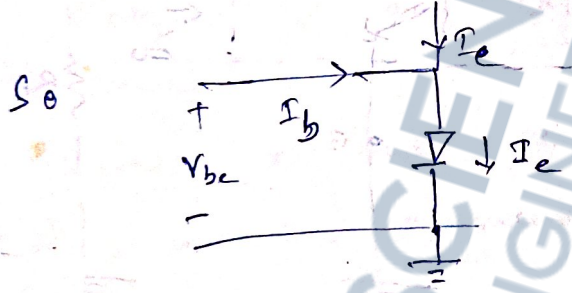


Finding the rP equivalent ckt for BJT transistor

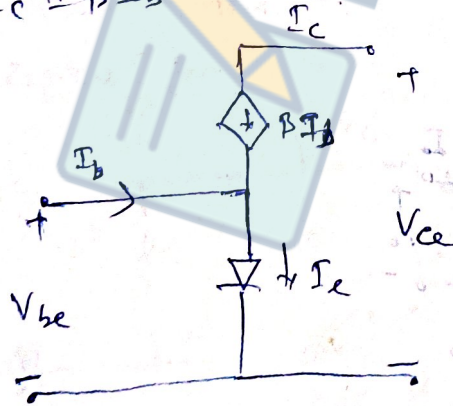


(CE Configuration)

(Looks as simple forward-biased diode)



$I_c = \beta I_b$



Note:-

$$I_D = I_{S_0} (e^{\frac{V_D}{V_T}} - 1) \quad \text{--- (1)}$$

$$\Rightarrow I_D = I_{S_0} \cdot e^{\frac{V_D}{V_T}} - I_{S_0}$$

$$\Rightarrow I_{S_0} \cdot e^{\frac{V_D}{V_T}} = I_D + I_{S_0} \quad \text{--- (2)}$$

From eq (1) $\frac{dI_D}{dV_D} = I_{S_0} \cdot e^{\frac{V_D}{V_T}} \cdot \frac{1}{V_T} \quad \text{--- (3)}$

Putting eq (2) in eq (3)

$$\frac{dI_D}{dV_D} = \frac{I_D + I_{S_0}}{V_T} \approx \frac{I_D}{V_T} \quad (I_{S_0} \approx 0)$$

≈ 1 , $V_T = \frac{kT}{q} = \frac{1.38 \times 10^{-23} \times 300}{1.6 \times 10^{-19}} \approx 26 \text{ mV}$

$$\Rightarrow \frac{dI_D}{dV_D} = \frac{I_D}{26 \text{ mV}}$$

$$\Rightarrow \frac{dV_D}{dI_D} = r_D = \frac{26 \text{ mV}}{I_D}$$

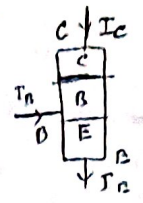
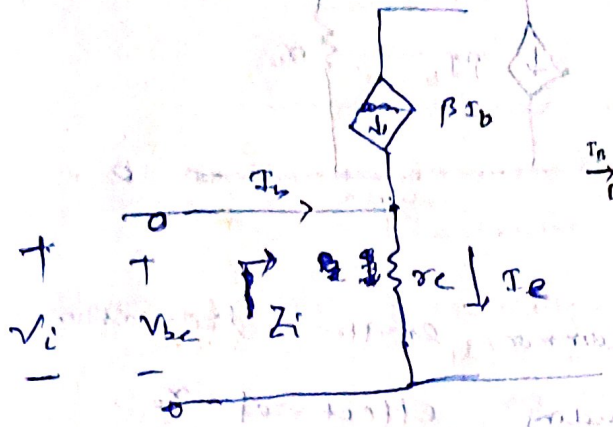
Diode can be replaced by a resistor,

diode's a.c resistance

$$r_D = \frac{26 \text{ mV}}{I_D}$$

(dynamic resistance)

for emitter current, $r_D \rightarrow r_e$
 $I_D \rightarrow I_e$



$$\therefore r_e = \frac{26 \text{ mV}}{I_E}$$

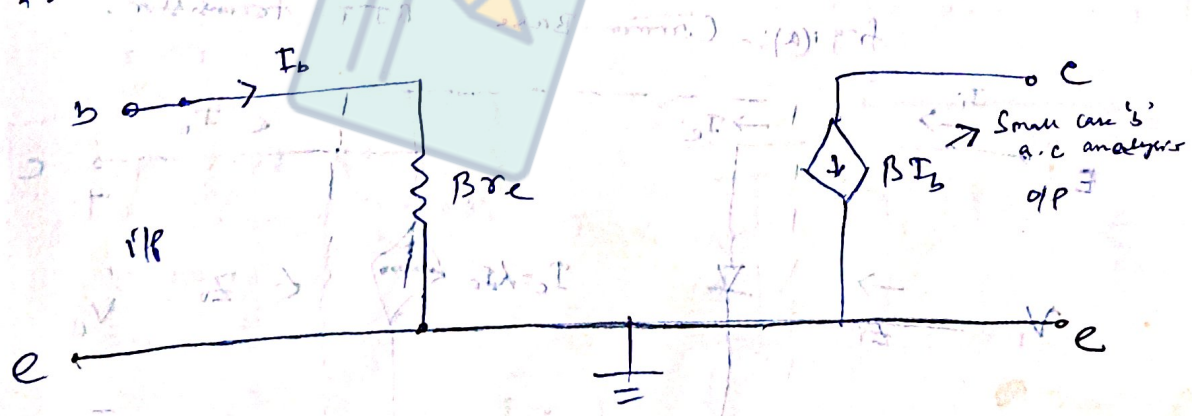
Now for i/p side $Z_i = \frac{V_i}{I_b} = \frac{V_{be}}{I_b}$

But $V_{be} = I_e r_e = (I_c + I_b) r_e$
 $= (\beta I_b + I_b) r_e$
 $= (\beta + 1) I_b r_e$

$$Z_i = \frac{V_{be}}{I_b} = \frac{(\beta + 1) I_b r_e}{I_b} = (\beta + 1) r_e \approx \beta r_e$$

(Looking into a base of the m/w)

The Improved BJT equivalent circuit



To include the o/p cut of BJT, we have to consider o/p resistance too. Using any. value for the o/p resistance, we have

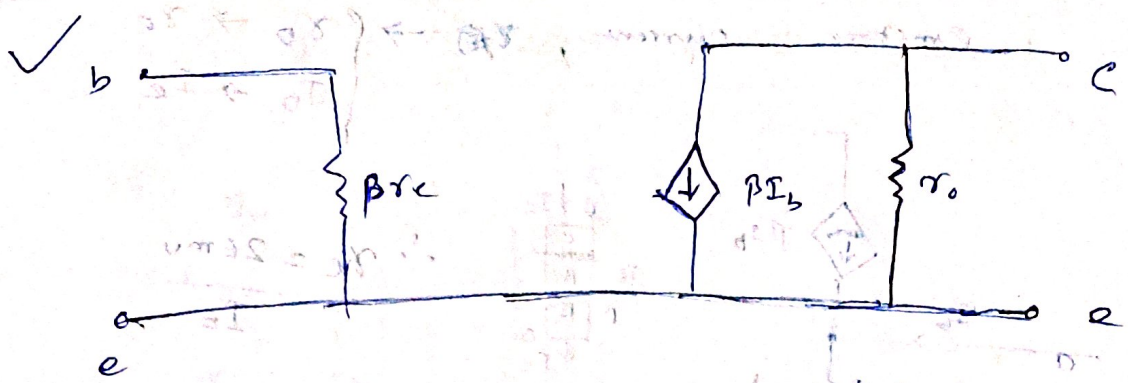
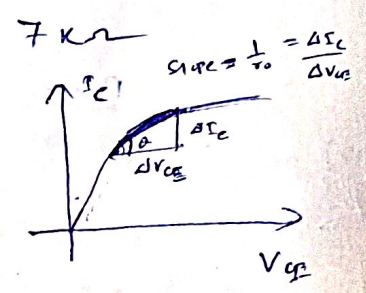


fig-1: r_e model for Common-emitter Configuration including effect of r_o .

$\beta \approx 50$ to 200

$\beta r_e \approx$ few hundred Ohm - $7\text{ k}\Omega$

$r_o \approx 40\text{ k}\Omega$ - $50\text{ k}\Omega$



Common-Base Configuration:

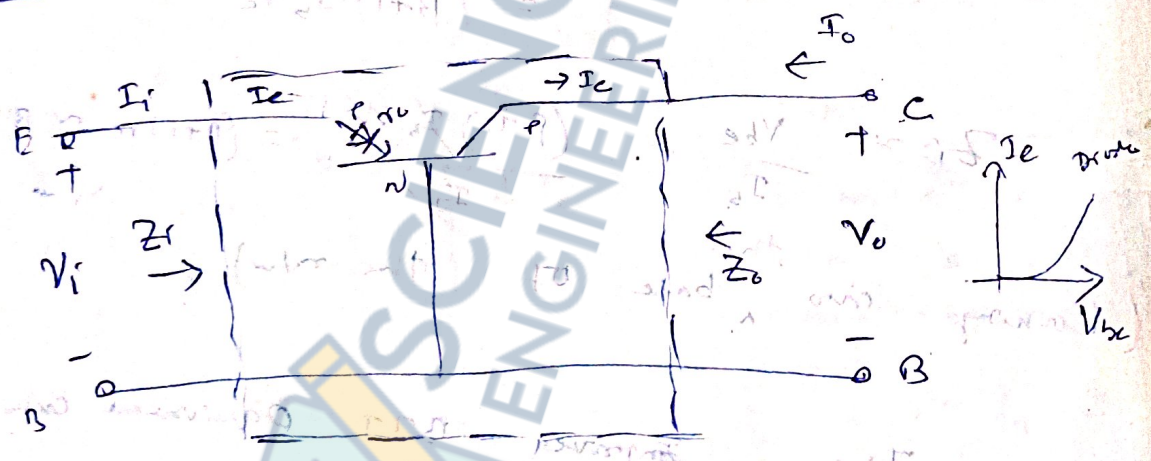


fig 1(a): Common-Base BJT transistor.

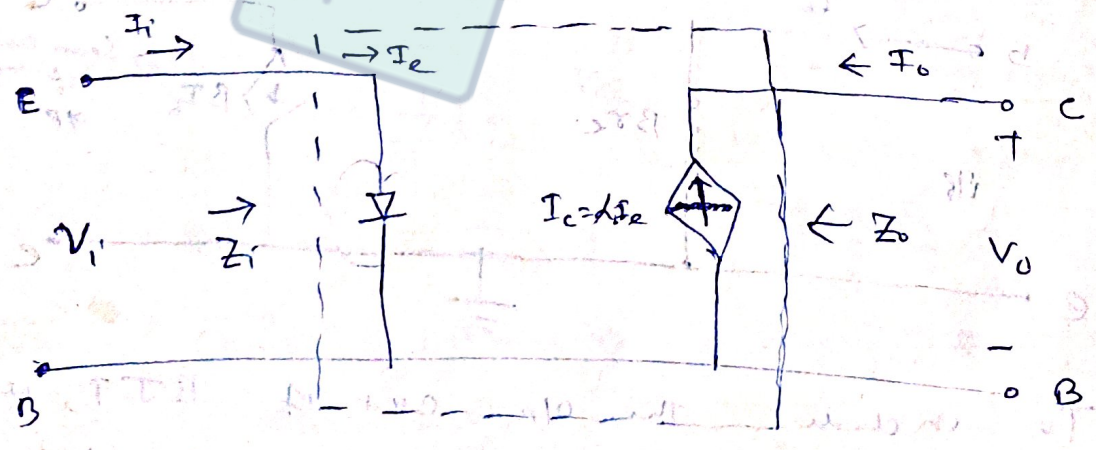
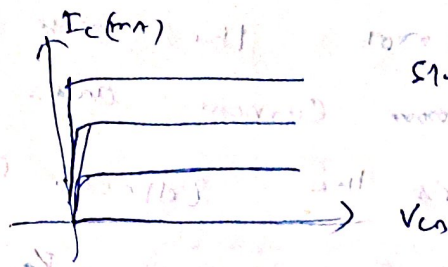


fig:(b) Equivalent ckt for Configuration A (a)



Slope $\approx \frac{1}{r_o}$

Note:-

$$Z_i = \frac{V_{thc}}{I_c} = \frac{I_e r_e}{I_c} = r_e$$

The almost horizontal lines clearly reveal that r_o will be quite high.

(Slope ≈ 0 , \Rightarrow but slope $= \frac{1}{r_o} \Rightarrow r_o \approx \frac{1}{0} \approx \infty$
 $r_o = \infty$ the order of Mega ohm)

Taking r_o into account

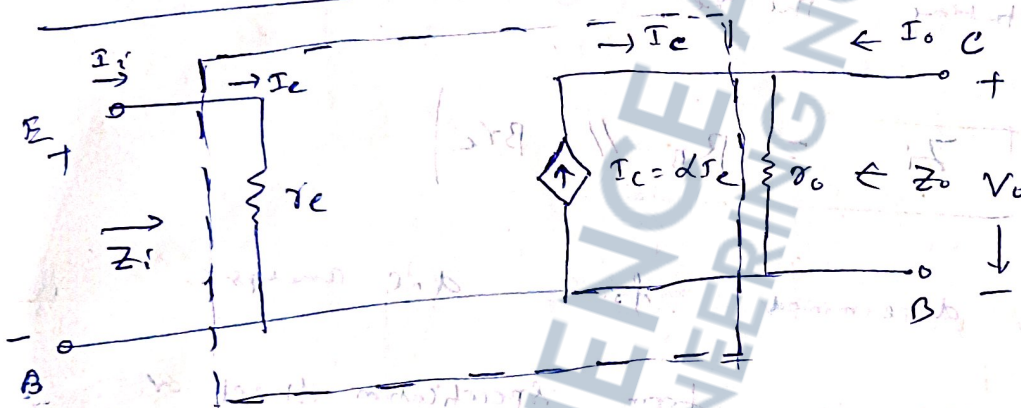
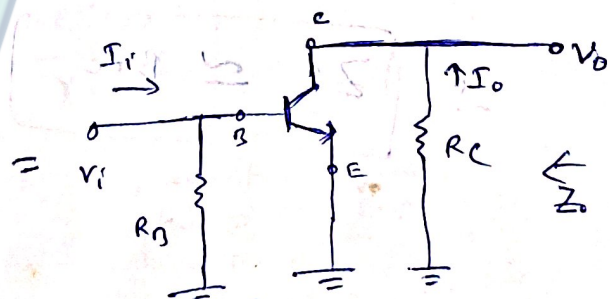
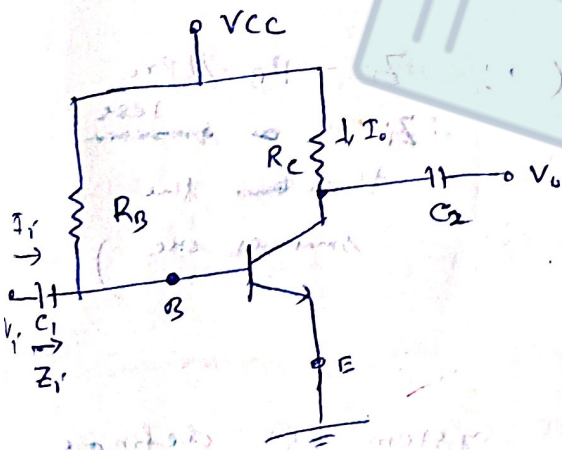


Fig:- Common base r_e equivalent ckt

Common-emitter fixed bias configuration:-

For A.C Analysis

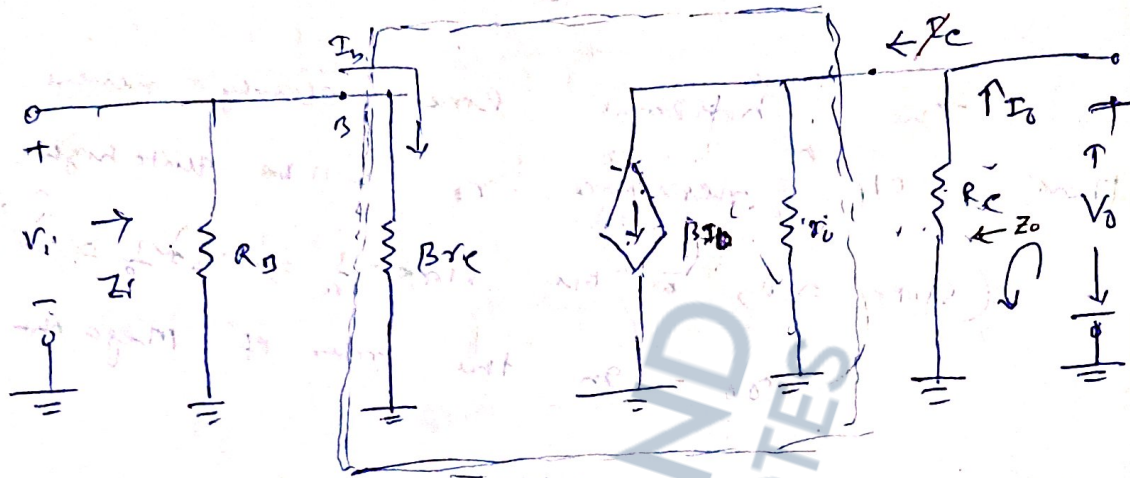


For A.C Analysis

Remaining V_{CC} , Short
 Creating C_1 & C_2

$I_i \neq I_B$

Here I_i is not the base current but the source current and the output current I_o is the collector current.



Substituting the r_e model into the π model

$$Z_i = R_B \parallel \beta r_e$$

$r_e \rightarrow$ determined from d.c analysis.

$r_o \rightarrow$ obtained from specification sheet or characteristics.

For majority situations,

R_B is greater than βr_e by more than a factor 10.

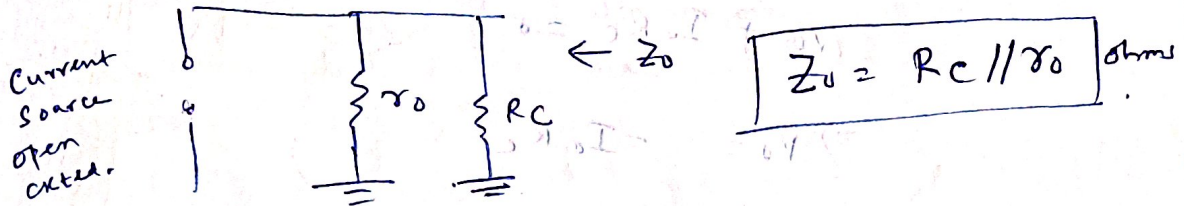
$$Z_i \approx \beta r_e$$

($\because Z_i = R_B \parallel \beta r_e$
 $\therefore Z_i$ is ~~more~~ less than ~~the~~ smallest one)

Determining (Z_o) :-

O/P impedance of any system is defined as the impedance Z_o determined when $V_i = 0$.

When $V_i = 0$, $I_i = 0$, $I_b = 0$



If $r_o \gg 10R_c$,

$Z_o \approx R_c$

Voltage Gain: - (A_v)

$V_o = -(\beta I_b) (R_c || r_o)$

But $I_b = \frac{V_i}{\beta r_e}$

$V_o = -\beta \left(\frac{V_i}{\beta r_e} \right) (R_c || r_o)$

$V_o = -\frac{V_i}{r_e} (R_c || r_o)$

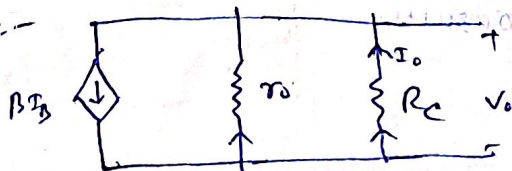
$\Rightarrow \frac{V_o}{V_i} = -\frac{(R_c || r_o)}{r_e}$

$A_v = -\frac{R_c || r_o}{r_e}$

When $r_o \gg 10R_c$,

$A_v = -\frac{R_c}{r_e}$

Derivation:-



By current division rule,

$I_o = \frac{\beta I_b \times r_o}{r_o + R_c}$

Applying KVL

$$V_0 + I_0 R_c = 0$$

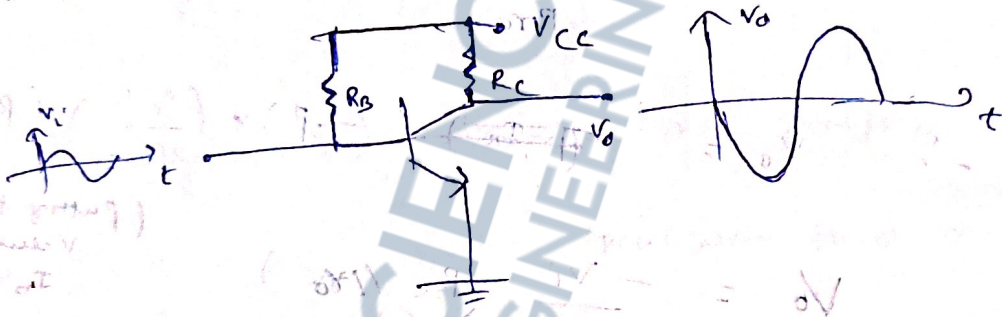
$$\Rightarrow V_0 = -I_0 R_c$$

$$= - \left(\frac{\beta I_B r_o R_c}{r_o + R_c} \right)$$

$$V_0 = -\beta I_B (r_o \parallel R_c) \quad (\text{Proved})$$

Phase shift :-

The $-ve$ sign in A_v $\left[A_v = - \frac{(R_c \parallel r_o)}{r_e} \right]$ reveals that 180° phase shift occurs between the i/p and o/p signals.



Ex:1 - 1) a) Determine r_e

(b) Find Z_i (with $r_o = \infty$)

(c) Calculate Z_o (with $r_o = \infty$)

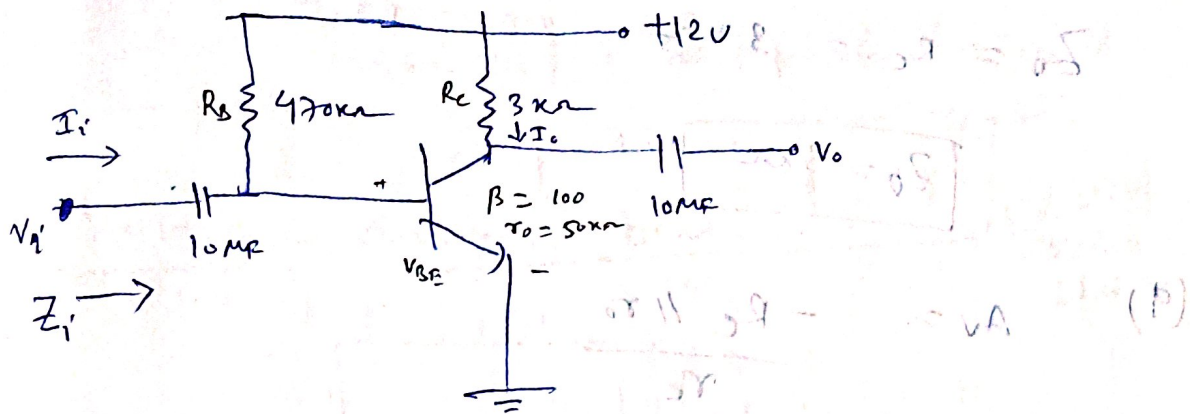
(d) Determine A_v (with $r_o = \infty$)

(e) Repeat part (c) & (d) including

$r_o = 50 \text{ k}\Omega$ in all calculations &

Compare results.

Soln :-



To determine r_e , we have to perform DC analysis, Applying KVL in the i/p loop,

$$12 - I_B \times 470 \times 10^3 - V_{BE} = 0$$

$$\Rightarrow I_B = \frac{12 - 0.7}{470 \times 10^3} = 24.04 \mu\text{A}$$

$$I_E = (\beta + 1) I_B = (100 + 1) \times 24.04 \mu\text{A}$$

$$I_E = 2.428 \text{ mA}$$

$$r_e = \frac{26 \text{ mV}}{I_E} = \frac{26 \times 10^{-3}}{2.428 \times 10^{-3}} = 10.71 \Omega$$

$$r_e = 10.71 \Omega$$

(a)

$$\beta r_e = 100 \times 10.71 = 1071 = 1.07 \text{ k}\Omega$$

(b)

$$Z_i = \beta r_e \parallel R_B = (1.07 \parallel 470) \text{ k}\Omega$$

$$= \frac{1.07 \times 470}{1.07 + 470} = 1.067 \text{ k}\Omega$$

$$Z_i \approx 1.07 \text{ k}\Omega$$

$$(c) Z_o = R_C \parallel r_0 = 3 \text{ k}\Omega \parallel 50 \text{ k}\Omega$$

$$\text{But } r_0 = \infty$$

$$Z_o \approx R_c = 3 \text{ k}\Omega$$

$$Z_o = 3 \text{ k}\Omega$$

$$(9) \quad A_v = \frac{-R_c \parallel r_o}{r_e}$$

$$\approx \frac{-R_c}{r_e}$$

$$= \frac{-3 \text{ k}\Omega}{10.71 \text{ m}\Omega}$$

$$= \frac{-3 \times 10^3}{10.71}$$

$$A_v = -280.11$$

(c) Repeat c & d using $r_o = 50 \text{ k}\Omega$

$$(c) \quad Z_o = R_c \parallel r_o = 3 \text{ k}\Omega \parallel 50 \text{ k}\Omega$$

$$= 3 \text{ k}\Omega \parallel 50 \text{ k}\Omega$$

$$= \frac{3 \times 50}{3 + 50} \text{ k}\Omega$$

$$= 2.83 \text{ k}\Omega \quad (\text{But with } r_o = \infty, Z_i = 3 \text{ k}\Omega)$$

$$(d) \quad A_v = \frac{-R_c \parallel r_o}{r_e}$$

$$R_c \parallel r_o = 3 \text{ k}\Omega \parallel 50 \text{ k}\Omega = 2.83 \text{ k}\Omega$$

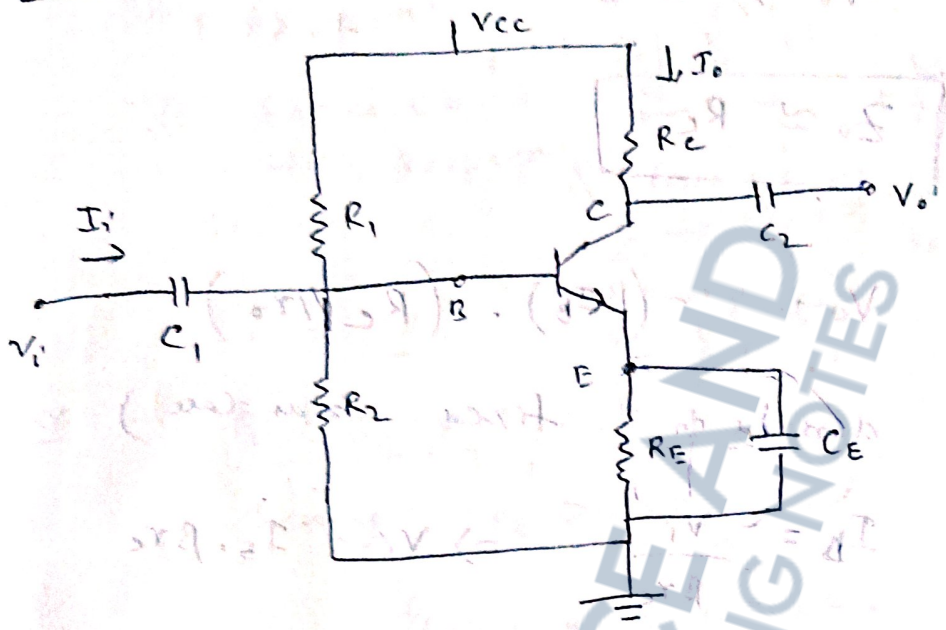
$$A_v = \frac{-2.83 \text{ k}\Omega}{10.71 \text{ m}\Omega} = -264.24$$

$$A_v = -264.24$$

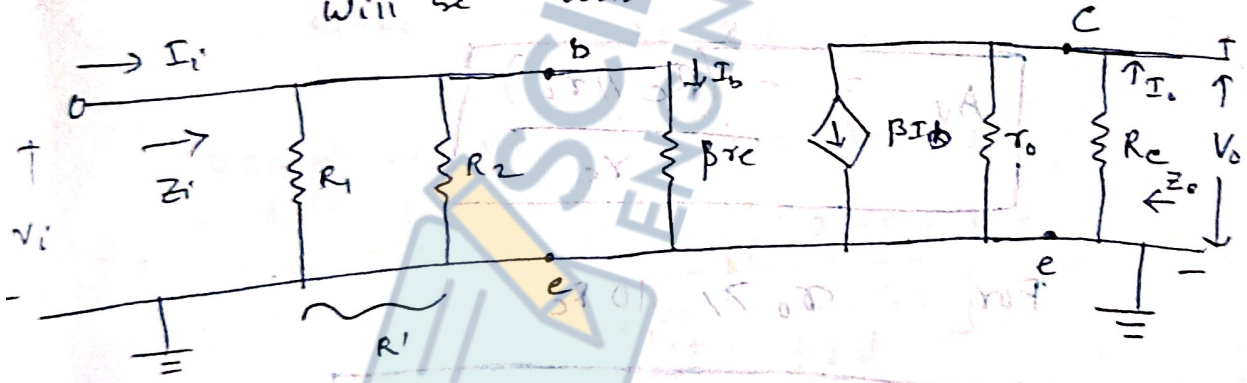
As compared to -280.11
When $r_o \rightarrow \infty$.

Voltage - divider bias :-

(With Bypass Capacitor)



For A.C analysis, $V_{CC} = 0$.
Capacitor C_E is short circuited, so R_E will be absent.



Arg:- Substituting the equivalent circuit into a.c equivalent network.

$$R' = R_1 // R_2 = \frac{R_1 R_2}{R_1 + R_2}$$

$$Z_i = R' // \beta r_c$$

O/P impedance (Z_o) determined when $V_i = 0$

When $V_i = 0V$, $I_B = 0mA$, $\beta I_B = 0mA$

$$Z_o = r_o \parallel R_c$$

$$r_o \gg 10R_c$$

$$Z_o \approx R_c$$

A_v :-

$$V_o = -(\beta I_B) (R_c \parallel r_o)$$

(A_v derived on fixed bias case)

$$I_B = \frac{V_i}{\beta R_c} \Rightarrow V_o = I_B \beta R_c$$

$$\therefore \frac{V_o}{V_i} = \frac{-(\beta I_B) (R_c \parallel r_o)}{(I_B \beta R_c)}$$

$$A_v = \frac{-(R_c \parallel r_o)}{R_c}$$

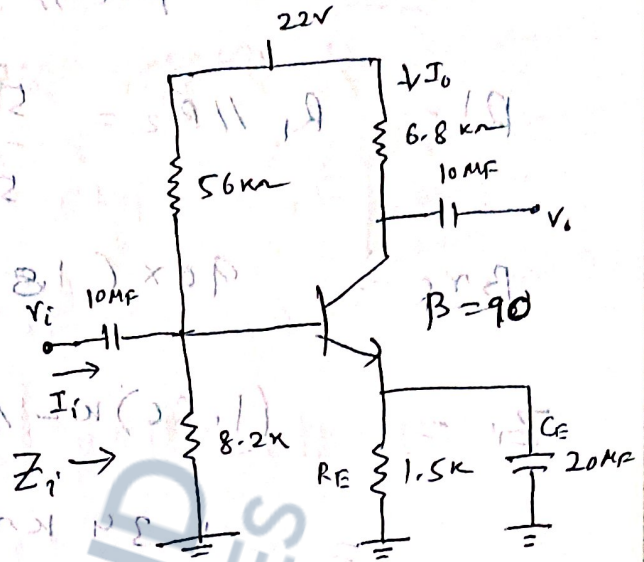
For $r_o \gg 10R_c$

$$A_v \approx \frac{V_o}{V_i} = -\frac{R_c}{R_c}$$

Phase relationship :- The $-ve$ sign of the above equation shows a 180° phase shift betⁿ V_o & V_i

Ex-2: - For the n/w given below, determine

- (a) r_e
 (b) Z_i
 (c) Z_o ($r_o = \infty$)
 (d) A_v ($r_o = \infty$)
 (e) Repeat c & d with $r_o = 50k\Omega$



Ans: - D.C. analysis

Testing $\beta R_E \gg 10 R_2$

$90 \times 1.5k \gg 10 \times 8.2k$

$135k \gg 82k$ (satisfied)

$V_B = \frac{V_{CC} \times R_2}{R_1 + R_2} = \frac{22 \times 8.2}{56 + 8.2} = 2.81 \text{ Volt}$

$V_{BE} = V_B - V_E \Rightarrow V_E = V_B - V_{BE}$

$= 2.81 - 0.7$

$V_E = 2.11 \text{ V}$

$I_E R_E = 2.11$

$I_E = \frac{2.11}{1.5 \times 10^3} = 1.406 \text{ mA} \approx 1.41 \text{ mA}$

$r_e = \frac{26 \text{ mV}}{1.41 \text{ mA}} = 18.44 \Omega$

(a)

$r_e = 18.44 \Omega$

(b) $Z_i = \beta r_e \parallel R_1$

$R_1 = R_1 \parallel R_2 = \frac{56 \times 8.2}{56 + 8.2} = 7.15 \text{ k}\Omega$

$\beta r_e = 90 \times (18.44) = 1.66 \text{ k}\Omega$

$Z_i = (1.66 \text{ k}\Omega \parallel 7.15 \text{ k}\Omega)$
 $= 1.34 \text{ k}\Omega$

(c) $Z_o \approx R_c$ (for $\beta r_e \gg 10 R_c$)

$Z_o = 6.8 \text{ k}\Omega$

(d) $A_v \approx \frac{-R_c}{r_e}$ for $r_e \gg 10 R_c$

$A_v = \frac{-6.8 \text{ k}\Omega}{18.44 \Omega} = -368.76$

(e) $Z_i = 1.34 \text{ k}\Omega$

$Z_o = R_c \parallel r_o$
 $= (6.8 \text{ k}\Omega) \parallel 50 \text{ k}\Omega$

$= 5.98 \text{ k}\Omega$ [Compared to $6.8 \text{ k}\Omega$]

$A_v = \frac{-R_c \parallel r_o}{r_e} = \frac{-5.98 \text{ k}\Omega}{18.44 \Omega} = -324.3$

as compared to
 $\left[\frac{-368.76}{1.81} = 204 \right]$

Common-Emitter [with Bias] Configuration:-

Unbypassed (No bypass capacitor C_E is there)

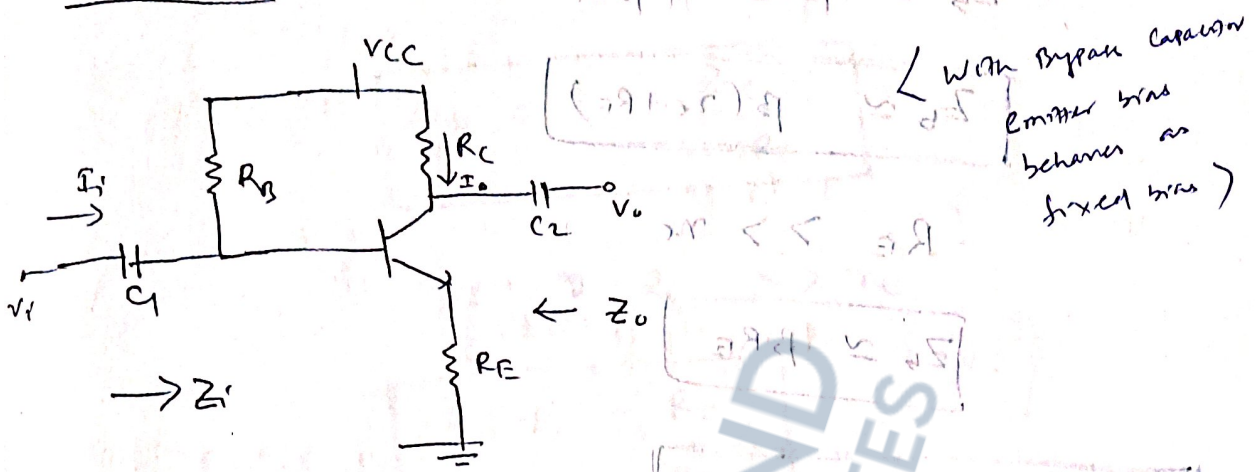


Fig:- CE emitter bias Configuration

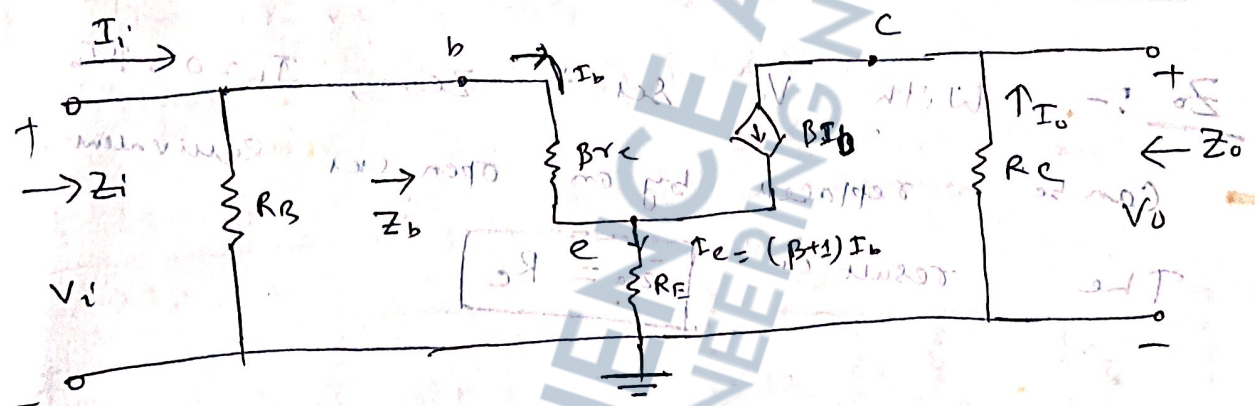


Fig:- substituting the r_e equivalent circuit into the ac equivalent network.

Applying KVL to the r_e side

$$V_i - I_b (\beta r_e) - (\beta + 1) I_b R_E = 0$$

$$\Rightarrow V_i = I_b \beta r_e + (\beta + 1) I_b R_E$$

The r_e impedance looking into the network to the right of R_B

$$Z_b = \frac{V_i}{I_b} = \beta r_e + (\beta + 1) R_E$$

$$\therefore Z_b = \beta r_e + (\beta + 1) R_E$$

Since $\beta + 1 \approx \beta$

$$Z_b \approx \beta r_e + \beta R_E$$

$$Z_b \approx \beta (r_e + R_E)$$

$$R_E \gg r_e$$

$$Z_b \approx \beta R_E$$

$$Z_i = R_B \parallel Z_b$$

Z_o :- With V_i set to zero, $I_b = 0$, βI_b can be replaced by an open-ckt equivalent.

The result is $Z_o = R_c$

A_v :- $I_b = \frac{V_i}{Z_b}$

and $V_o = -I_o R_c = -\beta I_b R_c$

$$V_o = -\beta \left(\frac{V_i}{Z_b} \right) R_c$$

$$\Rightarrow \frac{V_o}{V_i} = \frac{-\beta R_c}{Z_b}$$

$$A_v = \frac{-\beta R_c}{Z_b}$$

But

$$Z_b = \beta (r_e + R_E)$$

$$\therefore A_v = -\frac{\beta R_c}{\beta(r_e + R_E)}$$

$$\Rightarrow A_v = -\frac{R_c}{r_e + R_E}$$

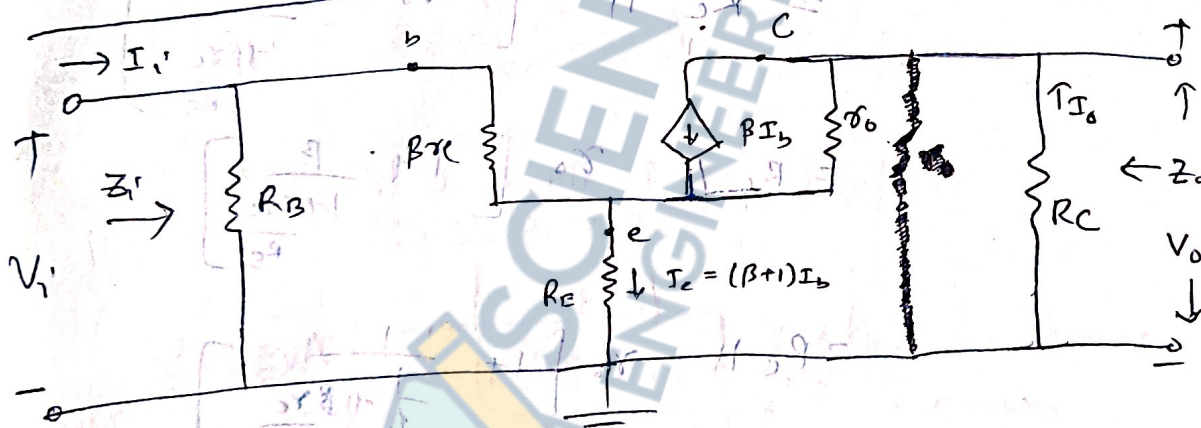
$R_E \gg r_e$

$$A_v = -\frac{R_c}{R_E}$$

Phase: $\rightarrow 180^\circ$
 The 180° phase shift between V_o & V_i shows that 180° phase shift

* Effect of r_o :-

(Not required) *



Additional complexity resulting from including r_o in the analysis. However, when certain conditions are met, the relations return to the form just derived.

$$Z_i = \beta r_e + \left[\frac{(\beta + 1) + \frac{R_c}{r_o}}{1 + \frac{R_c + R_E}{r_o}} \right] R_E$$

The ratio $\frac{R_c}{r_o} \ll \beta + 1$

$$Z_b = \beta r_e + \frac{(\beta + 1) R_E}{1 + \frac{R_c + R_E}{r_o}}$$

$$r_o \gg 10(R_c + R_E)$$

$$Z_b = \beta r_e + (\beta + 1) R_E$$

→ Same as derived earlier.

$$Z_o = R_c \parallel \left[r_o + \frac{\beta(r_o + r_e)}{1 + \beta r_e / R_E} \right]$$

$$r_o \gg r_e$$

$$Z_o = R_c \parallel \left[r_o + \frac{\beta r_o}{1 + \frac{\beta r_e}{R_E}} \right]$$

$$= R_c \parallel r_o \left[1 + \frac{\beta}{1 + \frac{\beta r_e}{R_E}} \right]$$

$$= R_c \parallel r_o \left[1 + \frac{1}{\frac{1}{\beta} + \frac{r_e}{R_E}} \right]$$

For $\beta = 100$, $r_e = 10 \Omega$, $R_E = 1k\Omega$

$$= R_c \parallel$$

$$r_o \left[1 + \frac{1}{\frac{1}{100} + \frac{10}{1000}} \right] = 100 \left[1 + \frac{1}{.02} \right]$$

$$= 51 r_o$$

$$= R_c \parallel 51 r_o$$

$$Z_o \approx R_c \rightarrow \text{Same as derived earlier}$$

$$A_v = \frac{V_o}{V_i} = \frac{-\beta R_c \left[1 + \frac{r_o}{r_o} \right] + \frac{R_c}{r_o}}{Z_b}$$

$$1 + \frac{r_o}{r_o}$$

$$r_o \gg r_e$$

$$\Rightarrow \frac{r_o}{r_o} \ll 1$$

$$A_v = \frac{-\beta R_c + \frac{R_c}{r_o}}{1 + \frac{R_c}{r_o}}$$

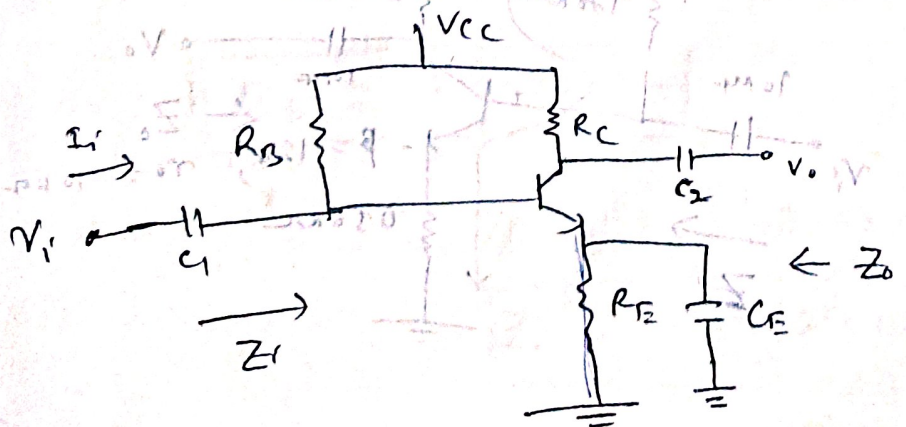
$$\text{If } r_o \geq 10 R_c$$

$$A_v = \frac{-\beta R_c}{Z_b}$$

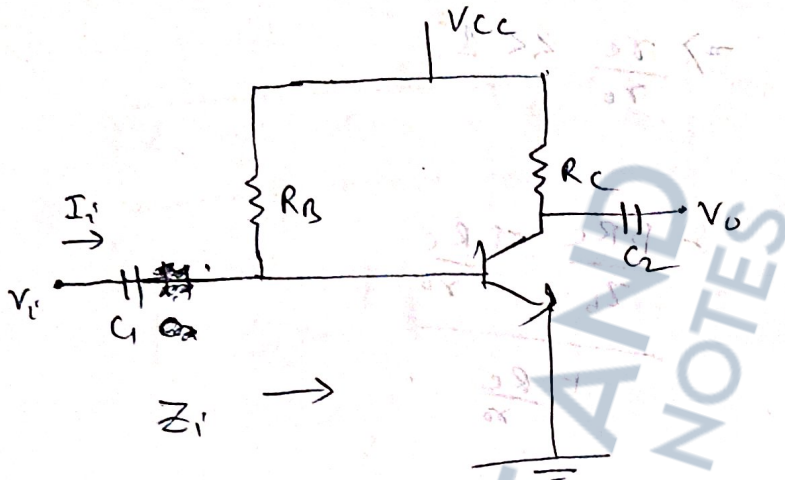
$$A_v = \frac{-\beta R_c}{\beta(r_e + R_e)}$$

$$\Rightarrow A_v = \frac{-R_c}{r_e + R_e} \quad \text{Same as derived earlier}$$

Imp CE - Emitter bias [Bypass capacitor]



Since for A.C analysis, capacitor is short
 circuited ~~CE~~ with capacitor (C_E) will be
 short circuited, so R_E will be short, so
 the emitter bias becomes fixed bias.



So fixed bias eqns are applicable

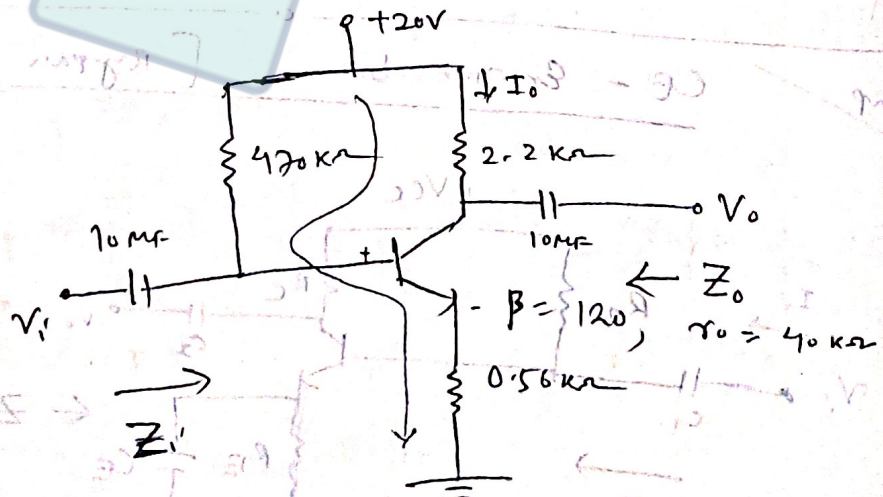
$$Z_i = R_B \parallel \beta r_e \quad \text{or} \quad Z_i \approx \beta r_e$$

$$Z_o = R_C \parallel r_o \quad \text{or} \quad Z_o \approx R_C$$

$$A_v = -\frac{(R_C \parallel r_o)}{r_e} \quad \text{or} \quad A_v = -\frac{R_C}{r_e}$$

Ex-3 :- For the n/w determine

- (a) r_e
- (b) Z_i
- (c) Z_o
- (d) A_v



Soln :- To determine r_e , we need d.c analysis.

Applying KVL in V_{BE} loop

$$20 - I_B \times 470 \times 10^3 - 0.7 - (\beta + 1) I_B R_E = 0$$

$$\Rightarrow I_B = \frac{20 - 0.7}{470 \times 10^3 + (121) \times 0.56 \times 10^3}$$

$$I_B = 35.89 \mu\text{A}$$

$$I_E = (\beta + 1) I_B = 121 \times 35.89 \times 10^{-6}$$

$$I_E = 4.34 \text{ mA}$$

(a) $r_e = \frac{26 \times 10^{-3} \text{ V}}{4.34 \times 10^{-3} \text{ A}} = 5.99 \Omega$

(b) Testing Condition

$$r_o \gg 10(R_C + R_E)$$

$$40 \text{ k}\Omega \gg 10(2.2 + 0.56) \text{ k}\Omega$$

$$40 \text{ k}\Omega \gg 27.6 \text{ k}\Omega$$

Then effect of r_o can be neglected. (Satisfied)

$$Z_b = \beta (r_e + R_E)$$

$$= 120 (5.99 + 560)$$

$$Z_b = 67.92 \text{ k}\Omega$$

$$Z_i = Z_b \parallel R_B = 67.92 \text{ k}\Omega \parallel 470 \text{ k}\Omega$$

$$Z_i = 59.34 \text{ k}\Omega$$

(c) $Z_o = R_c = 2.2 \text{ k}\Omega$

(d) $r_o \gg 10 R_c$

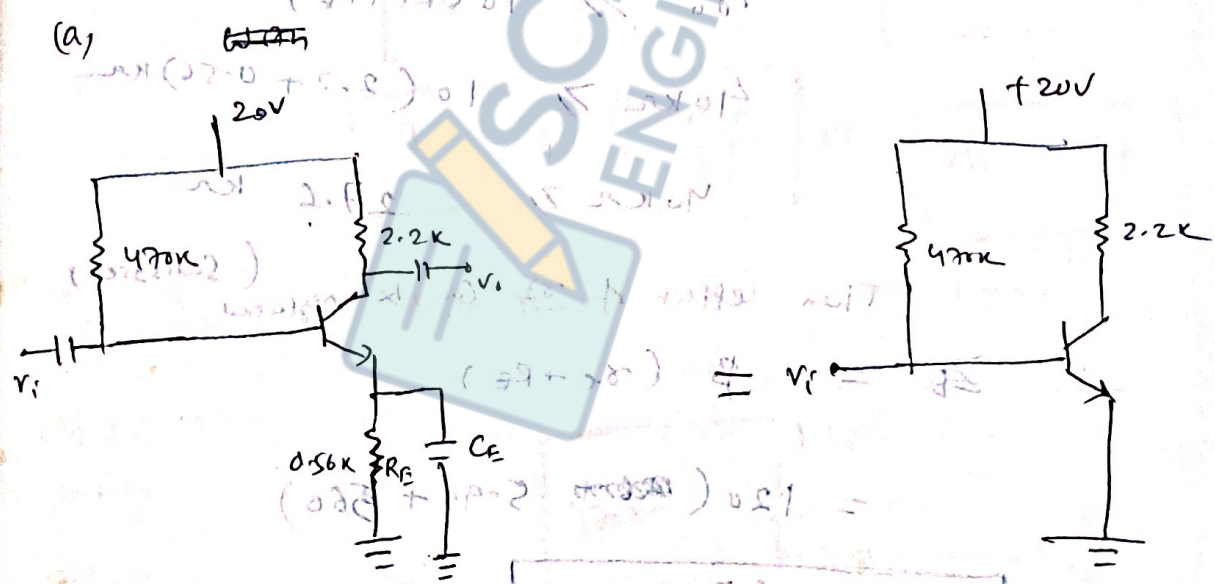
$40 \text{ k}\Omega \gg 10 \times 2.2 \text{ k}\Omega$

$$A_v = \frac{-\beta R_c}{Z_b} = \frac{-120 \times 2.2 \text{ k}\Omega}{67.92 \text{ k}\Omega}$$

$A_v \approx -3.89$

or $A_v = \frac{-R_c}{R_E} = \frac{-2.2 \text{ k}\Omega}{0.56 \text{ k}\Omega} \approx -3.93$

EX:-4 Repeat the analysis with by pass capacitor C_E in place.



R_E is short, because capacitor is short ckted.

$Z_b = 47 \text{ k}\Omega \parallel 2.2 \text{ k}\Omega = 2.12 \text{ k}\Omega$

So we can apply the formula of fixed bias

(a) $r_e = \frac{V_{BE}}{I_E} = \frac{0.7}{5.99} = 0.117 \text{ } \Omega$ [same] [Because for d.c analysis, R_E will be there] [Capacitor open circuit for d.c analysis]

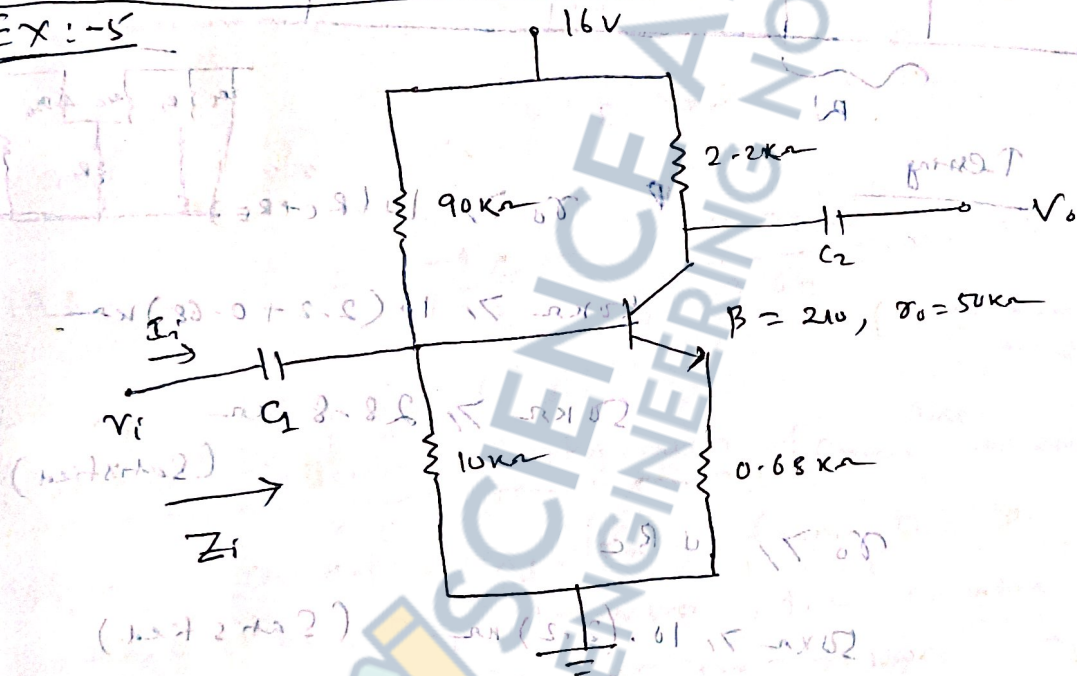
(b) $Z_i = R_B \parallel \beta r_e = 470 \text{ k}\Omega \parallel (120)(0.117) = 717.70 \text{ } \Omega$

(c) $Z_o = R_C = 2.2 \text{ k}\Omega$

(d) $A_v = \frac{-R_C}{r_e} = \frac{-2.2 \text{ k}\Omega}{0.117} = -367.28$

Voltage divider biasing with unbypassed capacitor

EX: -5



Determine r_e, Z_i, Z_o, A_v .

Ans: Testing

$\beta R_E > 10 R_2$
 $210 \times 0.68 \times 10^3 > 10 \times 10 \text{ k}\Omega$

$142.8 \text{ k}\Omega > 100 \text{ k}\Omega$

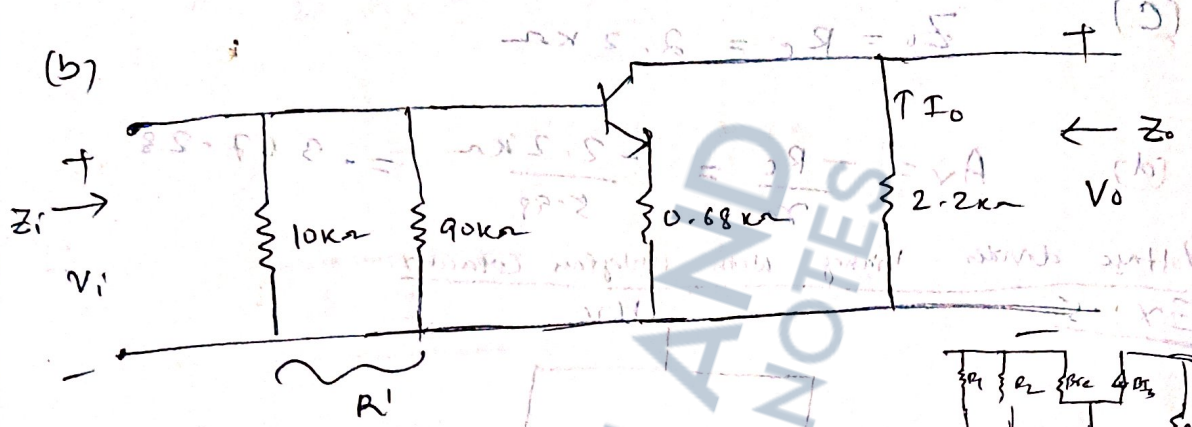
$\therefore V_B = \frac{V_{CC}}{R_1 + R_2} \times R_2 = \frac{16}{90 + 10} \times 10 = 1.6 \text{ V}$

$V_{BE} = V_B - V_E \Rightarrow 0.7 = 1.6 - V_E$
 $\therefore V_E = 0.9 \text{ V}$

$I_E R_E = V_E$

$\Rightarrow I_E = \frac{V_E}{R_E} = \frac{0.9 \text{ V}}{0.68 \text{ k}\Omega} = 1.324 \text{ mA}$

(a) $r_e = \frac{26 \text{ mV}}{I_E} = \frac{26 \text{ mV}}{1.324 \text{ mA}} = 19.64 \Omega$



$r_o \gg 10(R_C + R_E)$
 $50 \text{ k}\Omega \gg 10(2.2 + 0.68) \text{ k}\Omega$
 $50 \text{ k}\Omega \gg 28.8 \text{ k}\Omega$ (Satisfied)
 $r_o \gg 10 R_C$
 $50 \text{ k}\Omega \gg 10 \cdot (2.2) \text{ k}\Omega$ (Satisfied)

$Z_b = \beta R_E = 210 \times 0.68 = 142.8 \text{ k}\Omega$

(Derivation is similar to emitter bias with bypass capacitor)

$Z_i = R' \parallel Z_b = \left(\frac{10 \times 90}{10 + 90} \right) \parallel 142.8 \text{ k}\Omega$

$8.24 \text{ k}\Omega \parallel 142.8 \text{ k}\Omega$

$Z_i = 8.47 \text{ k}\Omega$

(c) $Z_o = R_C = 2.2 \text{ k}\Omega$

(d) $A_v = \frac{-R_C}{R_E} = \frac{-2.2 \text{ k}\Omega}{0.68 \text{ k}\Omega} = -3.23$

Ex-6 :- Repeat example 5 with bypass capacitor (C_E).

→ It will be like voltage divider bias configuration (in ex-2)

Ans = (a) $r_c = 19.64 \Omega$

(b) $Z_i = \beta r_c \parallel R_1$
 $= (210)(19.64) \parallel 9 \text{ k}\Omega$
 $= 4.12 \parallel 9 \text{ k}\Omega$
 $Z_i = 2.83 \text{ k}\Omega$

(c) $Z_o = R_c = 2.2 \text{ k}\Omega$

(d) $A_v = \frac{-R_c}{r_c} = \frac{-2.2 \text{ k}\Omega}{19.64} = -112.02$
 (Significant increase)

Emitter-Follower Configuration [Common Collector] Also

When output is taken from the emitter terminal of the transistor, the network is referred to as an emitter-follower. The output voltage is always & slightly less than the i/p signal due to the drop from base to emitter, but approximately $A_v \approx 1$, is good.

→ Unlike Collector voltage, emitter voltage is in phase with signal V_i . That is, both V_o & V_i attain their positive & -ve peak values at the same time. The fact that V_o "follows" the magnitude of V_i with an

In-phase relationship accounts for the terminology

emitter-follower,

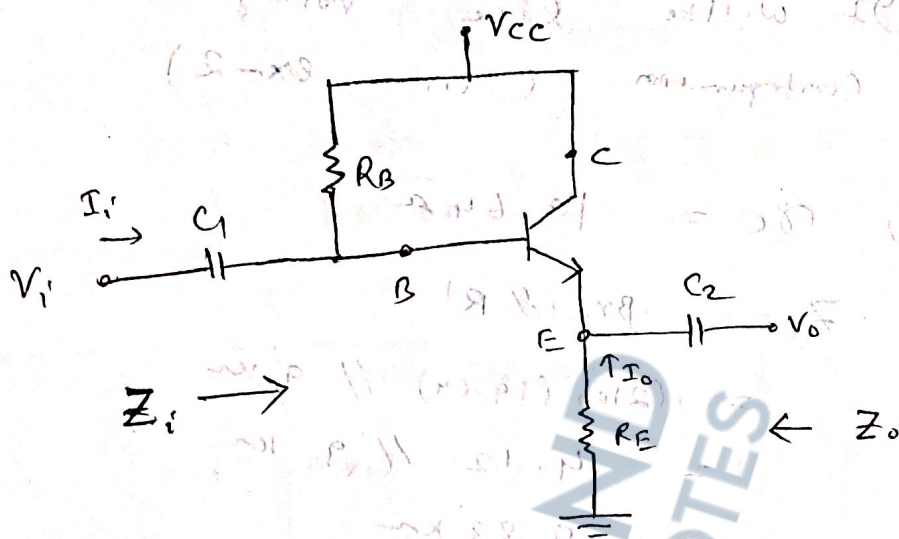


Fig 1:- Emitter follower Configuration

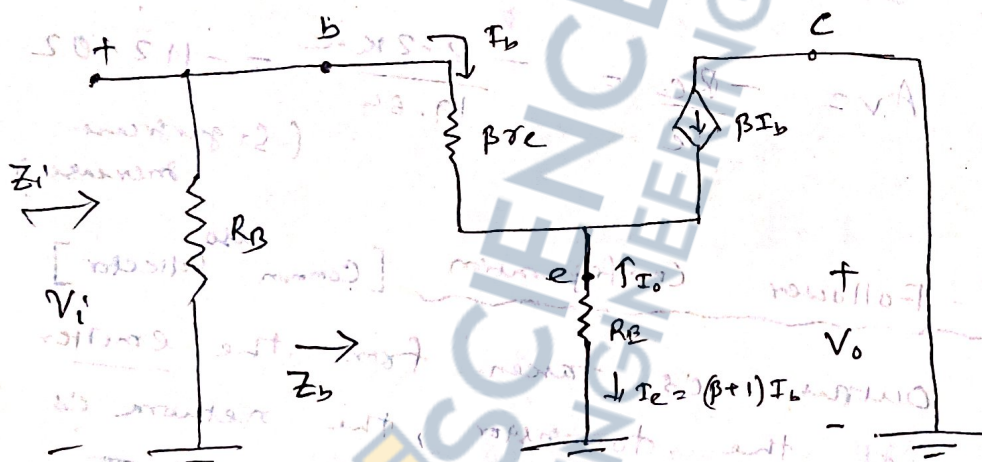


Fig 2:- Substituting the ac equivalent circuit of Fig 1

$$Z_i = R_B \parallel Z_b$$

With $Z_b = \beta r_e + (\beta + 1) R_E$

$$Z_b \approx \beta (r_e + R_E)$$

$$Z_b \approx \beta R_E$$

$$\beta R_E \gg r_e$$

The O/P impedance is best described by first writing the equation for the current I_b ,

$$I_b = \frac{V_i}{Z_b}$$

$$\Rightarrow (\beta+1) I_b = (\beta+1) \frac{V_i}{Z_b} \quad \left[\begin{array}{l} \text{multiplying} \\ (\beta+1) \text{ in} \\ \text{both the sides} \end{array} \right]$$

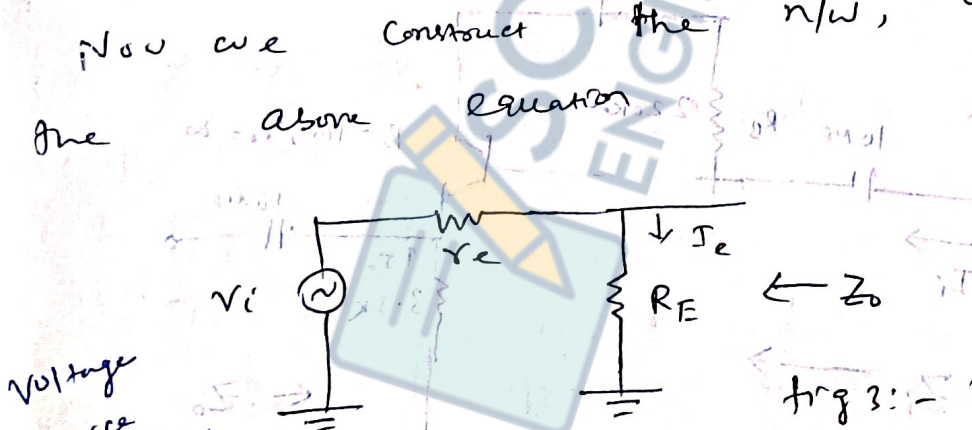
$$\Rightarrow I_e = \frac{(\beta+1) V_i}{\beta r_e + (\beta+1) R_E} \quad \left[\begin{array}{l} \text{Substituting} \\ \text{value of } Z_b \end{array} \right]$$

$$= \frac{V_i}{\frac{\beta r_e}{\beta+1} + R_E}$$

$$\beta+1 \approx \beta \quad \Rightarrow \frac{\beta r_e}{\beta+1} \approx \frac{\beta r_e}{\beta} = r_e$$

$$I_e \approx \frac{V_i}{r_e + R_E}$$

Now we construct the n/w, defined by the above equation



Voltage source short cktd.

$$Z_o = R_E \parallel r_e$$

Since $R_E \gg r_e$

$$Z_o \approx r_e$$

Fig 3:- Defining o/p impedance for emitter-follower configuration

A_v :- Fig (3) (previous page) can be used to determine the voltage gain through an applⁿ of the voltage-divider rule.

$$V_o = \frac{V_i}{r_e + R_E} \times R_E$$

$$\Rightarrow \frac{V_o}{V_i} = \frac{R_E}{r_e + R_E} \quad (1)$$

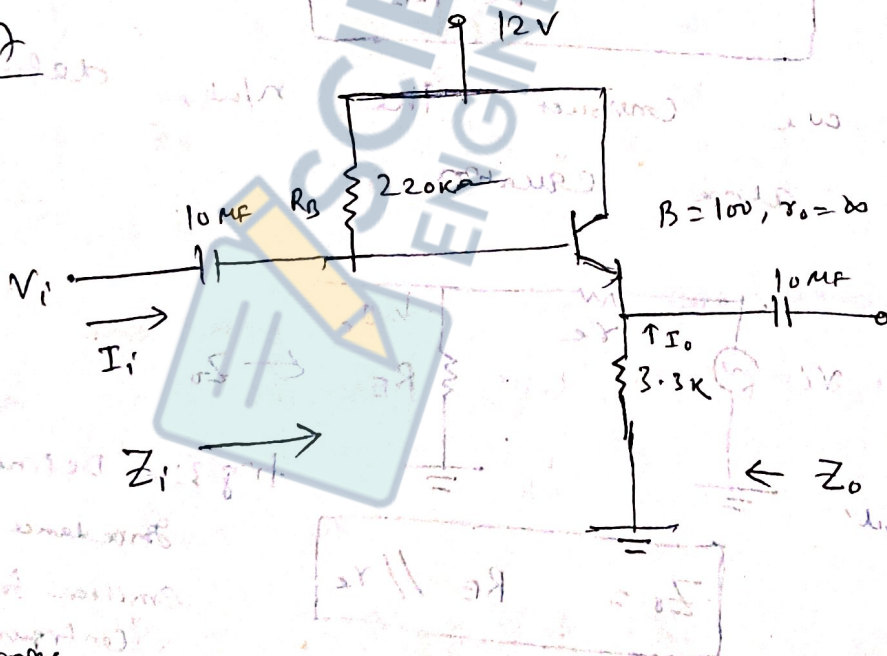
Since $R_E \gg r_e$, $\frac{V_o}{V_i} \approx \frac{R_E}{R_E}$

$$A_v \approx 1$$

Phase relationship :-

From eqⁿ (1), the V_o & V_i are in phase for emitter-follower configuration.

EX 7



Determine

- (a) r_e , (b) Z_i , (c) Z_o , (d) A_v

Ans :- $I_B = \frac{V_{CC} - V_{BE}}{R_B + (\beta + 1)R_E}$

$$(\because V_{CC} - I_B R_B - V_{BE} - (\beta + 1) I_B R_E = 0)$$

$$\Rightarrow I_B = \frac{12 - 0.7}{220 \times 10^3 + (101) \times 3.3 \times 10^3}$$

$$I_B = 20.42 \mu\text{A}$$

$$I_E = (\beta + 1) I_B = (101) \times 20.42 \times 10^{-6}$$

$$I_E = 2.062 \text{ mA}$$

$$r_{ce} = \frac{26 \times 10^3}{2.062 \times 10^{-3}} = 12.61 \Omega$$

(b)

$$Z_b = \beta r_{ce} + (\beta + 1) R_E$$

$$= 100 \times 12.61 + 101 \times 3.3 \times 10^3$$

$$= (1.261 + 333.3) \text{ k}\Omega$$

$$= 334.56 \text{ k}\Omega$$

$$Z_i = Z_b \parallel R_B = (334.56 \parallel 220) \text{ k}\Omega$$

$$Z_i = 132.72 \text{ k}\Omega$$

$$(c) Z_o = R_E \parallel r_{ce} = 3.3 \text{ k}\Omega \parallel 12.61 \Omega$$

$$= 12.56 \Omega \approx r_{ce}$$

$$(d) A_v = \frac{V_o}{V_i} = \frac{R_E}{R_E + r_{ce}} = \frac{3.3 \times 10^3}{3.3 \times 10^3 + 12.61} = 0.996 \approx 1$$

Note :-

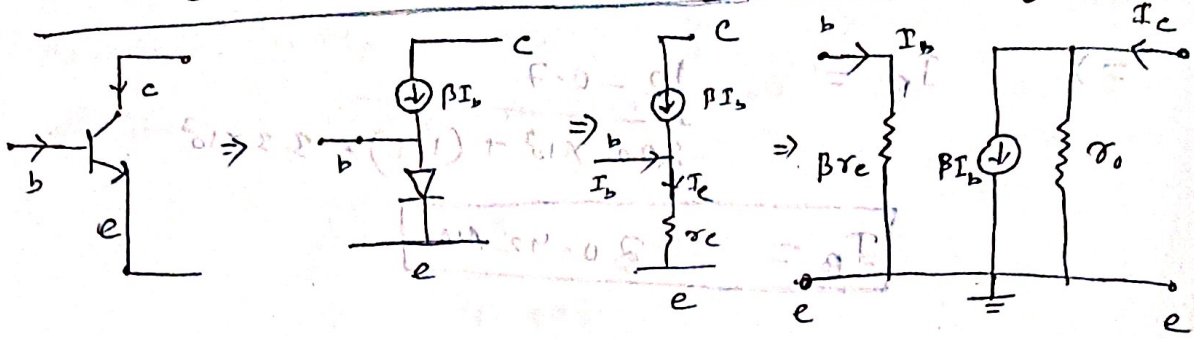
$$(e) A_I = \frac{I_o}{I_i} = \frac{\beta R_E}{R_B + Z_b}$$

$$I_o = \beta I_b$$

$$I_b = \frac{I_i \times R_B}{R_B + Z_b} \Rightarrow I_o = \beta \frac{I_i (R_B + Z_b)}{R_B}$$

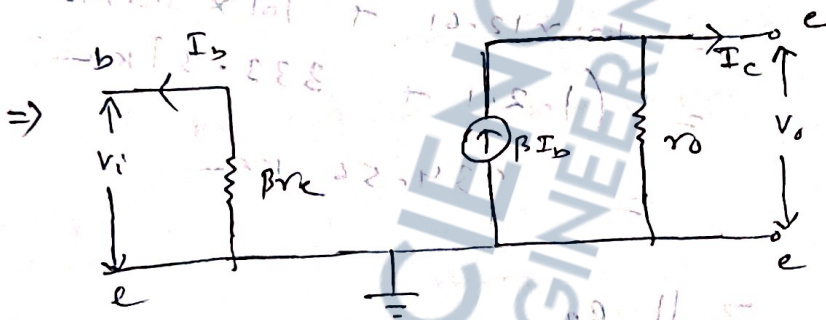
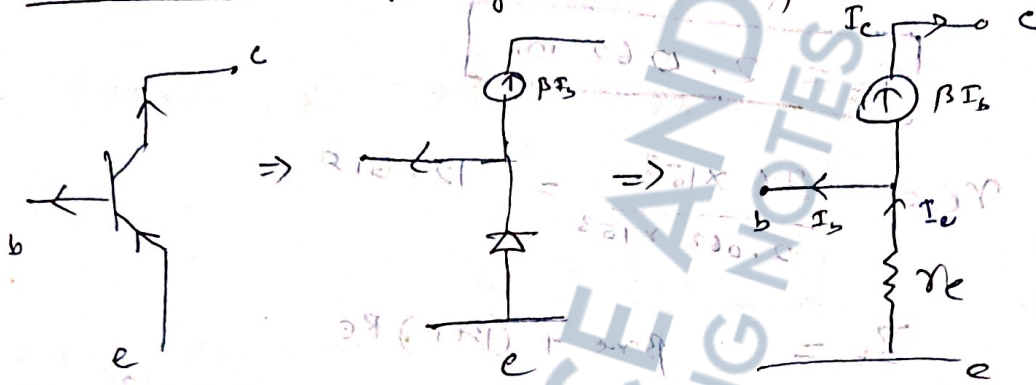
$$\frac{I_o}{I_i} = \frac{\beta R_E \times R_B}{R_B + Z_b} = \frac{\beta R_E}{R_B + Z_b}$$

Brief form of r_e -model of CE Configuration



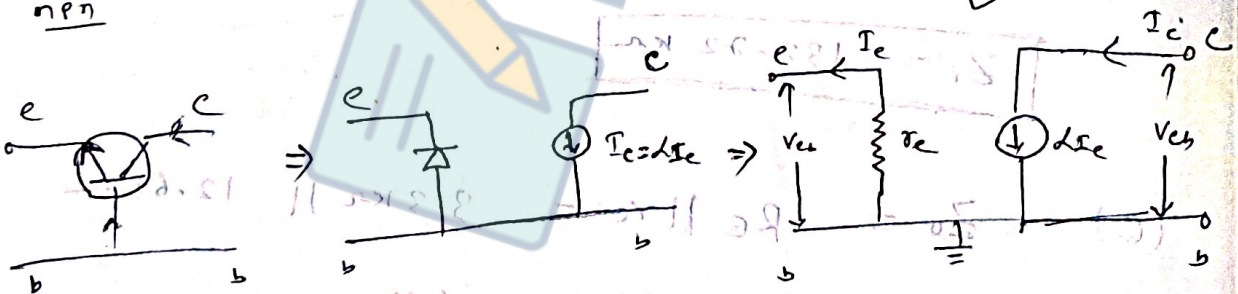
(fig: 1) r_e model for n-p-n

For PNP (only change in direction)

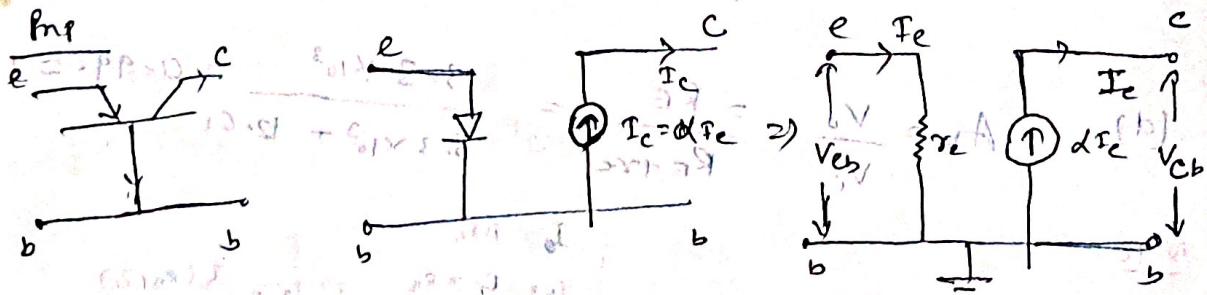


r_e -model for CB Configuration:-

n-p-n



PNP



Common-Base Configuration :-

The Common-base Configuration is characterized as having a relatively low r_{iP} and high r_{oP} impedance and current gain less than 1. The voltage gain, however can be quite large.

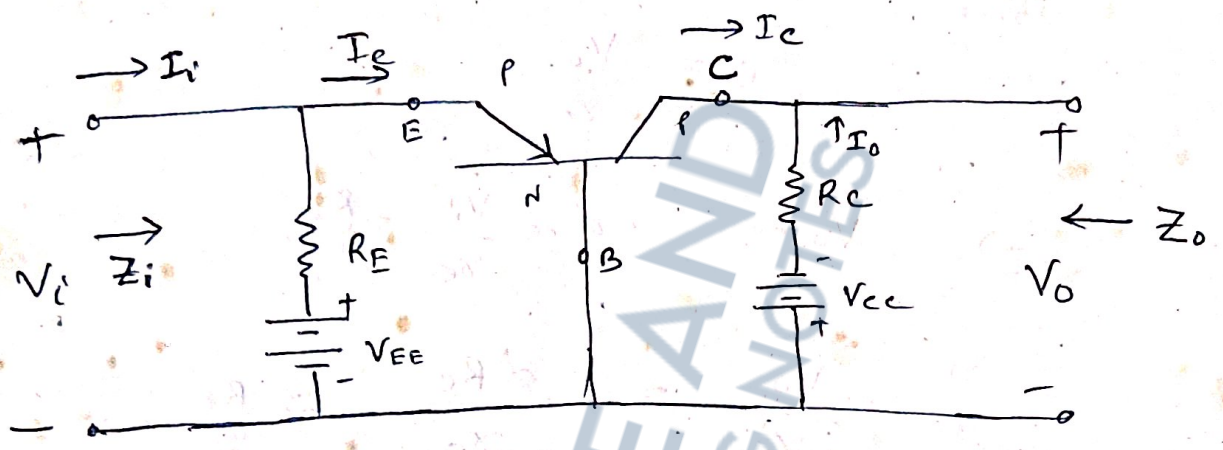
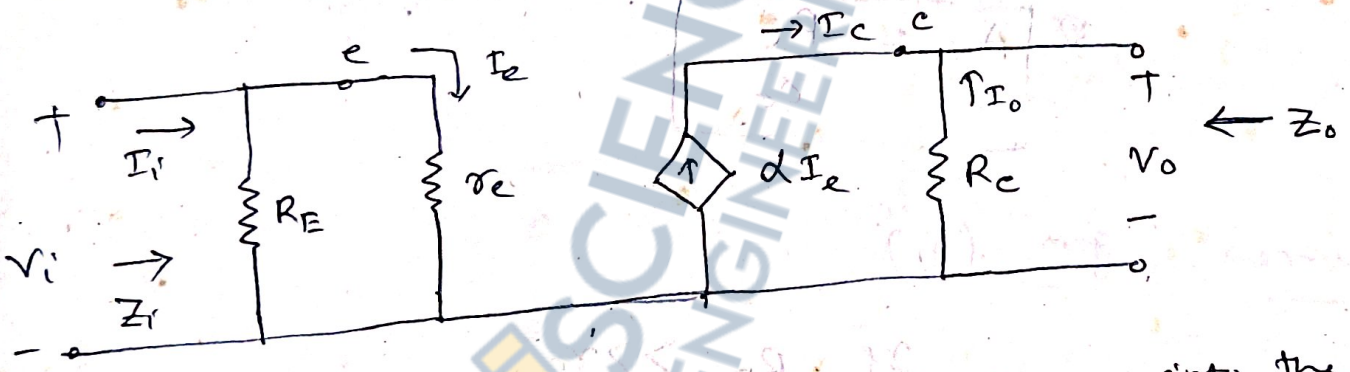


fig:- Common base Configuration



Substituting the r_e equivalent circuit into the ac equivalent network

→ The transistor output impedance r_o is not included for the common-base configuration because it is typically in the mega ohm and can be ignored in parallel with resistor R_c .

$Z_i :-$ $Z_i = R_E // r_e$

$Z_o = R_c$

A_v :- $V_o = -I_o R_c = -(-I_e) R_c$
(Voltage gain)

$V_o = I_e R_c = \alpha I_e R_c$

But

$I_e = \frac{V_i}{r_e}$

$\Rightarrow V_o = \alpha \cdot \frac{V_i}{r_e} \cdot R_c$

$\Rightarrow \frac{V_o}{V_i} = \frac{\alpha R_c}{r_e} \approx \frac{R_c}{r_e}$

($\because \alpha \approx 1$)

$A_v \approx \frac{R_c}{r_e}$

Current gain (A_i)

If $R_E \gg r_e$,

$I_i = I_e$ — (1)

and $I_o = -\alpha I_e$ — (2) ($\because I_o = -I_c$)

$\Rightarrow \frac{I_o}{I_i} = -\alpha \approx -1$

$A_i \approx -1$

-ve sign shows, actual I_o is out of the system not into the system.

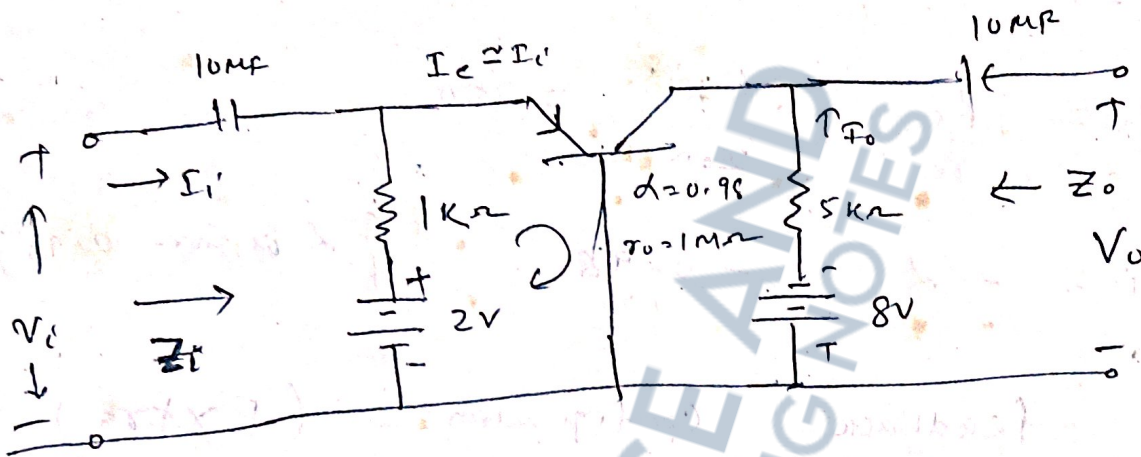
Phase :- The fact that A_v is a +ve number shows that V_o & V_i are in phase for

CB Configuration.

→ Effect of r_o is neglected, because r_o is in mega ohm. $r_o || R_c \approx R_c$.

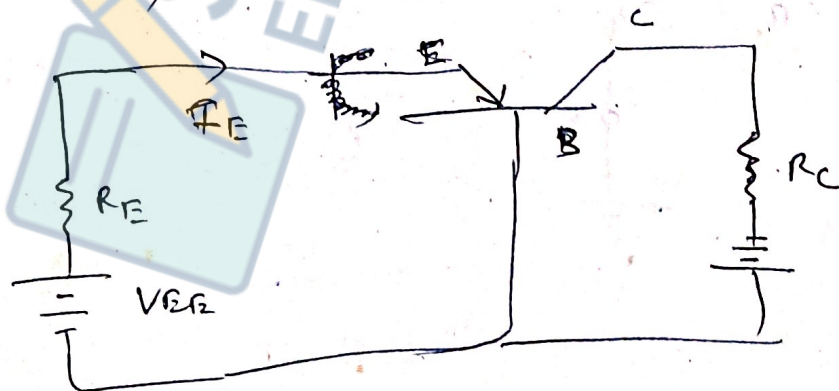
EX: - 8 : Determine

- (a) r_e (b) Z_i (c) Z_o (d) A_v (e) A_i



Ans → To determine r_e

Apply KVL in E/P loop (D.C Analysis)

$$V_{EE} - I_E \cdot R_E - V_{BE} = 0$$


$$\Rightarrow I_E = \frac{V_{EE} - V_{BE}}{R_E} = \frac{2 - 0.7}{1k\Omega} = 1.3 \text{ mA}$$

(a) $r_e = \frac{26 \text{ mV}}{I_E} = \frac{26 \times 10^{-3}}{1.3 \times 10^{-3}} = 20 \Omega$

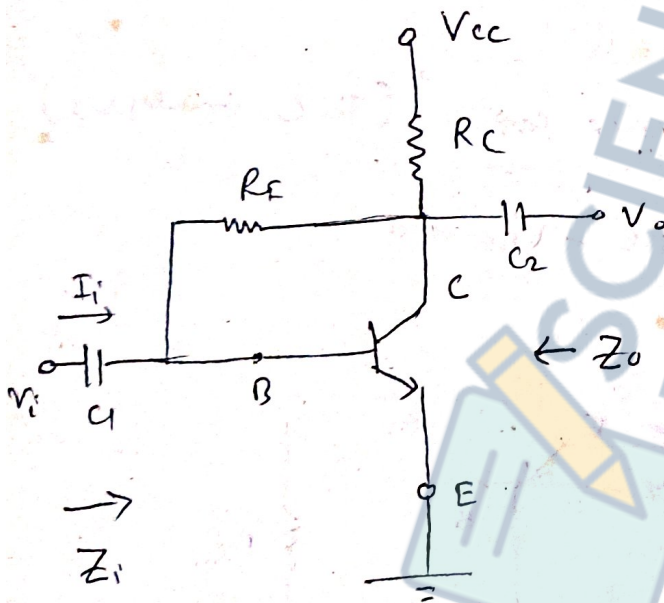
$$\begin{aligned}
 \text{(b)} \quad Z_i &= R_E \parallel r_e \\
 &= 1 \text{ k}\Omega \parallel 20 \Omega \\
 &= 19.61 \Omega \approx r_e
 \end{aligned}$$

$$\text{(c)} \quad Z_o = R_C = 5 \text{ k}\Omega$$

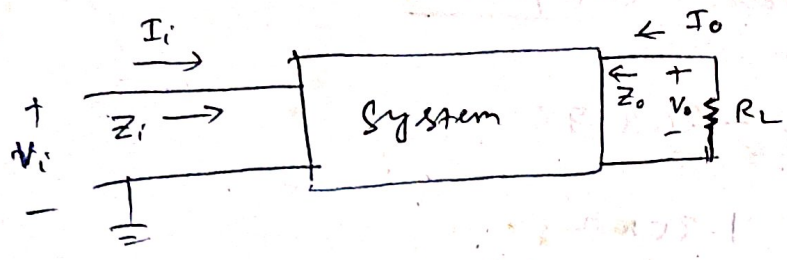
$$\text{(d)} \quad A_v = \frac{R_C}{r_e} = \frac{5 \text{ k}\Omega}{20 \Omega} = 250$$

$$\text{(e)} \quad A_i = -\alpha = -0.98 \quad (\alpha \text{ is given } 0.98)$$

* Collector feedback configuration: - (Extra)



Determining the Current gain :-



Current gain, $A_i = \frac{I_o}{I_i}$

Applying Ohm's law to the I/P & O/P cmts

result in

$$I_i = \frac{V_i}{Z_i}$$

and $I_o = \frac{-V_o}{R_L}$

The minus sign associated with the O/P voltage is simply there to indicate that the polarity of the O/P voltage is determined by an output current having the opposite direction.

$$A_{iL} = \frac{I_o}{I_i} = \frac{-\frac{V_o}{R_L}}{\frac{V_i}{Z_i}} = -\frac{V_o}{R_L} \times \frac{Z_i}{V_i}$$

(Current) gain with load(L)

$$\Rightarrow A_{iL} = -A_{vL} \times \frac{Z_i}{R_L}$$

L → Load

If source resistance is considered (R_s)

$$A_{is} = \frac{I_o}{I_s} = \frac{-\frac{V_o}{R_L}}{\frac{V_s}{R_s + Z_i}} = -\frac{V_o}{R_L} \times \frac{R_s + Z_i}{V_s} = -\frac{V_o}{V_s} \times \frac{R_s + Z_i}{R_L}$$

$$A_{is} = -A_{vs} \times \frac{R_s + Z_i}{R_L}$$

Ex:- In Example -2, page - 177. (of Note)

Voltage divider Configuration

$$A_v = -368.76$$

$$Z_r = 1.35 \text{ k}\Omega$$

$$R_L = 6.8 \text{ k}\Omega$$

$$A_{iL} = - (-368.76) \times \frac{1.35 \text{ k}\Omega}{6.8 \text{ k}\Omega}$$

~~= 368.76~~

$A_{iL} = 73.2$

Effect of R_L & R_s :-

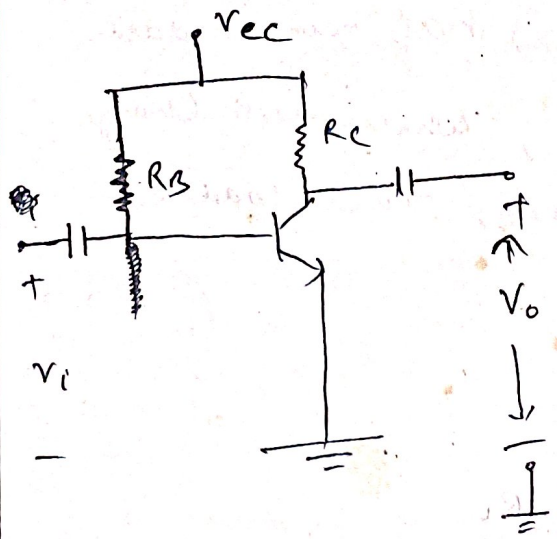
All the parameters determined so far, have been for an unloaded amplifier with i/p voltage connected directly to a terminal of the transistor. Now the effect of applying a load to the output terminal and effect of using a source with an internal resistance will be investigated.

Returning, ~~to~~ ^{to fig(2), (a)} Because a resistive load ~~is~~ ^{is} not attached to the output terminal, the gain ^{is} commonly referred to as the

no-load gain

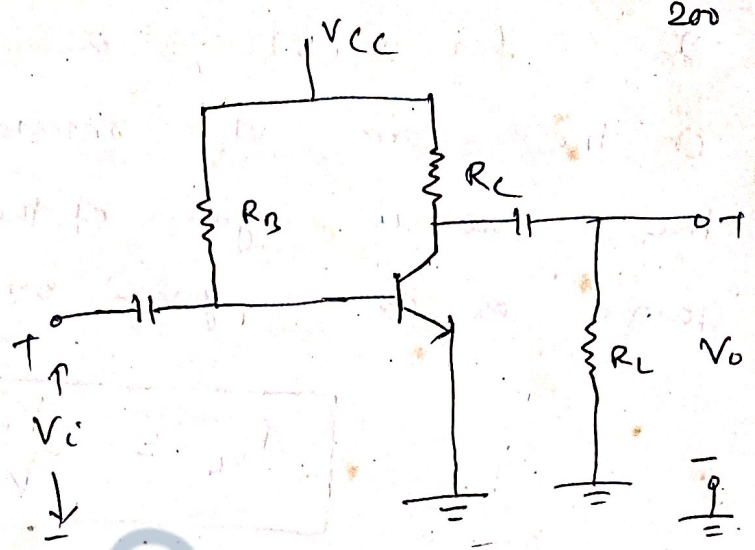
& is given as

$A_{V_{NL}} = \frac{V_o}{V_i}$



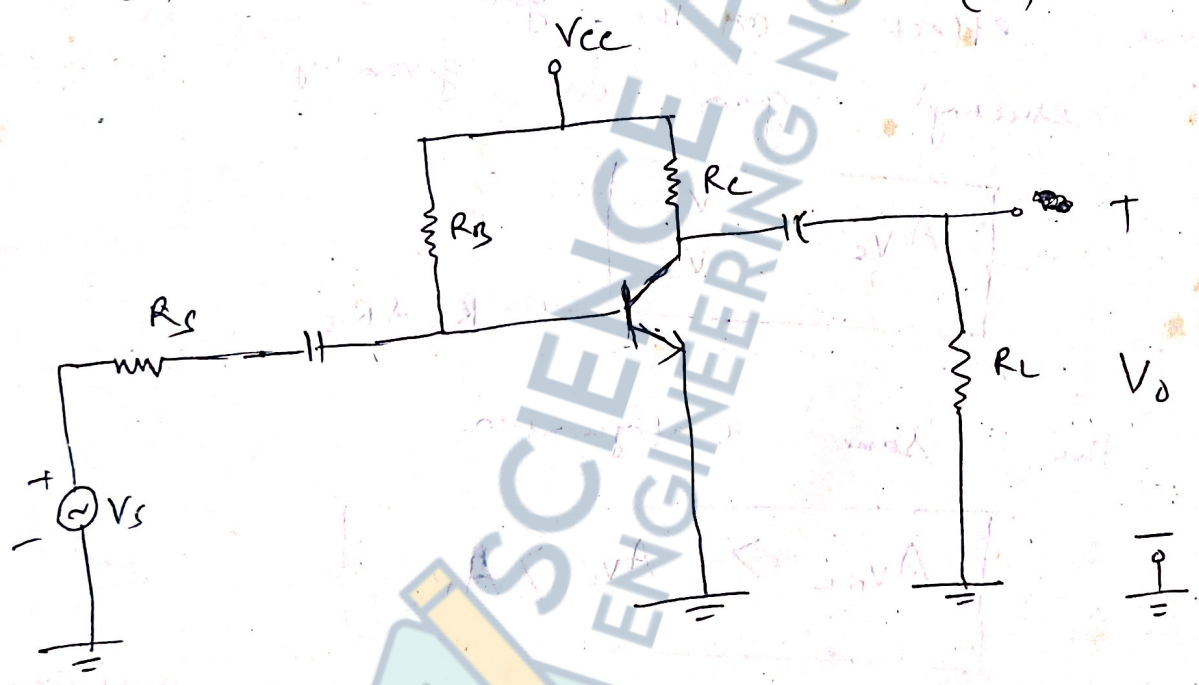
~~A_{vNL}~~ $A_{vNL} = \frac{V_o}{V_i}$

(a)



$A_{vL} = \frac{V_o}{V_i}$

(b)



(c) $A_{v_s} = \frac{V_o}{V_s}$

fig:2 Amplifier Configurations

(a) unloaded (b) loaded
 (c) loaded with a source resistance.

In fig-2, (b), a load has been added in the form of resistor R_L , which will change the overall gain of the system, This loaded gain is given by,

$$A_{V_L} = \frac{V_o}{V_i} \text{ with } R_L$$

In fig-2, (c) both a load & source resistance have been introduced, which will have an additive effect on the gain of the system. The resulting gain is given by

$$A_{V_s} = \frac{V_o}{V_s} \text{ with } R_L \& R_s$$

For the same configuration,

$$A_{V_{NL}} > A_{V_L} > A_{V_s}$$

i.e. The load voltage gain of an amplifier is always less than the no-load gain.

Further, The gain obtained with a source resistance in place will always less than that obtained under loaded or unloaded conditions.

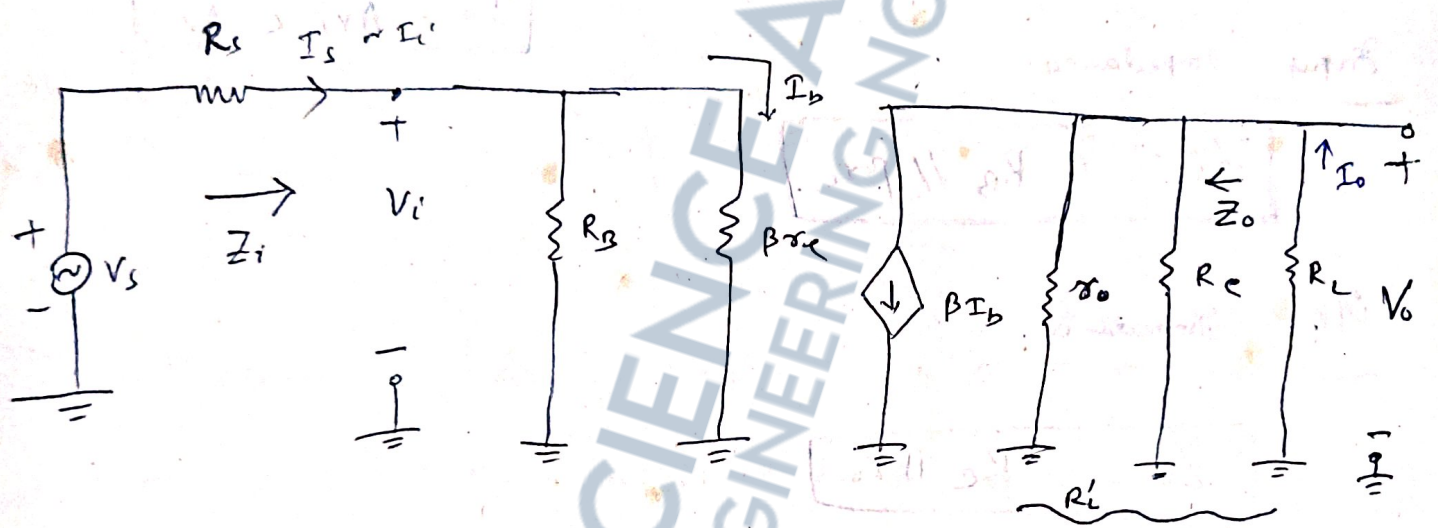
→ Larger the load resistance, greater is the level of ac gain. When R_L is very high, it is approximate to open ckted, i.e. no

load gain, (A_{V_L}) and gain is max^m.

→ Smaller is the source resistance, greater is the overall gain, when R_s is very small, it is equivalent to short circuit, so effect of R_s will be eliminated.

Fixed bias Configuration with R_L & R_s

For the fixed bias transistor amplifier, the ^{or} equivalent circuit for the transistor [Previous page, fig(2)C]



→ The only changes from the a.c equivalent circuit in fixed bias, is R_L is parallel with R_C and R_s is series with V_s .

To find A_{V_L} (Load gain)

$$R'_L = r_o \parallel R_C \parallel R_L \approx R_C \parallel R_L \quad (\because r_o \text{ is very high})$$

$$\text{and } V_o = -I_o R'_L = -(\beta I_B) \cdot R'_L = -(\beta I_B) \cdot (R_C \parallel R_L) \quad \text{--- (1)}$$

$$\text{But } I_B = \frac{V_i}{\beta R_C} \quad \text{--- (2)}$$

Putting eqⁿ ② in eqⁿ ①

$$V_o = -\beta \cdot \left(\frac{V_i}{\beta r_e} \right) \cdot (R_c \parallel R_L)$$

$$\Rightarrow \frac{V_o}{V_i} = - \frac{(R_c \parallel R_L)}{r_e}$$

$$\Rightarrow A_{vL} = - \frac{R_c \parallel R_L}{r_e}$$

When R_i is not considered,
 $A_v = - \frac{R_c}{r_e}$

[So $A_{vL} < A_v$]

Input Impedance

$$Z_i = R_B \parallel \beta r_e$$

Output Impedance

$$Z_o = R_c \parallel r_o$$

A_{vS} (Overall gain from input source V_s to output

voltage V_o)

Using voltage divider rule,

$$V_i = \frac{V_s}{R_s + Z_i} \times Z_i$$

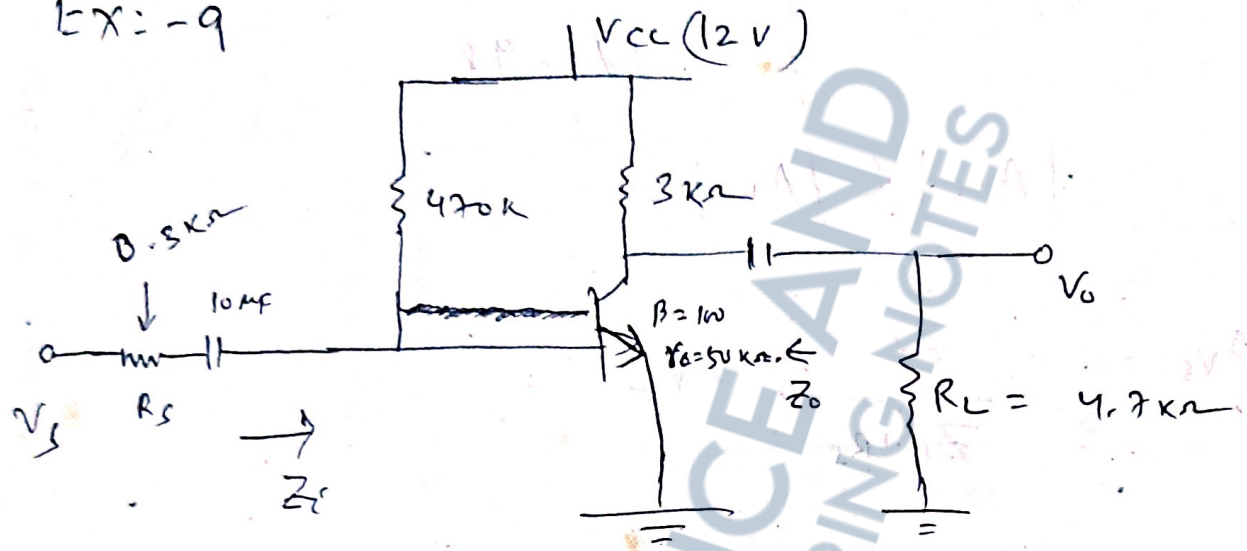
$$\Rightarrow \frac{V_i}{V_s} = \frac{Z_i}{R_s + Z_i}$$

$$A_{vS} = \frac{V_o}{V_s} = \frac{V_o}{V_i} \cdot \frac{V_i}{V_s} = A_{vL} \times \frac{Z_i}{R_s + Z_i}$$

$$A_{V_s} = \frac{Z_i}{Z_i + R_s} \cdot A_{V_L}$$

Since $\frac{Z_i}{Z_i + R_s} < 1$, $A_{V_s} < A_{V_L}$

EX: -9



Determine A_{V_L} , A_{V_s} , Z_i , Z_o , & compare with A_v (no load gain)

Ans: [See the example of Fixed bias A-C analysis]

$$I_B = \frac{V_{CC} - V_{BE}}{R_B} = \frac{12 - 0.7}{470k} = 24.04 \mu A$$

$$I_E = (\beta + 1) I_B = 101 \times 24.04 \times 10^{-6} = 2.428 \text{ mA}$$

$$r_e = \frac{26 \text{ mV}}{I_E} = \frac{26}{2.42} = 10.71 \Omega$$

$$\beta r_e = 101 \times 10.71 = 1.071 k\Omega$$

$$(a) A_v = \frac{-r_o \parallel R_c}{r_e} \approx \frac{-R_c}{r_e} = \frac{-3k\Omega}{10.71} = -280.11$$

$$A_{vL} = \frac{-R_c \parallel R_L}{r_e} = \frac{3k\Omega \parallel 4.7k\Omega}{10.71\Omega} = \frac{-1.831k\Omega}{10.71\Omega}$$

$$= -170.98$$

$$\therefore |A_{vL}| < |A_v|$$

$$(b) A_{v_s} = \frac{Z_i}{Z_i + R_s} \cdot A_{vL}$$

$$Z_i = \beta r_e \parallel R_B$$

$$= 1.071k\Omega \parallel 470k\Omega$$

$$Z_i = 1.07k\Omega$$

$$A_{v_s} = \frac{1.07}{1.07 + 0.3} \times (-170.98)$$

$$A_{v_s} = -133.54$$

$$\therefore |A_{v_s}| < |A_{vL}| < |A_v|$$

$$133.54 < 170.98 < 280.11$$

$$(c) A_{iL} = \frac{I_o}{I_i} = \frac{I_o}{I_b} = -A_{vL} \times \frac{Z_i}{R_L} = -(-170.98) \times \frac{1.07k\Omega}{4.7k\Omega} = 38.92$$

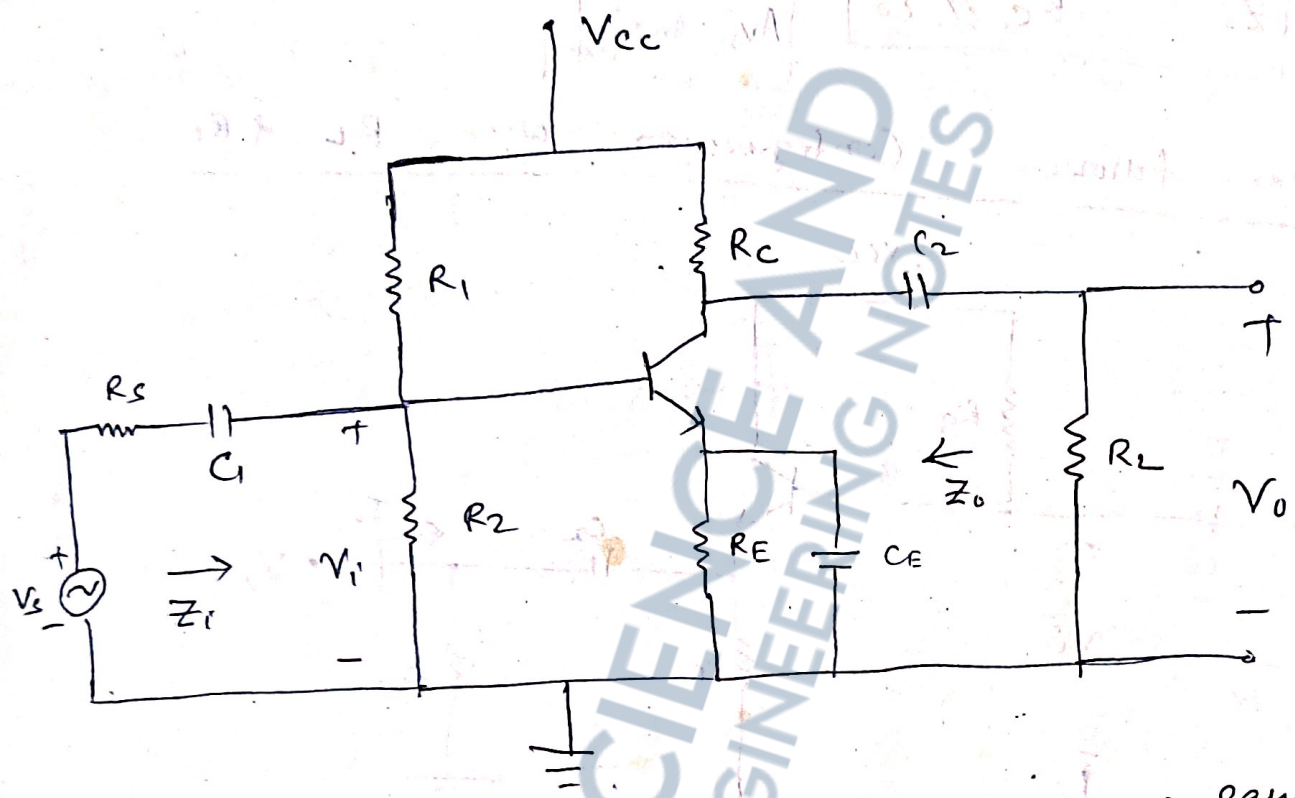
(d) $Z_i = 1.07 \text{ k}\Omega$

(Here $A_{iL} = A_{iS}$ because $I_i = I_s$)

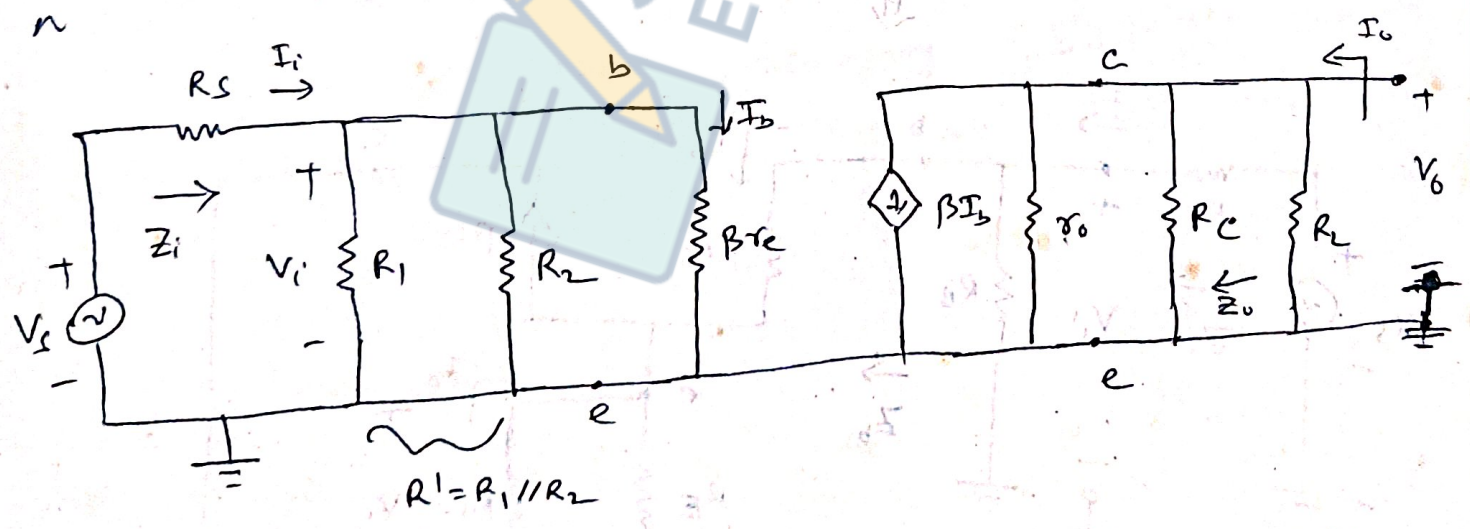
(e) $Z_o = R_c \parallel r_o \approx R_c = 3 \text{ k}\Omega$

(f) $A_{iS} = \frac{I_o}{I_s} = -A_{vS} \times \frac{R_s \parallel Z_i}{R_L} = -(-133.54) \times \left(\frac{0.3 \parallel 1.07}{4.7} \right) = 38.92$

Voltage-divider bias configuration with R_L & R_S



Substituting the r_e equivalent circuit into a.c equivalent of



→ The figure is similar to fixed bias, the only difference is $R_1 \parallel R_2$ instead of R_B .

$$A_{VL} = \frac{V_o}{V_i} = - \frac{R_c \parallel R_L}{r_e}$$

$$Z_i = R_1 \parallel R_2 \parallel \beta r_e$$

$$Z_o = R_c \parallel r_o$$

$$A_{VS} = A_{VL} \times \frac{Z_i}{R_s + Z_i}$$

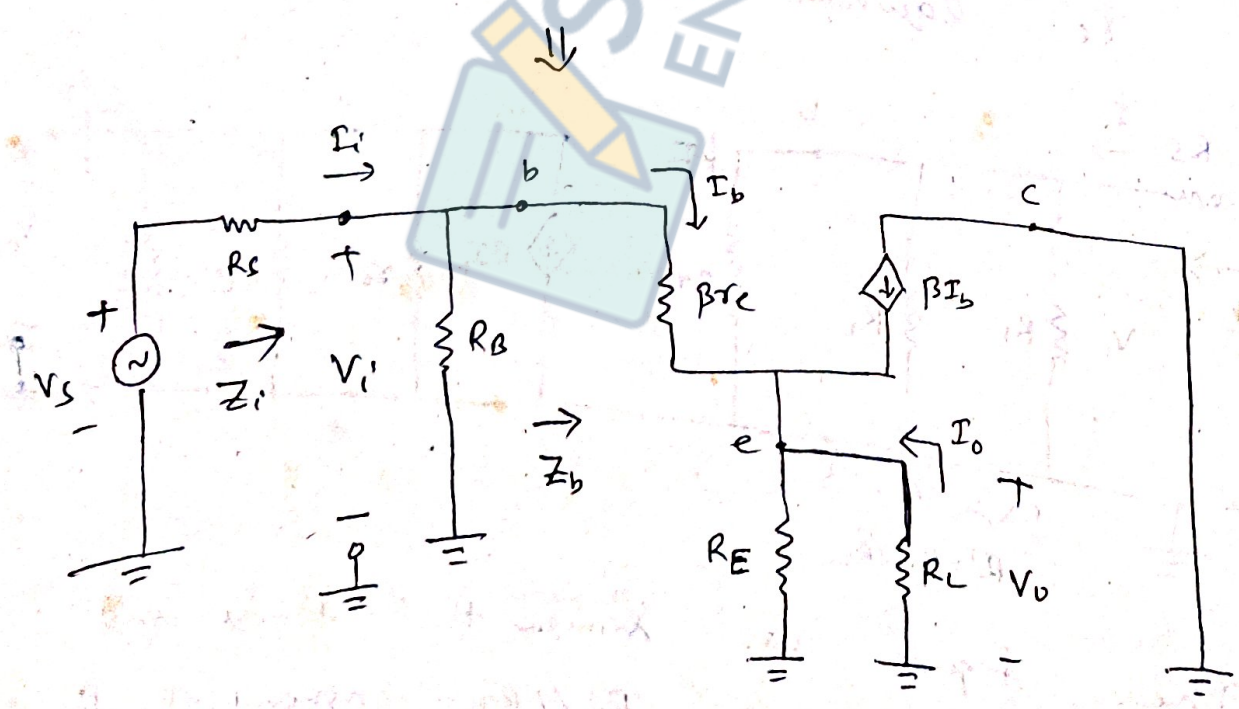
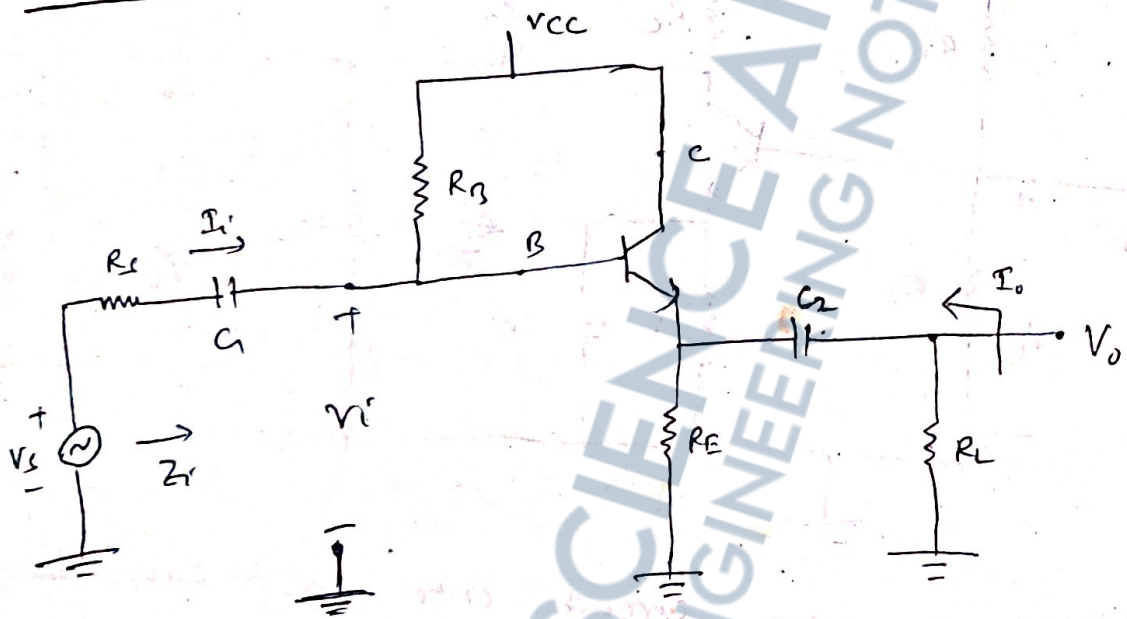
Without Bypass Capacitor [Refer 187 pag. EX-5]

$$A_{VL} = - \frac{R_c \parallel R_L}{R_E}$$

$$Z_i = R_1 \parallel R_2 \parallel \beta(r_e + R_E) \approx R_1 \parallel R_2 \parallel \beta R_E$$

$$Z_o = R_c \parallel r_o \approx R_c$$

Emitter-follower Configuration with R_L & R_s



$$A_{VL} = \frac{V_o}{V_i} = \frac{R_E // R_L}{R_E // R_L + r_e}$$

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$(\because A_v = \frac{R_E}{R_E + r_e})$
 Replace $R_E \rightarrow R_E // R_L$

The difference betⁿ Emitter-follower without R_L
 & with R_L is that, Parallel combination
 of R_E & R_L . whenever R_E was appearing
 in no load case now will be replaced
 by $R_E // R_L$

$$Z_i = R_B // Z_b$$

where $Z_b \approx \beta (R_E // R_L)$

$$Z_o \approx r_e$$

Emitter follower
 In ~~no load case~~
 without R_L ,
 $Z_b \approx \beta R_E$
 without R_L
 $Z_o = R_E // r_e$
 with R_L
 $Z_o = (R_E // R_L) // r_e \approx r_e$

→ The effect of load resistor & a source
 impedance on the remaining BJT Configuration
 is shown in the summary table. (Next page)

TABLE 5.1
Unloaded BJT Transistor Amplifiers

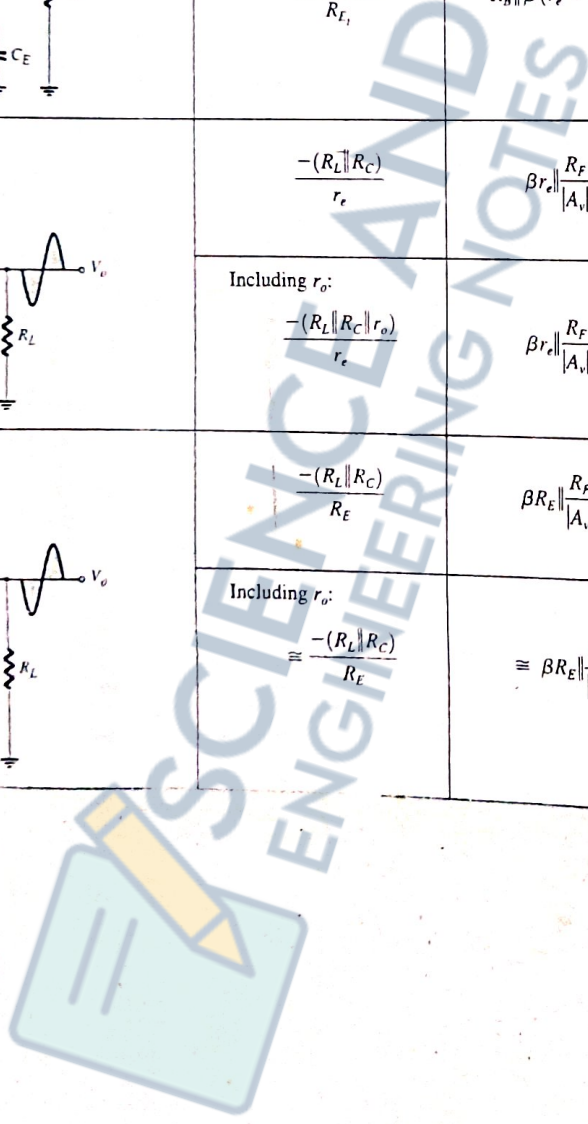
Configuration	Z_i	Z_o	A_v	A_i
Fixed-bias: 	Medium (1 kΩ) $= R_B \parallel \beta r_e$ $\approx \beta r_e$ ($R_B \approx 10\beta r_e$)	Medium (2 kΩ) $= R_C \parallel r_o$ $\approx R_C$ ($r_o \approx 10R_C$)	High (-200) $= \frac{(R_C \parallel r_o)}{r_e}$ $\approx \frac{R_C}{r_e}$ ($r_o \approx 10R_C$)	High (100) $= \frac{\beta R_B r_o}{(r_o + R_C)(R_B + \beta r_e)}$ $\approx \beta$ ($r_o \approx 10R_C$, $R_B \approx 10\beta r_e$)
Voltage-divider: 	Medium (1 kΩ) $= R_1 \parallel R_2 \parallel \beta r_e$	Medium (2 kΩ) $= R_C \parallel r_o$ $\approx R_C$ ($r_o \approx 10R_C$)	High (-200) $= \frac{R_C \parallel r_o}{r_e}$ $\approx \frac{R_C}{r_e}$ ($r_o \approx 10R_C$)	High (50) $= \frac{\beta (R_1 \parallel R_2) r_o}{(r_o + R_C)(R_1 \parallel R_2 + \beta r_e)}$ $\approx \frac{\beta (R_1 \parallel R_2)}{R_1 \parallel R_2 + \beta r_e}$ ($r_o \approx 10R_C$)
Unbypassed emitter-bias: 	High (100 kΩ) $= R_B \parallel Z_b$ $Z_b \approx \beta(r_e + R_E)$ $\approx R_B \parallel \beta R_E$ ($R_E \gg r_e$)	Medium (2 kΩ) $= R_C$ (any level of r_o)	Low (-5) $= \frac{R_C}{r_e + R_E}$ $\approx \frac{R_C}{R_E}$ ($R_E \gg r_e$)	High (50) $\approx \frac{\beta R_B}{\beta r_e + Z_b}$
Emitter-follower: 	High (100 kΩ) $= R_B \parallel Z_b$ $Z_b \approx \beta(r_e + R_E)$ $\approx R_B \parallel \beta R_E$ ($R_E \gg r_e$)	Low (20 Ω) $= R_E \parallel r_e$ $\approx r_e$ ($R_E \gg r_e$)	Low (≈ 1) $= \frac{R_E}{R_E + r_e}$ ≈ 1	High (-50) $\approx \frac{\beta R_B}{R_B + Z_b}$
Common-base: 	Low (20 Ω) $= R_E \parallel r_e$ $\approx r_e$ ($R_E \gg r_e$)	Medium (2 kΩ) $= R_C$	High (200) $\approx \frac{R_C}{r_e}$	Low (-1) ≈ -1
Collector feedback: 	Medium (1 kΩ) $= \frac{r_e}{\frac{1}{\beta} + R_F}$ ($r_o \approx 10R_C$)	Medium (2 kΩ) $\approx R_C \parallel R_F$ ($r_o \approx 10R_C$)	High (-200) $\approx \frac{R_C}{r_e}$ ($r_o \approx 10R_C$, $R_F \gg R_C$)	High (50) $= \frac{\beta R_F}{k_f + \beta R_C}$ $\approx \frac{R_F}{R_C}$

TABLE 5.2
BJT Transistor Amplifiers Including the Effect of R_s and R_L

Configuration	$A_{v_i} = V_o/V_i$	Z_i	Z_o
	$\frac{-(R_L \parallel R_C)}{r_e}$	$R_B \parallel \beta r_e$	R_C
	Including r_o : $\frac{(R_L \parallel R_C \parallel r_o)}{r_e}$	$R_B \parallel \beta r_e$	$R_C \parallel r_o$
	$\frac{-(R_L \parallel R_C)}{r_e}$	$R_1 \parallel R_2 \parallel \beta r_e$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C \parallel r_o)}{r_e}$	$R_1 \parallel R_2 \parallel \beta r_e$	$R_C \parallel r_o$
<p style="text-align: center;">Emitter follower type</p>	≈ 1	$R'_E = R_L \parallel R_E$ $R_1 \parallel R_2 \parallel \beta(r_e + R'_E)$	$R'_E = R_L \parallel R_E$ $R_E \parallel \left(\frac{R'_E}{\beta} + r_e \right)$
	Including r_o : ≈ 1	$R_1 \parallel R_2 \parallel \beta(r_e + R'_E)$	$R_E \parallel \left(\frac{R'_E}{\beta} + r_e \right)$
	$\approx \frac{-(R_L \parallel R_C)}{r_e}$	$R_E \parallel r_e$	R_C
	Including r_o : $\approx \frac{-(R_L \parallel R_C \parallel r_o)}{r_e}$	$R_E \parallel r_e$	$R_C \parallel r_o$
	$\frac{-(R_L \parallel R_C)}{R_E}$	$R_1 \parallel R_2 \parallel \beta(r_e + R_E)$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C)}{R_E}$	$R_1 \parallel R_2 \parallel \beta(r_e + R_E)$	$\approx R_C$

TABLE 5.2 (Continued)
BJT Transistor Amplifiers Including the Effect of R_s and R_i

Configuration	$A_{v_i} = V_o/V_i$	Z_i	Z_o
	$\frac{-(R_L \parallel R_C)}{R_{i_1}}$	$R_B \parallel \beta(r_e + R_{E_1})$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C)}{R_{E_1}}$	$R_B \parallel \beta(r_e + R_E)$	$\cong R_C$
	$\frac{-(R_L \parallel R_C)}{r_e}$	$\beta r_e \parallel \frac{R_F}{ A_{v_i} }$	R_C
	Including r_o : $\frac{-(R_L \parallel R_C \parallel r_o)}{r_e}$	$\beta r_e \parallel \frac{R_F}{ A_{v_i} }$	$R_C \parallel R_F \parallel r_o$
	$\frac{-(R_L \parallel R_C)}{R_E}$	$\beta R_E \parallel \frac{R_F}{ A_{v_i} }$	$\cong R_C \parallel R_F$
	Including r_o : $\cong \frac{-(R_L \parallel R_C)}{R_E}$	$\cong \beta R_E \parallel \frac{R_F}{ A_{v_i} }$	$\cong R_C \parallel R_F$



Cascaded Systems:-

Two-Port System Approach:-

This approach plays an important role in the design of today's systems where the designer works with packaged products rather than individual elements.

In other words, a particular package may house an amplifier with all the components appearing in the no-load version of configuration. Along with that package are the gain, i/p and o/p impedances. $[A_{VNL}, Z_i, Z_o]$



Ex: Two port system.

If we take a 'Thevenin look' at the output terminals, we find, with V_i set to zero,

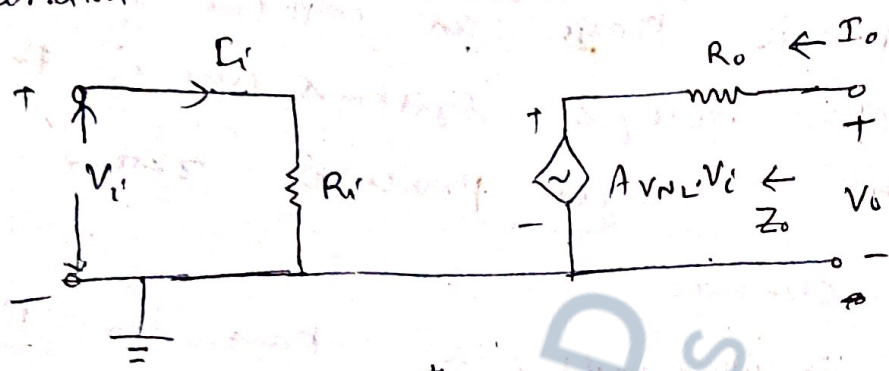
$$Z_{th} = Z_o = R_o$$

E_{th} is the open ckt voltage betⁿ the o/p terminals, identified as ' V_o '.

$$A_{VNL} = \frac{V_o}{V_i} \Rightarrow V_o = A_{VNL} \cdot V_i$$

$$\Rightarrow E_{th} = A_{VNL} \cdot V_i$$

Substituting the Thevenin, equivalent circuit
 betⁿ the O/P terminals results in the O/P
 Configuration



In Thevenin equivalent, voltage source with series resistor R_o .

$$Z_o = R_o$$

$$Z_i = R_i$$

For output circuit, the parameters

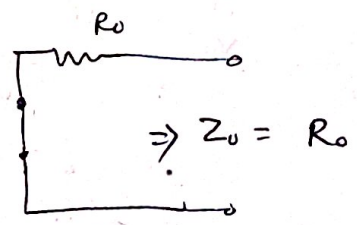
V_i & I_i are related by $Z_i = R_i$,

permitting the use of R_i to represent I/P circuit.

Z_o :- To find Z_o , set $V_i = 0$, &

$$\therefore A_{VNL} V_i = 0, \text{ permitting a}$$

short-circuit for the source



$$\therefore Z_o = R_o$$

Open circuit,

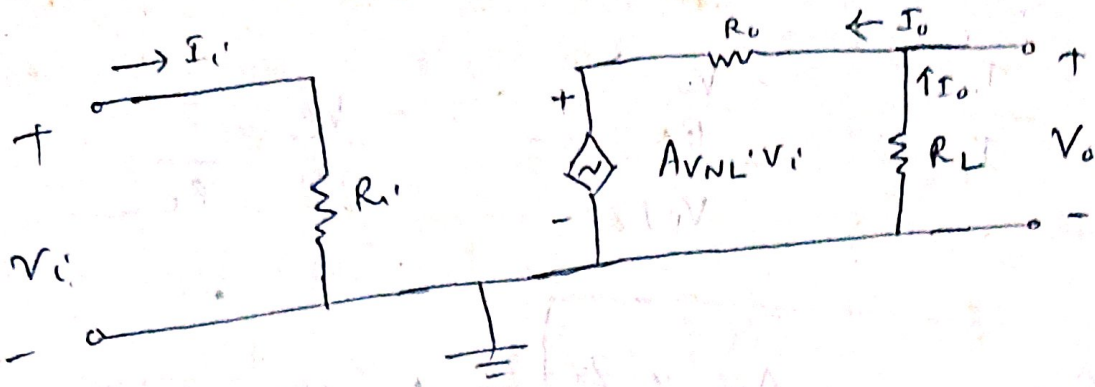
In the absence of load, $I_o = 0$, $I_o R_o = 0$.

$$\therefore \text{open circuit voltage} = A_{VNL} V_i$$

$$V_o = A_{VNL} V_i$$

Applying load to the 2-port system,

shown in fig 2. Applying voltage-divider rule



$$V_o = \frac{A_{VNL} \cdot V_i \times R_L}{R_o + R_L}$$

o/p loop

(∴ Applying KVL, on the

$$-I_o R_o - A_{VNL} \cdot V_i - I_o R_L = 0$$

$$\Rightarrow I_o (R_o + R_L) = -A_{VNL} \cdot V_i$$

$$\Rightarrow I_o = \frac{-A_{VNL} \cdot V_i}{R_o + R_L}$$

$$V_o = + I_o R_L = 0$$

$$\Rightarrow V_o = -I_o R_L = - \left(\frac{-A_{VNL} \cdot V_i}{R_o + R_L} \right) \cdot R_L$$

$$\Rightarrow V_o = \frac{A_{VNL} \cdot V_i \times R_L}{R_o + R_L}$$

$$A_{VL} = \frac{V_o}{V_i} = \frac{A_{VNL} \cdot R_L}{R_o + R_L}$$

$$\therefore \frac{R_L}{R_o + R_L} < 1$$

$$\Rightarrow A_{VL} < A_{VNL}$$

Verify (Fixed Bias)

$$A_{VNL} = -\frac{R_C}{r_c}$$

$$A_{VL} = -\frac{R_C}{r_c} \times \frac{R_L}{R_C + R_L}$$

(∵ $R_C \neq R_C$)

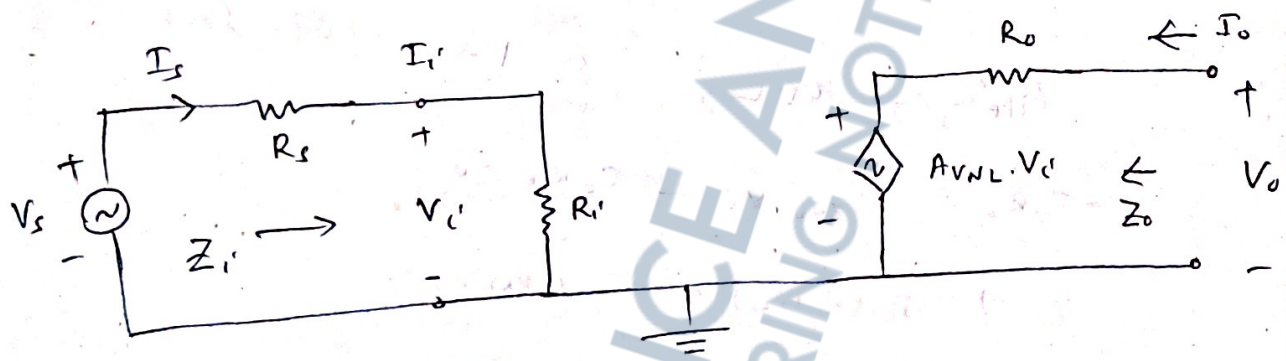
$$\Rightarrow -\frac{R_C}{r_c}$$

∴ As derived earlier in effect of R_i & R_L

$$A_{iL} = \frac{I_o}{I_i} = \frac{-\frac{V_o}{R_L}}{V_i / Z_i} = -\frac{V_o}{V_i} \times \frac{Z_i}{R_L}$$

$$A_{iL} = -A_{vL} \times \frac{Z_i}{R_L} \quad (\text{here } Z_i = R_i)$$

Considering the effect of R_s (No R_L)



$$V_i = \frac{V_s}{R_s + R_i} \times R_i \quad \text{--- (1)}$$

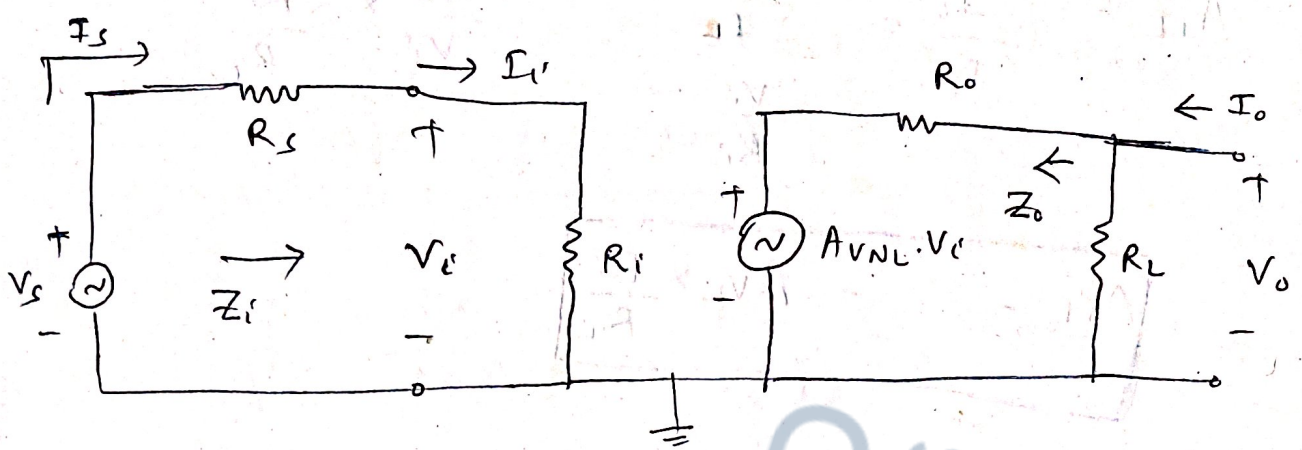
$$V_o = A_{vNL} \cdot V_i$$

$$= A_{vNL} \times \left(\frac{V_s \cdot R_i}{R_s + R_i} \right)$$

$$\Rightarrow \frac{V_o}{V_s} = \frac{R_i}{R_i + R_s} \cdot A_{vNL}$$

$$\Rightarrow A_{v_s} = \frac{R_i}{R_i + R_s} \cdot A_{vNL}$$

If both R_s & R_L present,



The total gain

$$A_{V_s} = \frac{V_o}{V_s} = \frac{V_o}{V_i} \times \frac{V_i}{V_s}$$

$$= A_{V_L} \cdot \frac{R_i}{R_i + R_s}$$

$$\Rightarrow \frac{V_i}{V_s} = \frac{R_i}{R_i + R_s}$$

$$\Rightarrow A_{V_s} = \frac{R_L}{R_o + R_L} \cdot A_{VNL} \cdot \frac{R_i}{R_i + R_s}$$

$$\left(\because A_{V_L} = \frac{V_o}{V_i} = \frac{A_{VNL} \cdot R_L}{R_o + R_L} \right)$$

Final

$$A_{V_s} = \frac{R_i}{R_i + R_s} \cdot \frac{R_L}{R_L + R_o} \cdot A_{VNL}$$

$$A_{iL} = \frac{I_o}{I_i} = \frac{-V_o}{\frac{V_i}{R_i}} = -\frac{V_o}{V_i} \times \frac{R_i}{R_L}$$

$$A_{iL} = -A_{vL} \times \frac{R_i}{R_L}$$

$$A_{iS} = \frac{I_o}{I_s} = \frac{-V_o}{\frac{V_s}{R_s + R_i}} = -\frac{V_o}{V_s} \times \frac{R_s + R_i}{R_L}$$

$$A_{iS} = -A_{vS} \times \frac{R_s + R_i}{R_L}$$

Since $I_i = I_s$ (Same current see the fig)

$$\Rightarrow A_{iL} = A_{iS} = \frac{I_o}{I_i} = \frac{I_o}{I_s}$$

$$I_i = \frac{V_i}{R_i} \quad \text{--- (2)}$$

$$I_s = \frac{V_s}{R_s + R_i} \quad \text{--- (3)}$$

From eq (3), page 215

$$\frac{V_i}{R_i} = \frac{V_s}{R_s + R_i} \Rightarrow \dots$$

EX :- 10 Determine A_{vL} & A_{vS} using 2-port

n/w method. (EX - 9 - Page 205, this note)

Given $A_{vNL} = -280 \parallel$, $Z_T = 1.07 \text{ k}\Omega$

$Z_o = 3 \text{ k}\Omega$, $R_L = 4.7 \text{ k}\Omega$, $R_s = 0.3 \text{ k}\Omega$

(a) $A_{vL} = \frac{R_L}{R_L + Z_o} \times A_{vNL}$

$$\Rightarrow A_{VL} = \frac{4.7}{4.7 + 3} \times (-280.11) \quad (\because R_o = z_o = 3 \text{ k}\Omega)$$

$$A_{VL} = -170.98$$

$$(b) A_{VS} = \frac{R_i}{R_i + R_s} \times \frac{R_L}{R_L + R_o} \times A_{VNL} \quad \left| \begin{array}{l} R_i = z_i \end{array} \right.$$

$$= \frac{1.07}{1.07 + 0.3} \times \frac{4.7}{4.7 + 3} \times (-280.11)$$

$$A_{VS} = -133.45$$

Ex - 11) Given the packaged (no-entry-possible) Amplifier to Edit

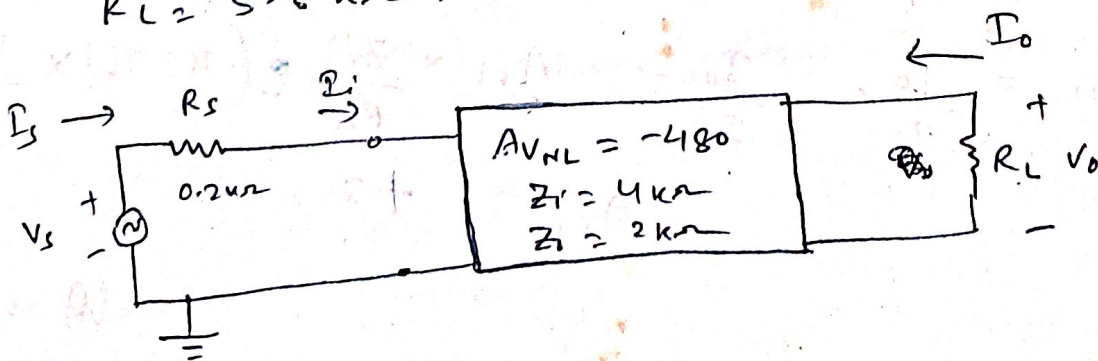
(a) Determine the gain A_{VL} and compare it to the no-load value with $R_L = 1.2 \text{ k}\Omega$

(b) Repeat part (a) with $R_L = 5.6 \text{ k}\Omega$ and compare solutions.

(c) ~~Find~~ Determine A_{VS} with $R_L = 1.2 \text{ k}\Omega$

(d) Find the current gain $A_i = \frac{I_o}{I_s} = \frac{I_o}{I_s}$ with

$R_L = 5.6 \text{ k}\Omega$.



(a)
$$A_{VL} = \frac{R_L}{R_L + R_o} A_{VNL}$$

$$= \frac{1.2}{1.2 + 2} \times (-480) \quad (\because R_o = Z_o = 2k\Omega)$$

$$= -180$$

Which is a dramatic drop from the no-load value.

(b) $R_L = 5.6k\Omega$

$$A_{VL} = \frac{5.6}{5.6 + 2} \times (-480)$$

$$= -353.76$$

Which clearly reveals that the larger the load resistance, better is the gain.

(c)
$$A_{VS} = \frac{R_i}{R_i + R_s} \times \frac{R_L}{R_L + R_o} \cdot A_{VNL}$$

$$= \frac{4}{4 + 0.2} \times \frac{1.2}{1.2 + 2} \times (-480)$$

$$= (0.952) (0.375) \times (-480)$$

$$= -171.36$$

$$R_i = Z_i = 4k\Omega$$

(d)
$$A_{iL} = \frac{I_o}{I_i} = \frac{I_o}{I_s} = -A_{VL} \times \frac{Z_i}{R_L} = -(-353.76) \times \frac{4}{5.6}$$

$$A_{iL} = +252.6$$

(Ans.)

Cascaded System :

Two - post systems approach is particularly useful for Cascaded system, shown in figure 1, where A_{v1}, A_{v2}, A_{v3} & so on, are the voltage gains of each stage under loaded condⁿ. Total gain of the system is determined by the product of individual gains

i.e.

$$A_{VT} = A_{v1} \cdot A_{v2} \cdot A_{v3} \cdot \dots$$

and total current gain is given by,

$$A_{iT} = - A_{VT} \cdot \frac{Z_{i1}}{R_L}$$

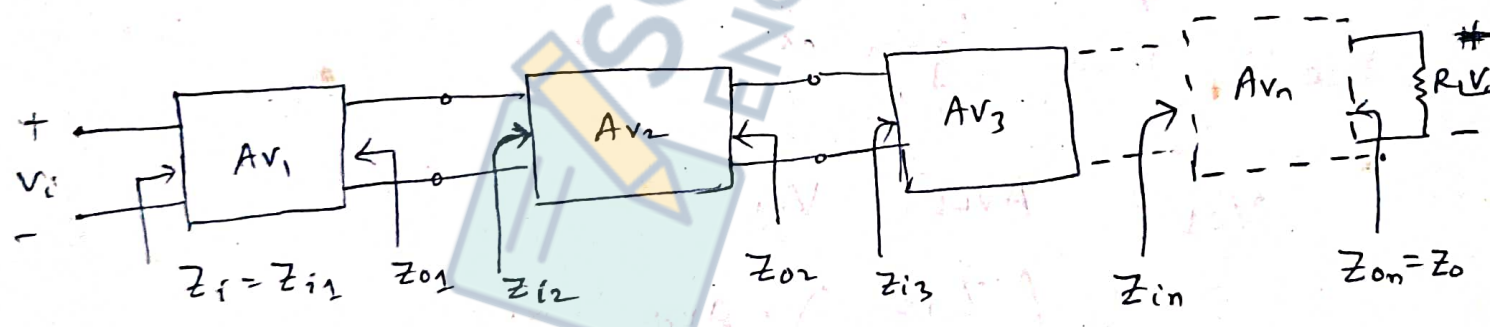
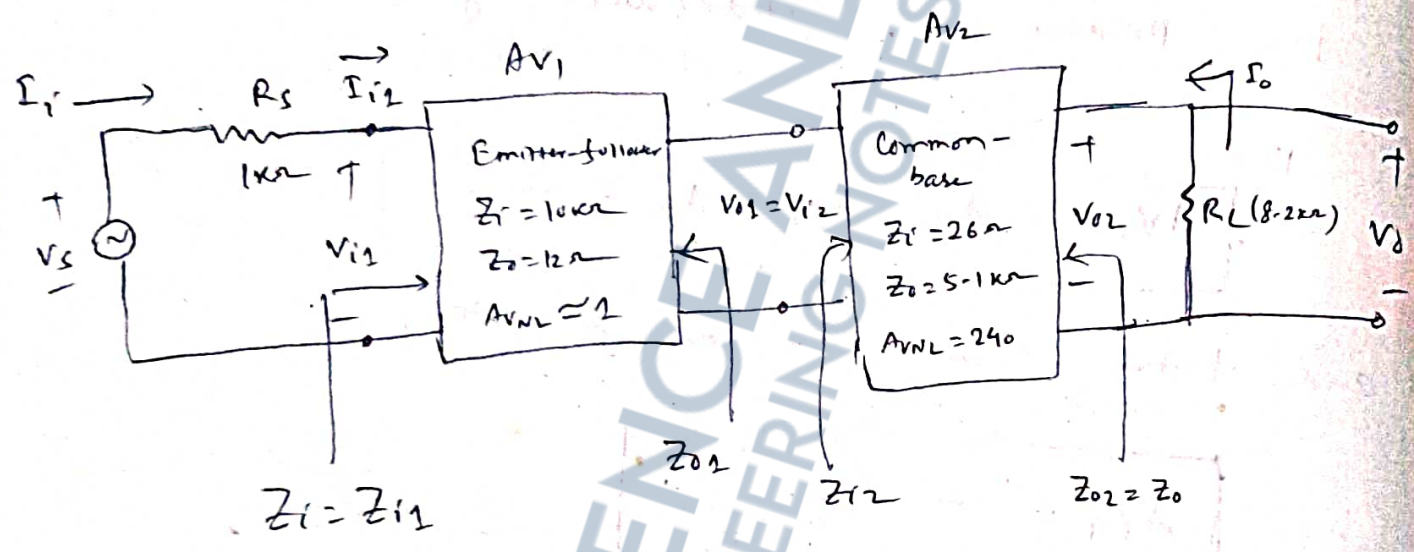


Fig: - Cascaded System.

Ex: 12. The two-stage system, shown, employs a transistor emitter-follower configuration prior to a common base configuration to ensure that the maximum percentage of applied signal appears at the input terminal of common base.

Amplifier. Determine

- (a) The loaded gain for each stage
- (b) The total gain for the system, A_{V_T} & A_{V_S}
- (c) The total current gain for the system.
- (d) The total gain for the system if the emitter-follower configuration were removed.



Ans: (a) For the emitter-follower configuration, the load gain is

$$V_{o1} = A_{VNL} \cdot V_{i1}$$

$$= \left(\frac{Z_{i2}}{Z_{i2} + Z_{o1}} \right) \cdot A_{VNL} \cdot V_{i1}$$

$$\therefore A_{VL} = \left(\frac{R_L}{R_L + R_o} \cdot A_{VNL} \right)$$

$$V_{o1} = \frac{26}{26 + 12} \cdot (1) \cdot V_{i1}$$

$$\Rightarrow \frac{V_{o1}}{V_{i2}} = \frac{26}{38} = 0.684$$

For Common-base Configuration,

$$V_{o2} = A_{V_{L2}} \cdot V_{i2}$$

$$= \left[\frac{R_L \cdot A_{V_{NL}}}{R_L + R_{o2}} \right] \cdot V_{i2}$$

$$V_{o2} = \frac{8.2}{8.2 + 5.1} \times 240 \times V_{i2}$$

$$\Rightarrow A_{V2} = \frac{V_{o2}}{V_{i2}} = \frac{8.2 \times 240}{8.2 + 5.1} = 147.97$$

(b)

Total gain

$$A_{VT} = A_{V1} \times A_{V2}$$

$$= (0.684) \cdot (147.97)$$

$$A_{VT} = 101.20$$

$$A_{VS} = \frac{Z_{i1}}{Z_{i1} + R_S} \cdot A_{VT}$$

$$= \frac{10 \times 101.20}{10 + 1}$$

$$A_{VS} = 92$$

$$\rightarrow A_{VS} = -A_{VS} \times \frac{Z_{i1} R_S}{R_L}$$

$$= -92 \times \frac{10 + 1}{8.2}$$

$$= -123.41$$

(c) Total current gain, $A_{i7} = -A_{VS} \cdot \frac{Z_{i1}}{R_L}$

$$= -(101.20) \cdot \frac{10}{8.2}$$

$$A_{i7} = -123.41$$

(d) If emitter-follower configuration is removed,

$$V_i = \frac{Z_{in}}{Z_{in} + R_s} \cdot V_s = \frac{26}{26 + 1k} \times V_s$$

CB = Common-Base

$$\Rightarrow \frac{V_i}{V_s} = 0.025$$

OR

$$A_{vs} = \frac{R_L}{R_L + R_o} \times \frac{Z_i}{Z_i + R_s} \times A_{vNL}$$

$$= \frac{8.2}{8.2 + 1} \times \frac{0.026}{0.026 + 1} \times 240$$

$$= 3.74$$

$$A_{v2} = 147.97 = \frac{V_o}{V_i}$$

$$A_{vs} = \frac{V_o}{V_s} = \frac{V_o}{V_i} \cdot \frac{V_i}{V_s} = 147.97 \times 0.025 = 3.74$$

∴ A_{vs} with emitter follower = 97
 A_{vs} without emitter follower = 3.74

} gain 25 times greater.

R-C Coupled BJT amplifier

The name is derived from the Capacitive Coupling capacitor C_c and the fact that the load on the first stage is an RC combination. The coupling capacitor isolates the two stages from a d.c viewpoint but acts as a short-circuit equivalent for ac response. The i/p impedance of the second stage act as a load on the first stage.

Ex-13

+20v

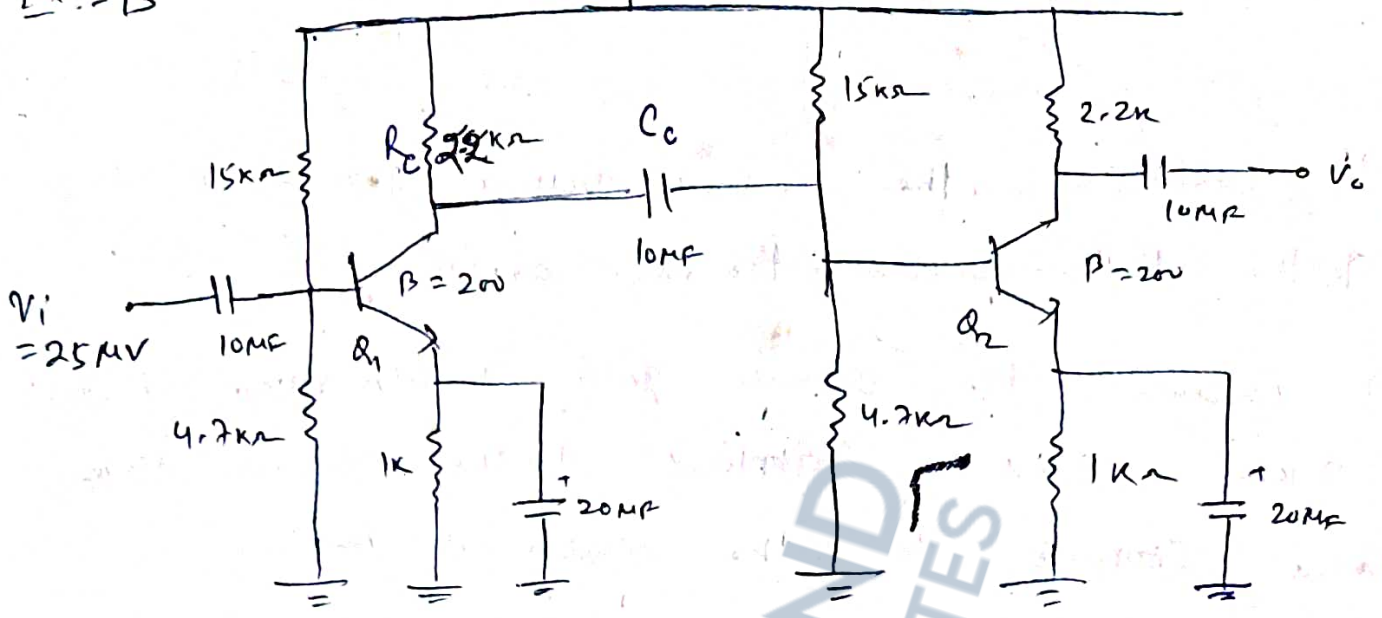


Fig:- RC-Coupled BJT amplifier example.

Q17: D.C bias analysis for each transistor
(Both ^{transistor} configuration are ^{exactly} similar)

Since $\beta R_E > 10 R_B$
 $200 \times 1k > 10 \times 4.7k$
 $200k > 47k$

$$V_B = \frac{20 \times 4.7}{15 + 4.7}$$

$$V_B = 4.7$$

$$V_{BE} = V_B - V_E \Rightarrow 0.7 = 4.7 - V_E \Rightarrow V_E = 4V$$

$$I_{ERE} = 4V$$

$$\Rightarrow I_E = \frac{4}{1k} = 4mA \Rightarrow I_E = 4mA$$

$$V_C = V_{CC} - I_C R_C = 20 - 4 \times 2.2 = 11.2 \text{ volt}$$

$$r_e = \frac{26 \text{ mV}}{I_E} = \frac{26}{4} = 6.5 \Omega$$

- (a) Calculate the no-load voltage gain & o/p voltage of the R-C Coupled transistor amplifier
- (b) Calculate the overall gain & o/p voltage if a $4.7 \text{ k}\Omega$ load is applied to the second stage and compare to the results of part (a)
- (c) Calculate the i/p impedance of the first stage and o/p impedance of the second stage.
- (d) The loading of the second stage is

$$Z_{i2} = R_1 \parallel R_2 \parallel \beta r_e$$

$$= (15 \parallel 4.7 \parallel 1.3) \text{ k}\Omega \quad \left(\begin{array}{l} \beta r_e \\ = 200 \times 6.5 \end{array} \right)$$

Gain of the first stage = $-\frac{R_c \parallel R_L}{r_e}$

$$= -\frac{R_c \parallel Z_{i2}}{r_e}$$

$$= -\frac{2.2 \parallel (15 \parallel 4.7 \parallel 1.3) \text{ k}\Omega}{6.5}$$

$$= -\frac{665.2 \Omega}{6.5}$$

$$A_{V1} = -102.3$$

For the unloaded second stage, the gain is

$$A_{V2(NL)} = \frac{-R_C}{r_e} = \frac{-2.2 \text{ k}\Omega}{6.5} = -338.46$$

$$\begin{aligned} \text{Overall gain} &= A_{V1} \times A_{V2(NL)} \\ &= -102.3 \times -338.46 \\ A_{VT} &= 34.6 \times 10^3 \end{aligned}$$

The o/p voltage is then,

$$\begin{aligned} V_o &= A_{VT} \times V_i \\ &= 34.6 \times 10^3 \times 25 \text{ mV} \end{aligned}$$

$$V_o = 865 \text{ mV}$$

(b) The overall gain with 4.7 k Ω load,

$$A_{VT} = \frac{R_L}{R_L + R_o} \times A_{VNL} = \frac{4.7}{4.7 + 2.2} \times 34.6 \times 10^3$$

$$A_{VT} = 23.6 \times 10^3$$

$$\begin{aligned} V_o &= A_{VT} \times V_i \\ &= 23.6 \times 10^3 \times 25 \times 10^{-6} \end{aligned}$$

$$V_o = 590 \text{ mV}$$

$$\begin{aligned} (c) \quad Z_{i1} &= R_1 \parallel R_2 \parallel \beta r_e = (15 \parallel 4.7 \parallel 1.3) \text{ k}\Omega \\ &= 953.6 \Omega \end{aligned}$$

$$Z_{o2} = R_C = 2.2 \text{ k}\Omega$$

Darlington Connection :-

A very popular connection of two bipolar junction transistor for operation as one 'Super-beta' transistor is the Darlington Connection.

Shown below.

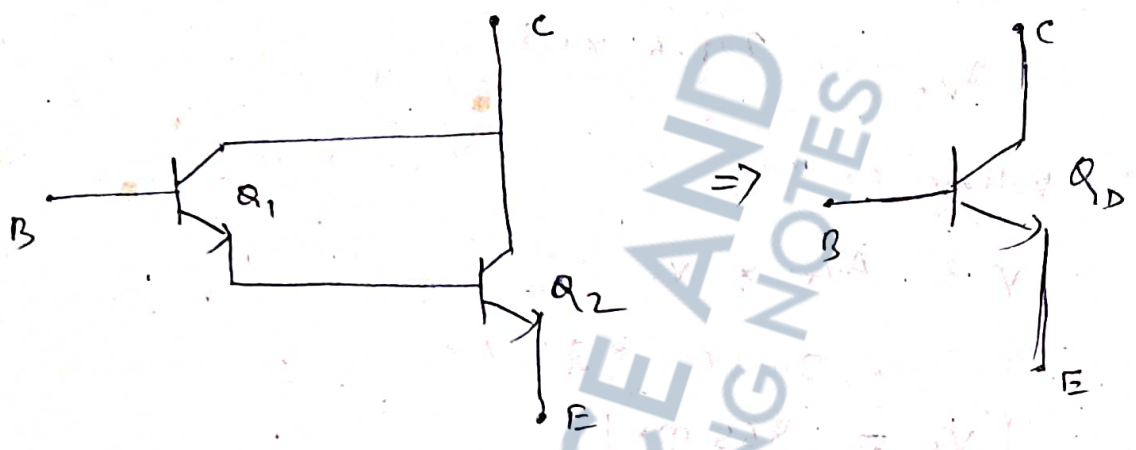


Fig:- Darlington Combination

The main feature of Darlington Connection is that the composite transistor acts as a single unit with a current gain that is the product of the current gains of the individual transistors.

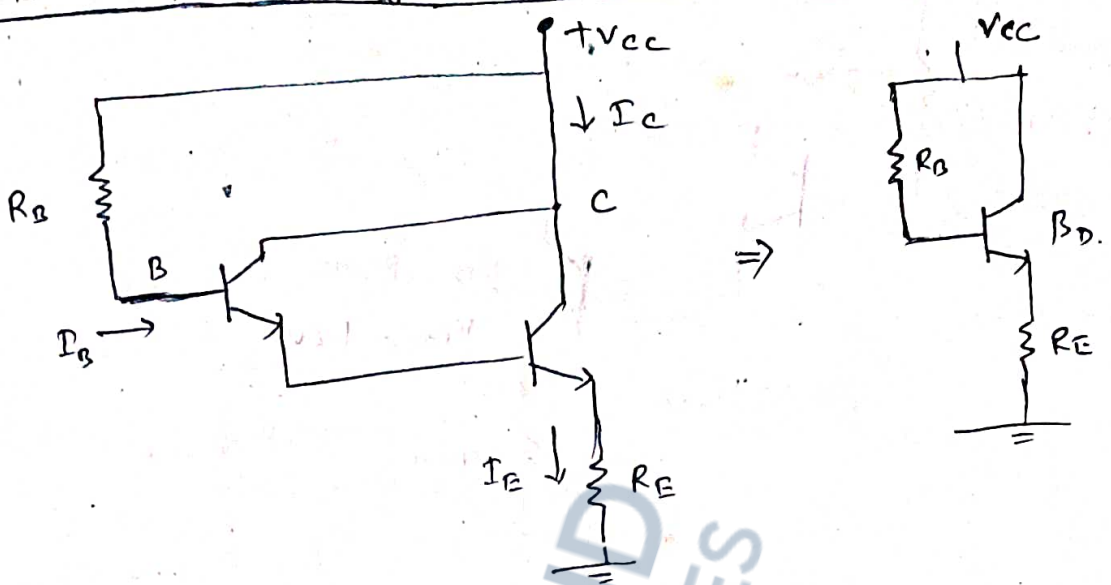
If the connection is made using 2 separate transistors having current gains of β_1 & β_2 , the Darlington Connection provides a current gain of

$$\beta_D = \beta_1 \cdot \beta_2$$

If the two transistors are matched, so that $\beta_1 = \beta_2 = \beta$, the Darlington Connection provides a current gain of

$$\beta_D = \beta^2$$

D.C Bias of Darlington Circuit:-



A Darlington transistor having very high current gain β_D is used.

Applying KVL,

$$V_{CC} - I_B R_B - V_{BE} - \beta_D I_B R_E = 0$$

$$\Rightarrow I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta_D R_E} \quad \text{--- (1)}$$

Here β_D & V_{BE} are much greater.

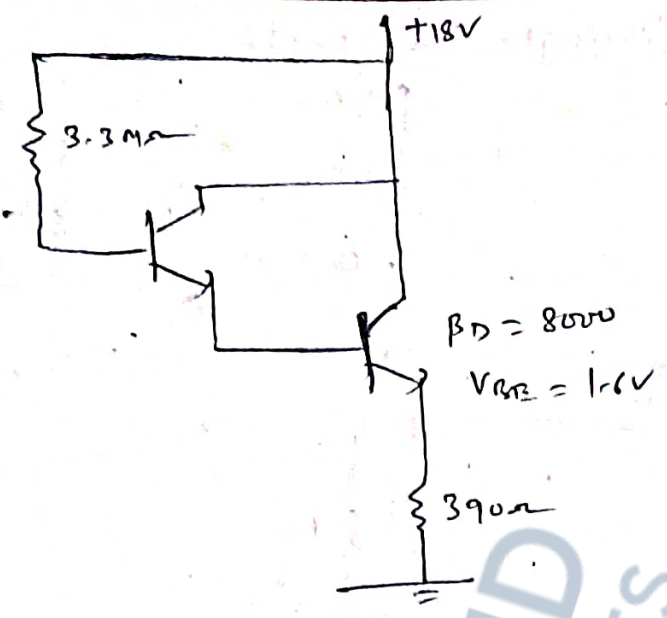
($\beta_D = \beta_1 \cdot \beta_2$, $V_{BE} = 1.6V$)

$$I_E \approx (\beta_D + 1) I_B \approx \beta_D I_B \quad \text{--- (2)}$$

$$V_E = I_E R_E \quad \text{--- (3)}$$

$$V_B = V_E + V_{BE} \quad \text{--- (4)}$$

Ex-14 :- Calculate the bias voltages & current shown in figure below.



Ans: -

$$I_B = \frac{V_{CC} - V_{BE}}{R_B + \beta D R_E} = \frac{18 - 1.6}{3.3 \times 10^6 + 8000 \times 390}$$

$$= \frac{16.4}{6.42 \times 10^6}$$

$$I_B = 2.55 \mu A$$

$$I_E \approx \beta D I_B = 8000 \times 2.55 \times 10^{-6} = 20.4 \text{ mA}$$

$$V_E = I_E R_E = 20.4 \times 10^{-3} \times 390 \times 10^{-3} = 7.96 \text{ Volt}$$

$$V_B = V_E + V_{BE} = 7.96 + 1.6 = 9.56 \text{ Volt}$$

$$V_C = \text{Supply Voltage} = 18V$$

AC Equivalent ckt: -

A ~~Diagram~~ diagram of emitter-follower ckt is shown. The ac input signal is applied to the

base of Darlington transistor through capacitor C_1 , with the ac o/p V_o obtained from the emitter through capacitor C_2 . Due to the absence of a load R_L , the o/p current I_o is defined through R_E .

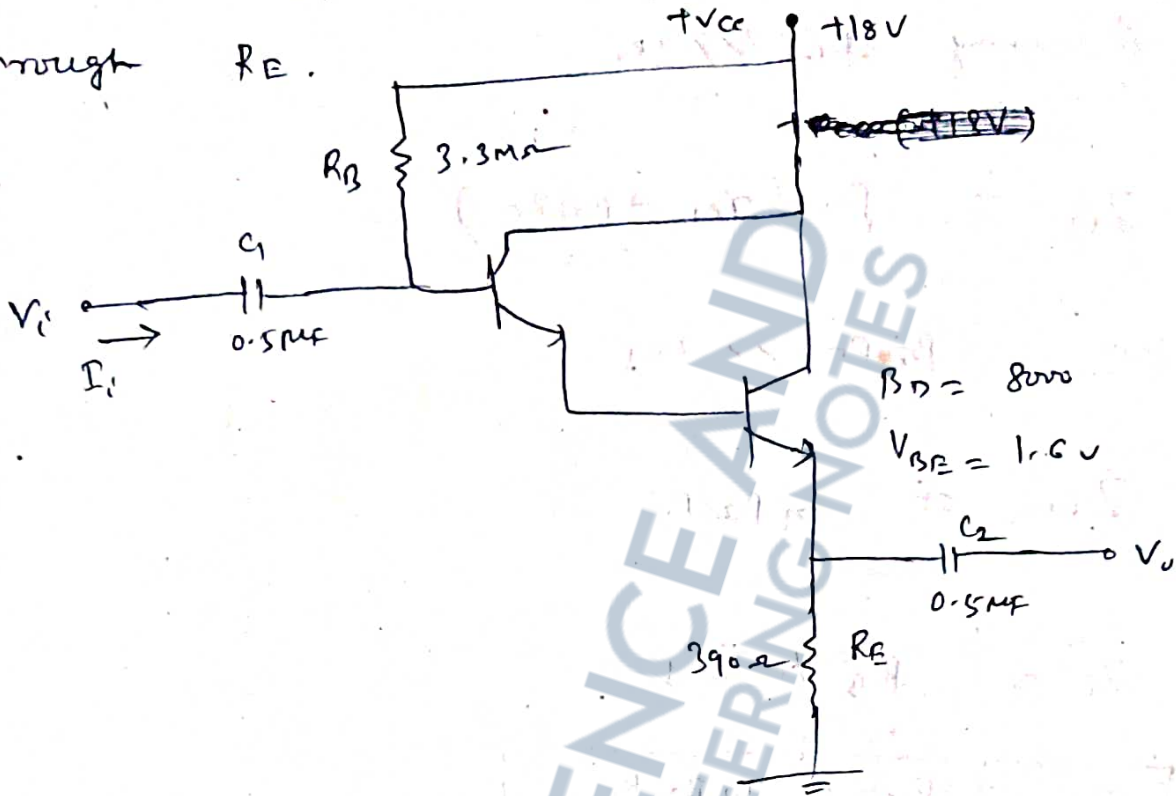
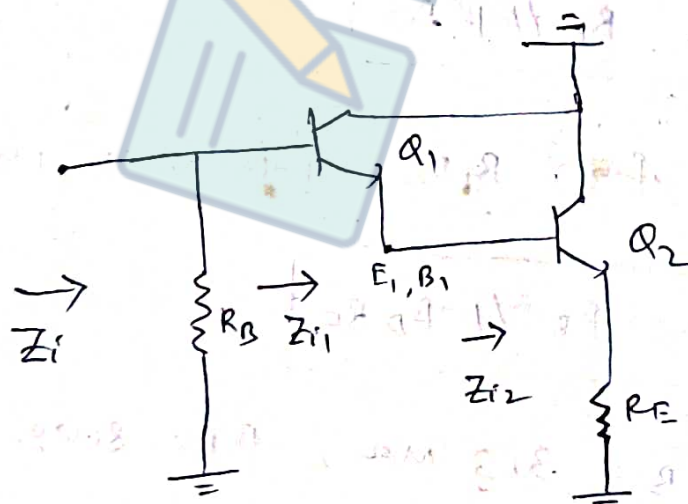


Fig: - Darlington emitter-follower circuit.

Input Impedance



$$Z_{i2} = \beta_2 (\gamma_{e2} + R_E) \quad \text{--- (1)}$$

$$Z_{i1} = \beta_1 (\gamma_{e1} + Z_{i2}) \quad \text{--- (2)}$$

[\because emitter-follower $Z_i = \beta(\gamma_e + R_E)$]

∴ Putting eqn (1), in eqn (2), we have

$$Z_{i1} = \beta_1 (\gamma_{e1} + \beta_2 (\gamma_{e2} + R_E))$$

Assuming $R_E \gg \gamma_{e2}$

$$Z_{i1} = \beta_1 (\gamma_{e1} + \beta_2 R_E)$$

Again $\beta_2 R_E \gg \gamma_{e1}$

$$Z_{i1} \approx \beta_1 \beta_2 R_E$$

AND $Z_i = R_B \parallel Z_{i1}$

$$Z_i = R_B \parallel \beta_1 \beta_2 R_E$$

For $\beta_1 = \beta_2 = \beta$,

$$Z_i = R_B \parallel \beta^2 R_E$$

or $\beta_D = \beta_1 \beta_2$ [from specification sheet]

$$Z_i = R_B \parallel \beta_D R_E$$

Ex: If $R_B = 3.3 \text{ M}\Omega$, $\beta_D = 8000$, $R_E = 390 \Omega$

$$Z_i = 3.3 \text{ M}\Omega \parallel (8000 \times 390)$$

$$= 3.3 \text{ M}\Omega \parallel 3.12 \text{ M}\Omega = 1.6 \text{ M}\Omega$$

(Ans)

* Current gain

$$A_i = \frac{I_o}{I_i} \approx \frac{\beta_1 \beta_2 R_B}{R_B + \beta_1 \beta_2 R_E}$$

$$A_c = \frac{\beta_D \cdot R_B}{R_B + \beta_D \cdot R_E}$$

* Voltage gain :-

$$A_v = \left[\frac{\beta_D \cdot R_B}{R_B + \beta_D \cdot R_E} \right] \cdot \left[\frac{R_E}{R_B + \beta_D R_E} \right]$$

$$A_v \approx 1 \quad (\text{in reality less than 1})$$

* Output Impedance

$$Z_o = \frac{r_{e1}}{\beta_2} + r_{e2}$$

* → Extra (Not required) -

→ Derivation in Boylestad.

Current Mirror Circuits:-

A current mirror circuit provides a constant current and used primarily in integrated ccts. (I_C)

The constant current is obtained from an OP current, which is the reflection or mirror of a constant current developed on one side of the ckt.

The ckt is particularly suited to IC manufacture because the ckt requires that the transistors used have - identical base-emitter voltage (V_{BE}) drops and identical values of β . Results best achieved when transistors are formed at the same time in IC manufacture.

In fig 1, the current I_x set by transistor Q_1 and resistor R_x is mirrored in the current I through transistor Q_2 .

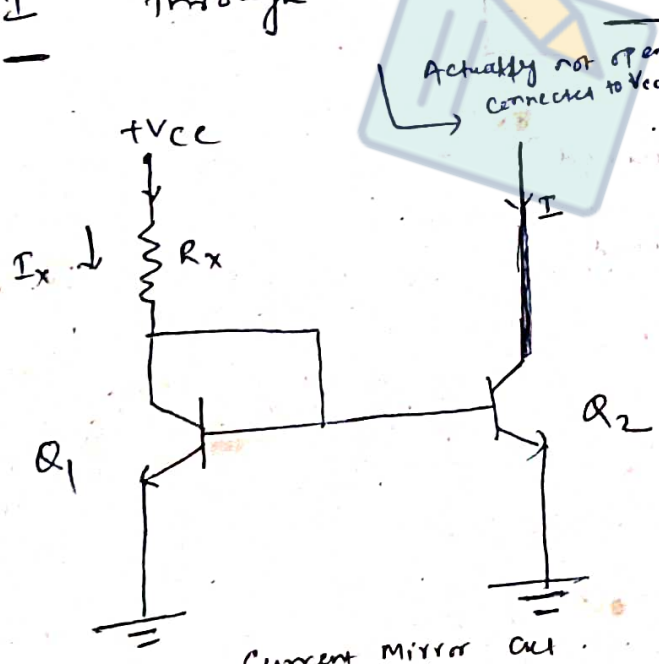


Fig 1:- Current mirror ckt.

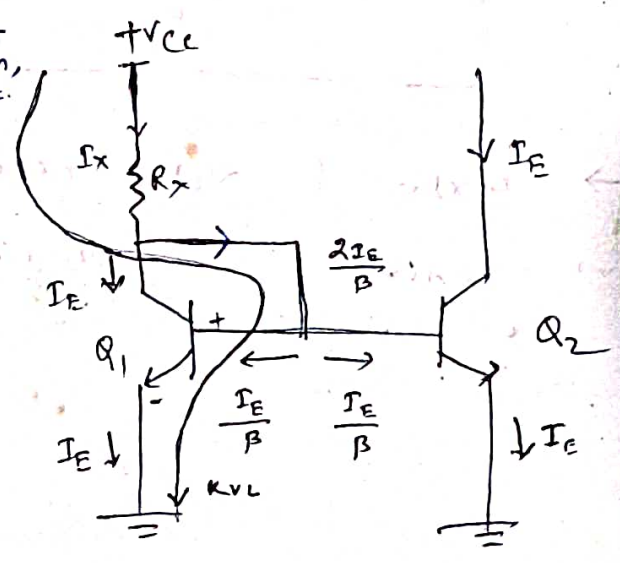


Fig 2:- Circuit currents for current mirror circuit.

The current I_x and I can be obtained using the circuit currents listed in fig 2 - We assume that the emitter current (I_E) for both transistors is the same (Q_1 & Q_2 being fabricated near each other on the same chip) The two transistor base currents are then approximately,

$$I_B = \frac{I_E}{\beta + 1} \approx \frac{I_E}{\beta}$$

The collector current of each transistor is then $I_C \approx I_E$

Finally, the current I_x through resistor R_x is

$$I_x = I_E + \frac{2I_E}{\beta} = \frac{\beta I_E + 2I_E}{\beta} = \frac{(\beta + 2)I_E}{\beta} \approx I_E$$

($\because \beta + 2 \approx \beta$)

Applying a KVL

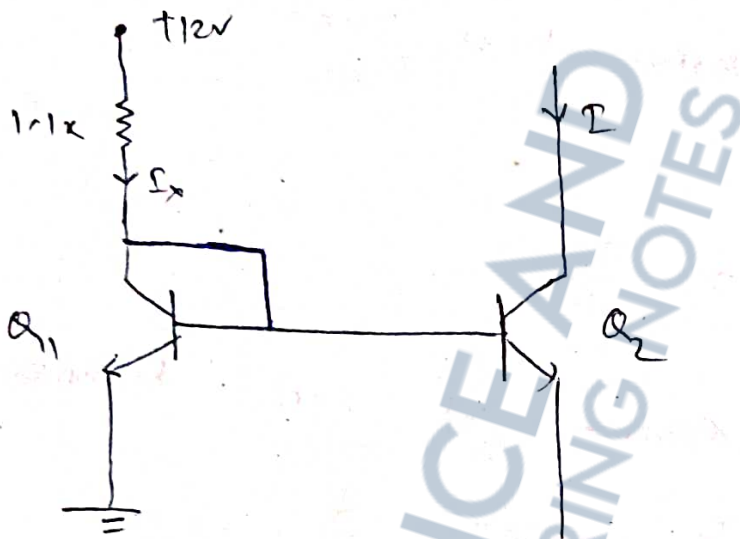
$$V_{CC} - I_x R_x - V_{BE} = 0$$

$$\Rightarrow I_x = \frac{V_{CC} - V_{BE}}{R_x} = I_E$$

\therefore The constant current provided at the collector of Q_2 mirrors that of Q_1 . The transistor

Q_1 is referred to as a diode-connected transistor because the base & collector are shorted together.

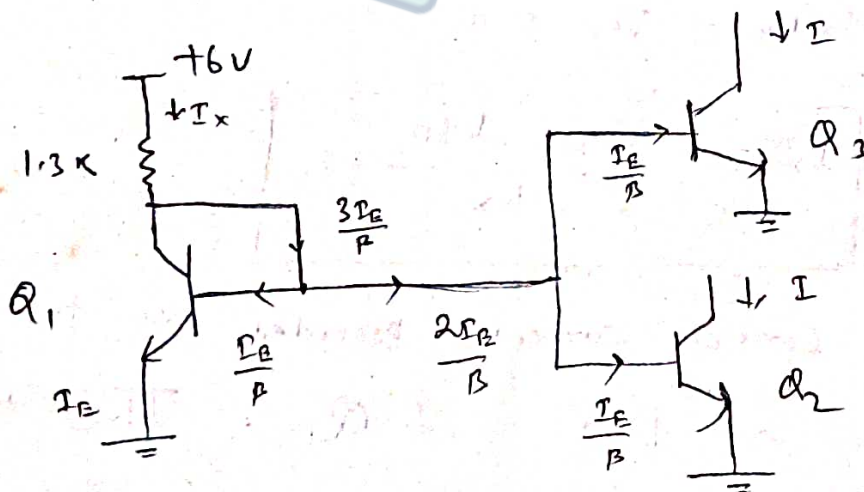
Ex-15 Calculate the mirrored current I on the ckt shown below.



$$I = I_x = \frac{V_{CC} - V_{BE}}{R_x} = \frac{12 - 0.7}{1.1 \times 10^3} = 11.3$$

$$I_x = I = 10.27 \text{ mA}$$

Ex-16: - Calculate the current I through each of the transistors Q_2 & Q_3 on the ckt, shown below



Soln :-

$$I_x = I_E + \frac{3I_E}{\beta} = \frac{\beta I_E + 3I_E}{\beta}$$

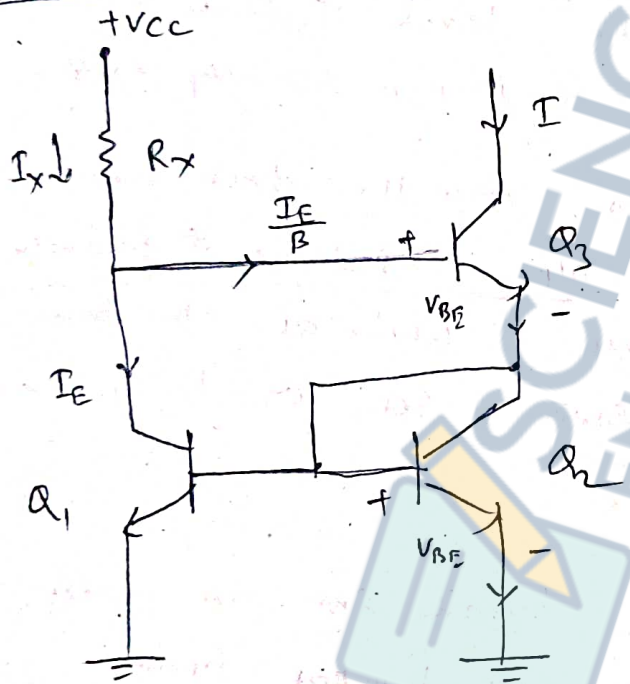
$$= \frac{(\beta + 3) I_E}{\beta}$$

$$I_x \approx I_E \quad (\because \beta + 3 \approx \beta)$$

$$\therefore I = I_x = \frac{V_{CC} - V_{BE}}{R_x} = \frac{6 - 0.7}{1.3 \times 10^3}$$

$I = 4.08 \text{ mA}$

*Extra: - (Not required)



Current Mirror Ckt

With higher output impedance

$$I_x = \frac{V_{CC} - 2V_{BE}}{R_x} = \frac{I_E + I_E}{\beta} = \frac{\beta I_E + I_E}{\beta} = \frac{(\beta + 1) I_E}{\beta}$$

$I_x \approx I_E$

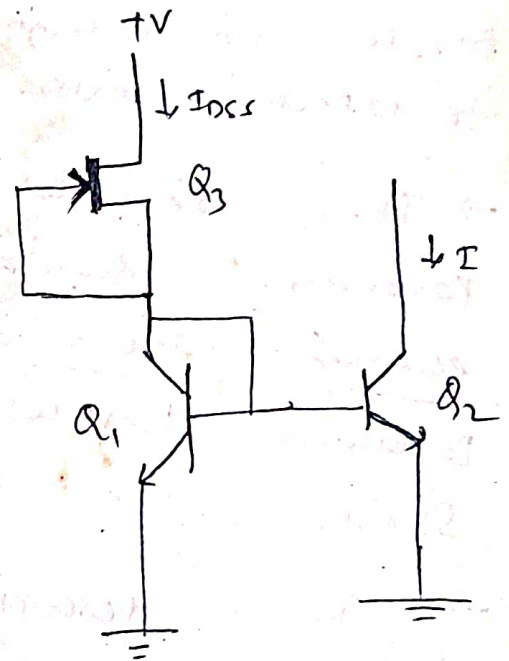


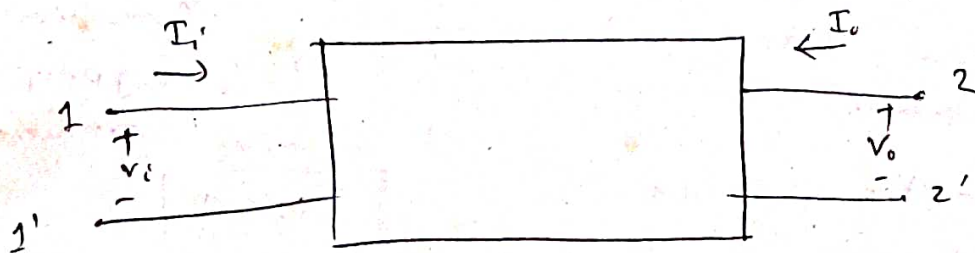
fig - Current mirror connection using a JFET & 2 transistors.

$I_{DSS} = I$

Handwritten notes in pink:
 The current mirror is used to provide a constant current source. It is used in many applications where a constant current is required.

The Hybrid Equivalent Model :-

- It was used in the early years before the popularity of the r_e model developed.
- The r_e model has the advantage that the parameters are defined by the actual operating conditions whereas the parameters of the hybrid equivalent circuit are defined by general terms for any operating conditions.
- In other words, the hybrid parameters may not reflect the actual operating conditions but simply provide an indication of the level of each parameter to expect, no matter what conditions actually exist.
- The r_e model suffers from the fact that parameters such as the o/p impedance & feedback elements are not available, whereas the hybrid parameters provide the entire set on the specification sheet.
- The description of hybrid equivalent model will begin with the general two-port system.



i/p Voltage & current in terms of o/p current & o/p voltage.

fig:- Two port system
$$\begin{bmatrix} V_i \\ I_o \end{bmatrix} = \begin{bmatrix} h_{11} & h_{12} \\ h_{21} & h_{22} \end{bmatrix} \begin{bmatrix} I_i \\ V_o \end{bmatrix}$$

$$V_i = h_{11} I_i + h_{12} V_o \quad \text{--- (1)}$$

$$I_o = h_{21} I_i + h_{22} V_o \quad \text{--- (2)}$$

The parameters relating the four variables are called h-parameters, from the word "hybrid". The term hybrid was chosen because the mixture of variables (V and I) in each equation results in a 'hybrid' set of units of measurement for h-parameters.

From eqn (1),

if $V_o = 0$, (short ckt the o/p terminals)

$h_{11} = \frac{V_i}{I_i} \Big _{V_o=0}$	Ohms.
--	-------

h_{11} = Short-circuit input impedance parameter.

if I_i is set to zero, by opening i/p leads,

$h_{12} = \frac{V_i}{V_o} \Big _{I_i=0}$	unitless.
--	-----------

h_{12} = Open-circuit reverse transfer voltage ratio.

Similarly,

if V_o is set equal to zero,

$h_{21} = \frac{I_o}{I_i} \Big _{V_o=0}$	unitless
--	----------

$h_{21} =$ ~~Forward~~ Short ckt forward transfer Current ratio parameter.

The last parameter, h_{22} can be found out setting $I_r = 0$,

$$h_{22} = \frac{I_o}{V_o} \Big|_{I_r=0} \text{ Siemens.}$$

$h_{22} =$ Open ckt O/P admittance parameter.

In Summary,

- $h_{11} =$ input resistance = h_i
- $h_{12} =$ reverse transfer voltage ratio = h_r
- $h_{21} =$ forward transfer current ratio = h_f
- $h_{22} =$ Output Conductance = h_o

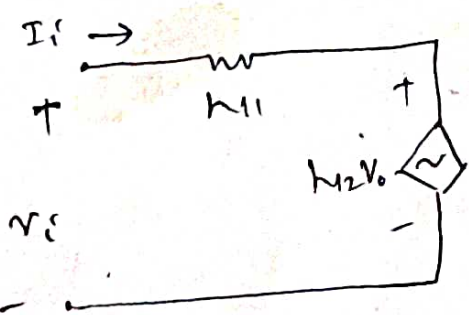
Trick to remember -

i	r	f	o
i	-	-	-
r	-	-	-
f	-	-	-
o	-	-	-

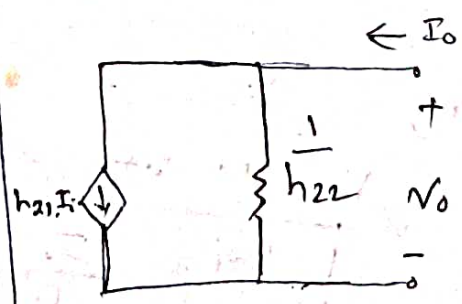
For Common-Emitter (CE)

	<u>CB</u>	<u>CE</u>
h_{ie}	h_{ib}	h_{ic}
h_{re}	h_{rb}	h_{rc}
h_{fe}	h_{fb}	h_{fc}
h_{oe}	h_{ob}	h_{oc}

Hybrid π P Equivalents



Hybrid O/P Equivalents



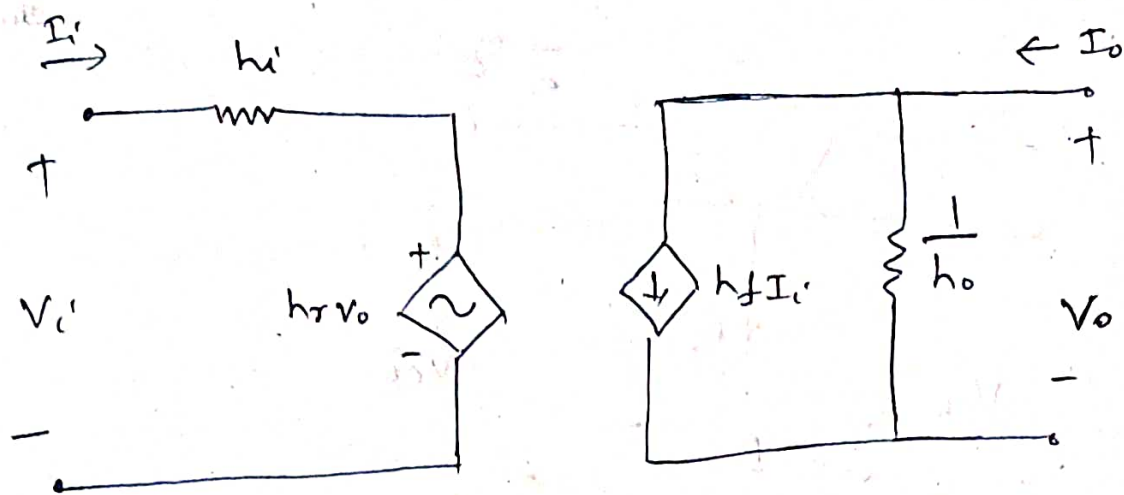
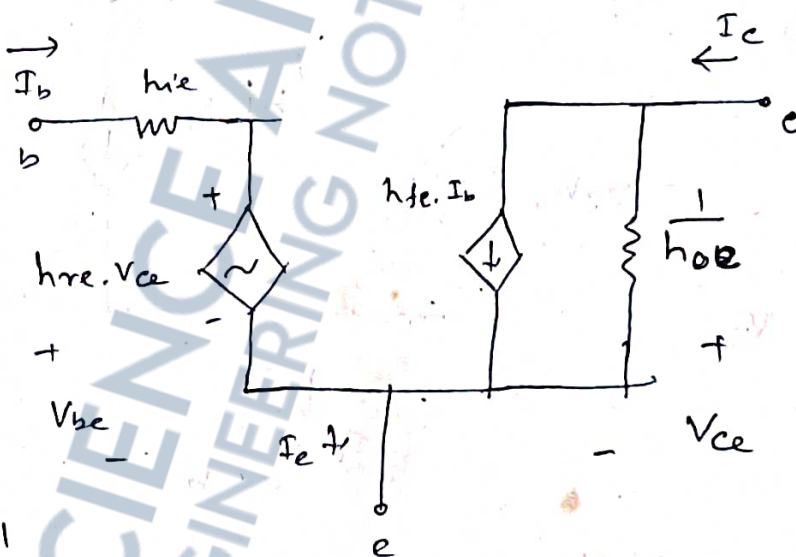
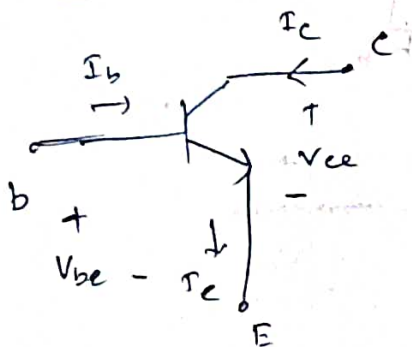


Fig. - Complete hybrid equivalent circuit.

CE 1 -



CE - (a) Graphical Symbol

(b) Hybrid equivalent model.

Here,

$$I_i = I_b, \quad I_o = I_c$$

$$I_e = I_b + I_c$$

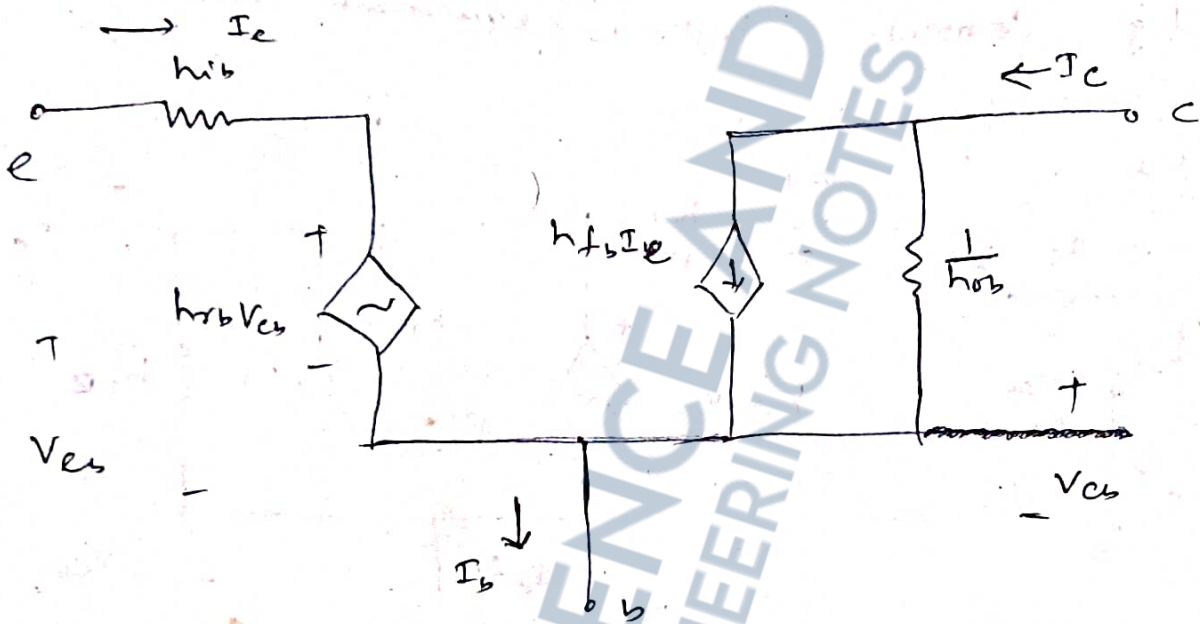
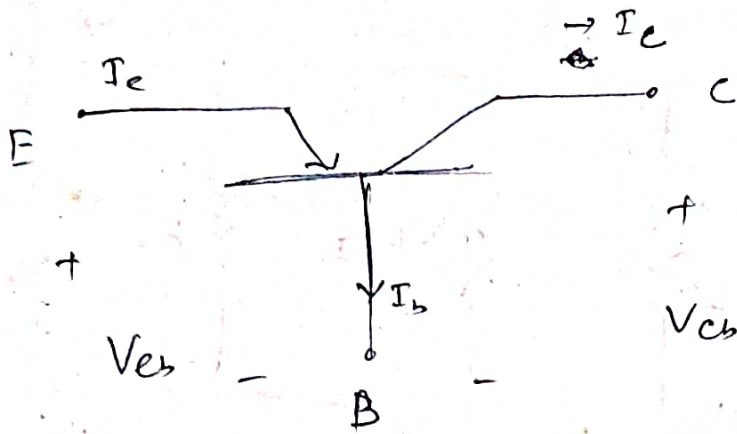
Input Voltage = V_{be} , O/P Voltage = V_{ce} .

For Common-base :-

$$I_i = I_e, \quad I_o = I_c, \quad \text{with } V_{eb} = V_{i'} \text{ and}$$

$$V_{cb} = V_o$$

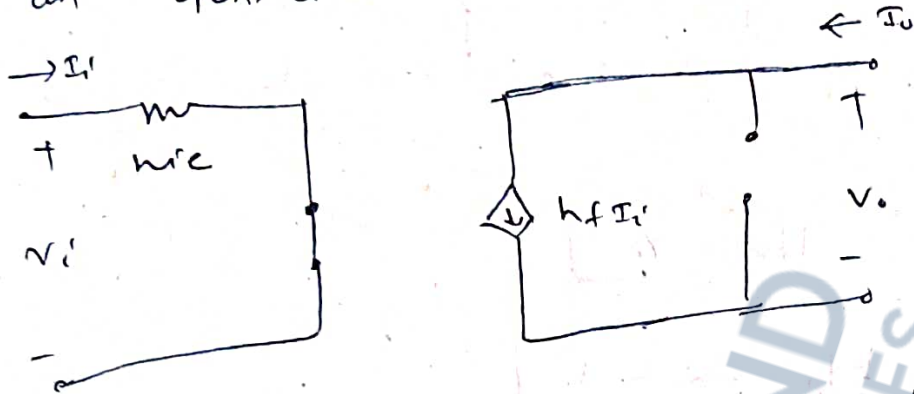
CB



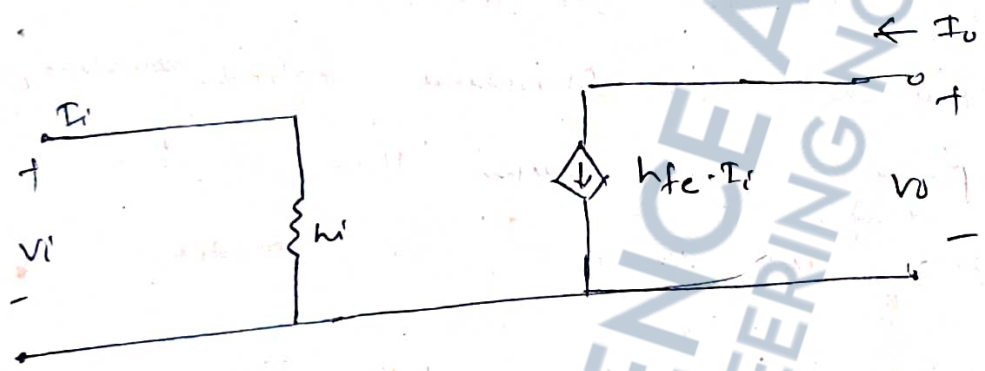
Here actual I_c direction should be outward or up, but in the two port-system or hybrid model I_o is shown into the system, I_e is shown outward. So h_{fb} will automatically come -ve.

Note :- h_{rb} is relatively small quantity.
 $h_{rb} \approx 0$
 $h_{re} \approx 0$, resulting a short-circuited equivalent.

Since resistance determined by $\frac{1}{h_o}$ is often too large enough to be ignored, permitting by an open-circuit equivalent,

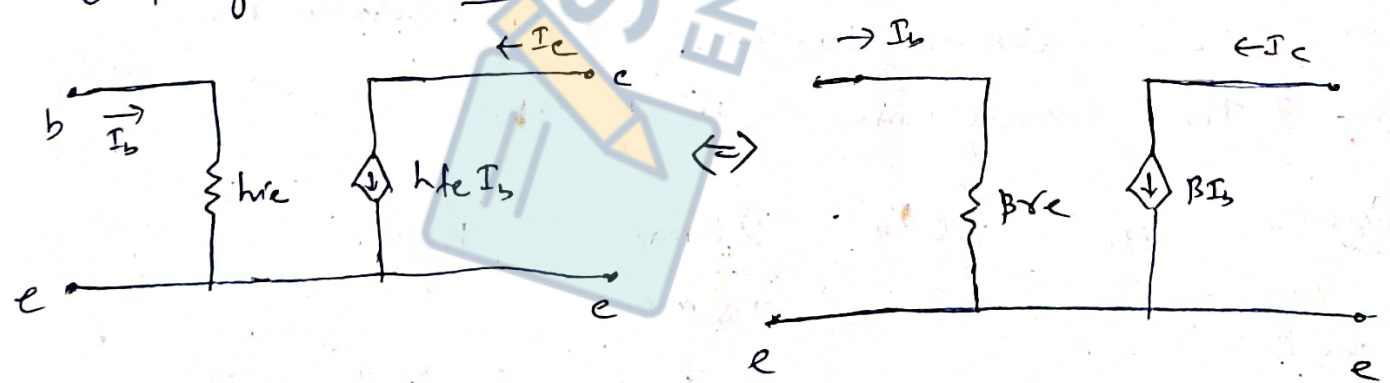


Effect of removing h_{ie} & h_{oe} .



Approximate hybrid equivalent model.

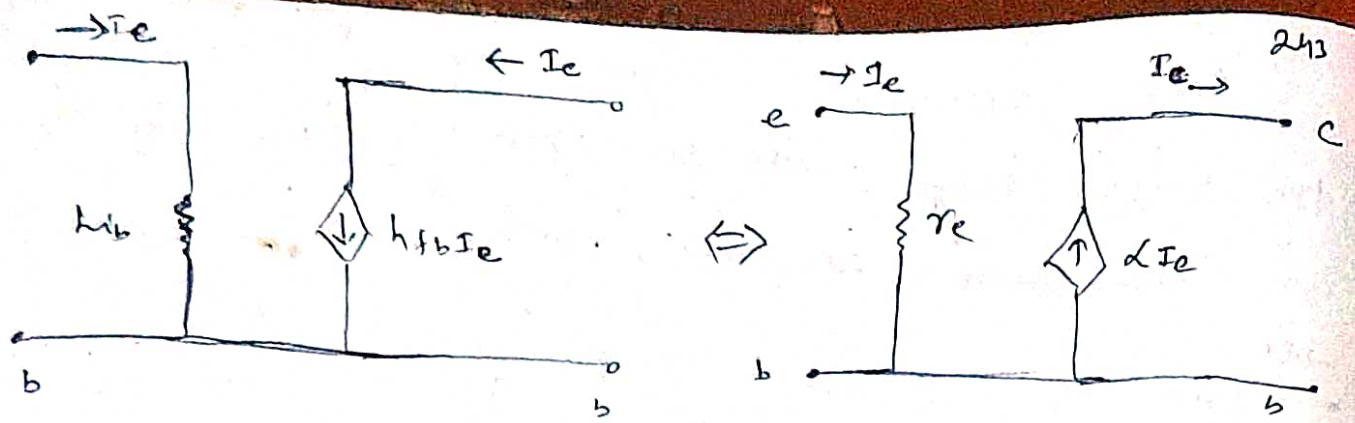
Comparing this approximate hybrid model with r_e model.



$$h_{ie} = \beta r_e$$

$$h_{fe} = \beta a_c$$

fig:- hybrid vs r_e model :- Common emitter configuration



$$\therefore \boxed{h_{ib} = r_e}$$

$$\boxed{h_{fb} = -\alpha \approx -1}$$

The $-$ sign, accounts for the fact that the current source of standard hybrid equivalent circuit is pointing down rather than in the actual direction as shown in r_e model.

Ex: 17 :- Given $I_E = 2.5 \text{ mA}$, $h_{fe} = 140$
 $h_{oe} = 20 \mu\text{S}$ (Negl), $h_{ob} = 0.5 \mu\text{S}$

determine:

- The Common-emitter hybrid equivalent circuit
- The Common-base r_e model

Ans :-
$$r_e = \frac{26 \text{ mV}}{I_E} = \frac{26 \text{ mV}}{2.5 \text{ mA}} = 10.4 \Omega$$

$$h_{ie} = \beta r_e \Rightarrow h_{ie} = \beta \cdot 10.4 \quad \text{--- (1)}$$

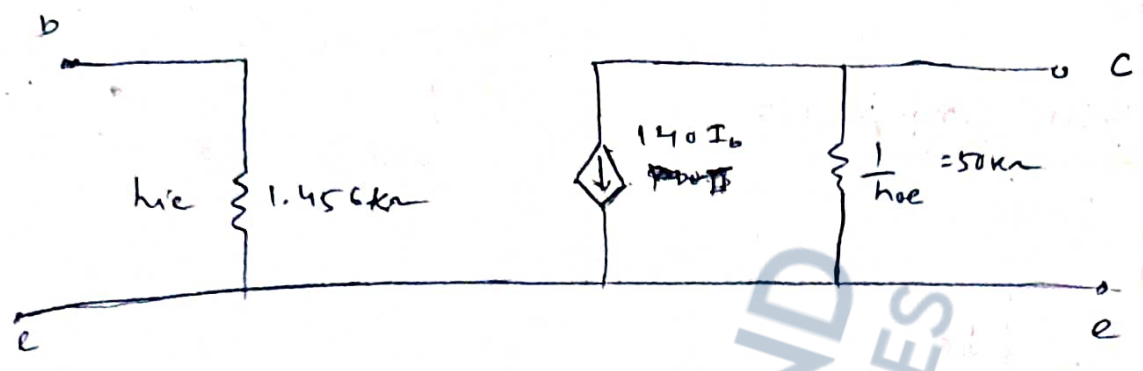
$$h_{fe} = \beta \Rightarrow 140 = \beta \quad \text{--- (2)}$$

Putting eqⁿ (2), in eqⁿ (1), $h_{ie} = 140 \times 10.4 = 1.456 \text{ k}\Omega$.

$$\boxed{h_{ie} = 1.456 \text{ k}\Omega}$$

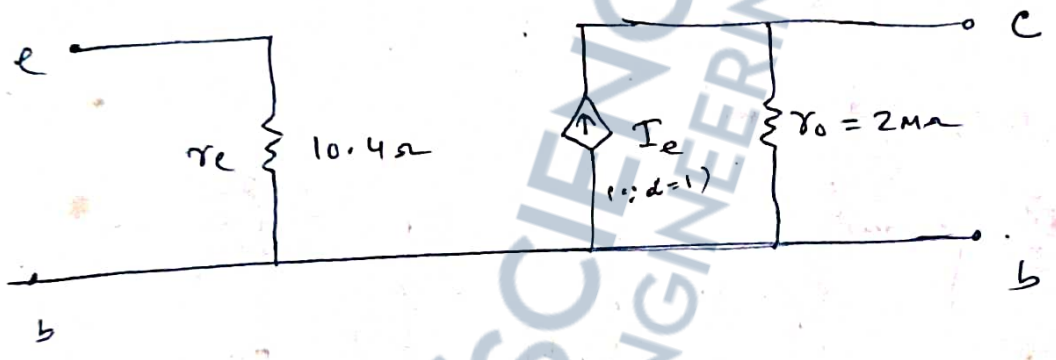
$h_{oe} = 20 \mu S$

$\Rightarrow \frac{1}{h_{oe}} = \frac{1}{20 \times 10^{-6}} = \frac{10^6}{20} = \frac{10^5 \times 10^2 \times 10^3}{20} = 50 k\Omega$

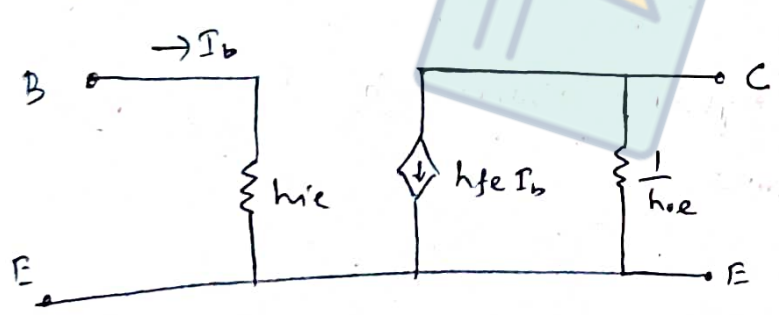


(b) $r_e = 10.4 \Omega$

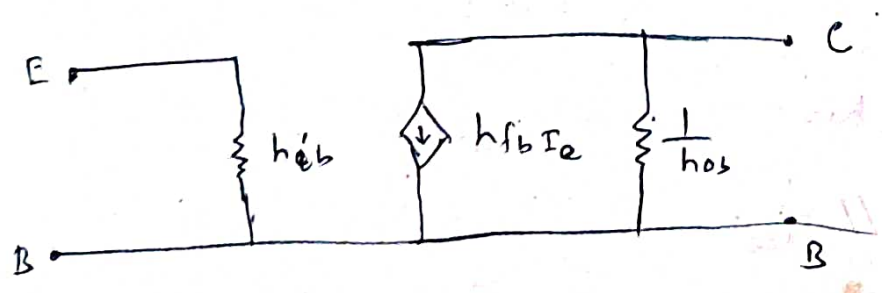
$\alpha \approx 1, r_o = \frac{1}{h_{ob}} = \frac{1}{0.5 \times 10^{-6}} = 2 M\Omega$



Approximate Hybrid Equivalent Circuit:-



\Rightarrow Approximate CE hybrid equivalent circuit



\Rightarrow Approximate CB hybrid equivalent circuit.

Relⁿ betⁿ r_e & hybrid model

$h_{ie} = \beta r_e$, $h_{fe} = \beta$, $h_{oe} = \frac{1}{r_o}$, $h_{fb} = -\alpha$, $h_{rb} = r_e$

$h_{ob} = \frac{1}{r_o}$

Fixed - Bias Configuration :-

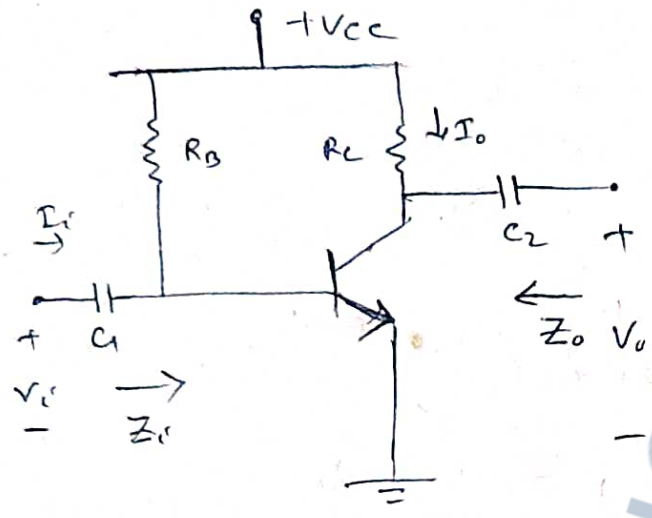


fig:- Fixed-Bias Configuration

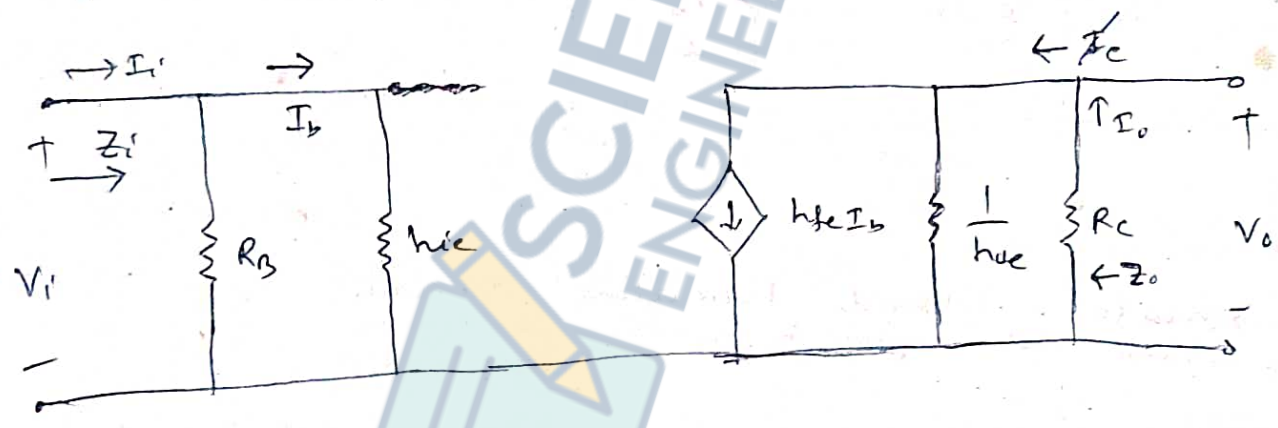


fig:- Approximate hybrid model for Fixed bias Configuration.

(a) Z_i

$Z_i = R_B \parallel h_{ie}$

(b) $Z_o = R_C \parallel \frac{1}{h_{oe}}$

(c) A_v

Let $R' = \frac{1}{h_{oe}} \parallel R_c$ — (1)

~~$V_o = I_o R'$~~

By current division rule \leftarrow current \leftarrow opposite branch resistance

$$I_o = \frac{(h_{fe} \cdot I_b) \times \frac{1}{h_{oe}}}{\left(\frac{1}{h_{oe}} + R_c\right)}$$

\leftarrow Total resistance

$$V_o = -I_o R_c = - \left[\frac{h_{fe} \cdot I_b \times \frac{1}{h_{oe}}}{\frac{1}{h_{oe}} + R_c} \right] \cdot R_c$$

$$= -h_{fe} I_b \cdot \left(\frac{\frac{1}{h_{oe}} \times R_c}{\frac{1}{h_{oe}} + R_c} \right)$$

$$V_o = -h_{fe} I_b \cdot R' \quad \text{--- (2)}$$

($\because R' = \frac{1}{h_{oe}} \parallel R_c$)

But $I_b = \frac{V_i}{h_{ie}}$ — (3)

Putting eqn (3), on eqn (2), we have

$$V_o = -h_{fe} \cdot \frac{V_i}{h_{ie}} \cdot R'$$

$$\Rightarrow \frac{V_o}{V_i} = - \frac{h_{fe}}{h_{ie}} \cdot \left(R_c \parallel \frac{1}{h_{oe}} \right)$$

$= - \frac{h_{fe} \cdot R_c}{h_{ie}}$

$$\Rightarrow \boxed{A_v = - \frac{h_{fe} \cdot (R_c \parallel \frac{1}{h_{oe}})}{h_{ie}}}$$

$\approx - \frac{h_{fe} \cdot R_c}{h_{ie}} \cdot \frac{R_c}{R_c + \frac{1}{h_{oe}}}$

for finding A_v $\frac{-R_c}{h_{ie} + R_c}$
 $\frac{-R_c}{h_{ie} + R_c} = \frac{-R_c}{\frac{h_{ie} + R_c}{h_{ie}}}$
 $\frac{-R_c}{h_{ie} + R_c} = \frac{-R_c \cdot h_{ie}}{h_{ie} + R_c}$

(d) A_i

Assuming $R_B \gg h_{ie}$, $I_b \approx I_c$

$\frac{1}{h_{fe}} \gg 10R_C$, $I_o = I_c$

Using I_o

$A_i = \frac{h_{fe} R_B}{R_B + h_{ie}}$
 (As derived in voltage divider bias next page)

$\therefore I_o = h_{fe} I_b \approx h_{fe} I_c$ ($\because I_b = I_c$)

$\Rightarrow \frac{I_o}{I_i} = h_{fe}$

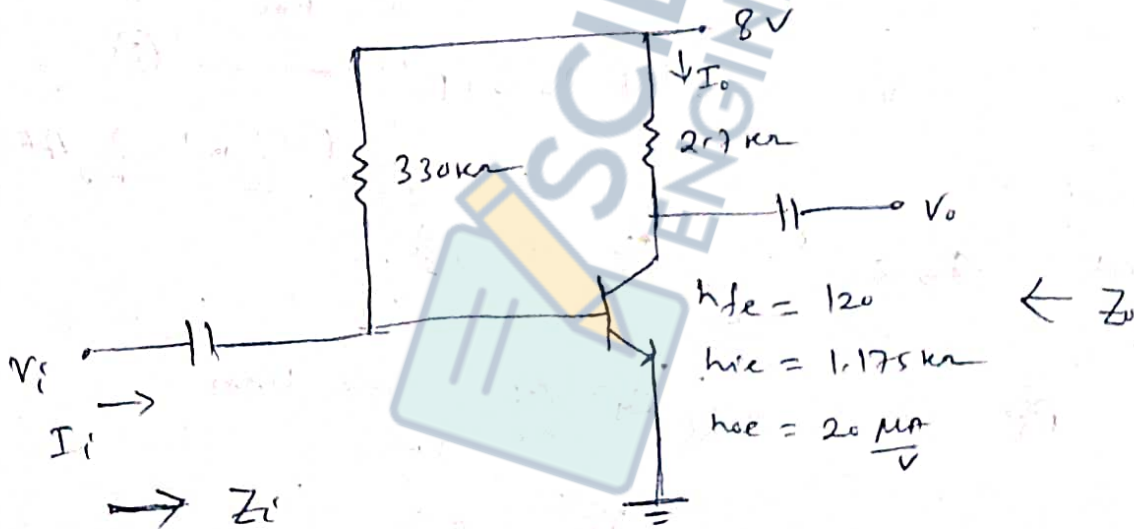
$\Rightarrow A_i = h_{fe}$

or
 $I_b = \frac{I_i \times R_B}{R_B + h_{ie}}$
 $\Rightarrow I_i = \frac{I_o (R_B + h_{ie})}{R_B}$
 $I_o = h_{fe} I_b$
 $\frac{I_o}{I_i} = \frac{h_{fe} R_B \times R_B}{I_o (R_B + h_{ie})} = \frac{h_{fe} R_B}{R_B + h_{ie}}$

Ex :- 18

For the r/w determine

- (a) Z_i (b) Z_o (c) A_v (d) A_i



Ans :-

$Z_i = R_B \parallel h_{ie} = 330k\Omega \parallel 1.175k\Omega$

$Z_i \approx 1.175k\Omega \approx h_{ie}$

(b) $Z_o = \frac{1}{h_{oe}} \parallel R_c$

$\frac{1}{h_{oe}} = \frac{1}{20 \times 10^{-6}} = \frac{100 \times 10^4}{20} = 50 \text{ k}\Omega$

$Z_o = 50 \text{ k}\Omega \parallel 2.7 \text{ k}\Omega \approx 2.56 \text{ k}\Omega \approx R_c$

(c) $A_v = \frac{-h_{fe}}{h_{ie}} \cdot (R_c \parallel \frac{1}{h_{oe}})$

$= \frac{-120}{1.175} \cdot (2.7 \text{ k}\Omega \parallel 50 \text{ k}\Omega)$

$= \frac{-120 \times 2.56 \text{ k}\Omega}{1.175 \text{ k}\Omega}$

$A_v = -261.45$

$A_v = \frac{h_{fe} R_B}{R_B + h_{ie}} = 119.57$
(exact)

(d) $A_v \approx h_{fe} = 120$

Voltage divider Configuration :- (With bypass capacitor)

For voltage divider

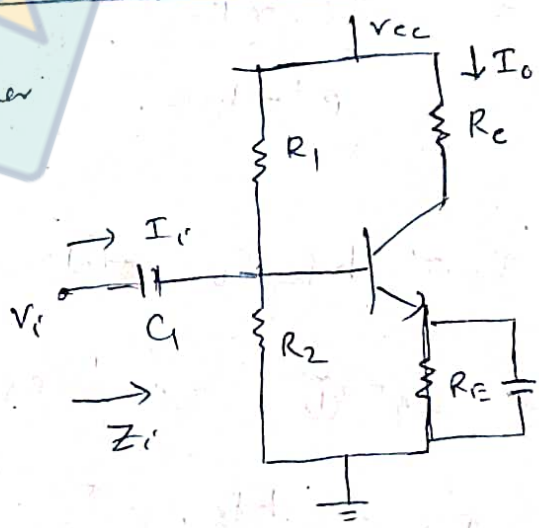
bias configuration,

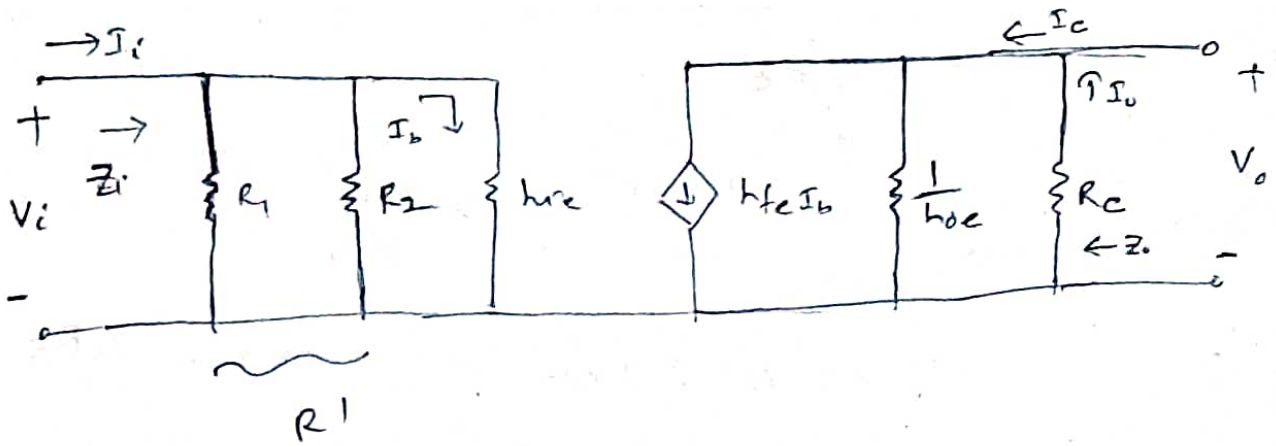
the a.c equivalent
circuit is same
as that of fixed

bias except R_B

will be replaced by $R_1 \parallel R_2$

$R_B \leftrightarrow R_1 \parallel R_2$





$$\underline{Z_i} =$$

$$Z_i = R_1 \parallel R_2 \parallel h_{ie}$$

$$\underline{Z_o}$$

$$Z_o \approx R_c$$

$$(\because Z_o = \frac{1}{h_{oe}} \parallel R_c)$$

$$\text{Since } \frac{1}{h_{oe}} \gg R_c$$

$$Z_o \approx R_c)$$

$$A_v =$$

$$- \frac{h_{fe}}{h_{ie}} \cdot (R_c \parallel \frac{1}{h_{oe}})$$

(Same as fixed bias)

$$\underline{A_i} =$$

$$I_b = \frac{I_i \times R'}{R' + h_{ie}} \quad \text{--- (1) (By current division rule)}$$

$$\& \quad I_o \approx h_{fe} I_b \quad \text{--- (2) } (\because \frac{1}{h_{oe}} \gg 10R_c)$$

Putting eqⁿ (1) in eqⁿ (2),

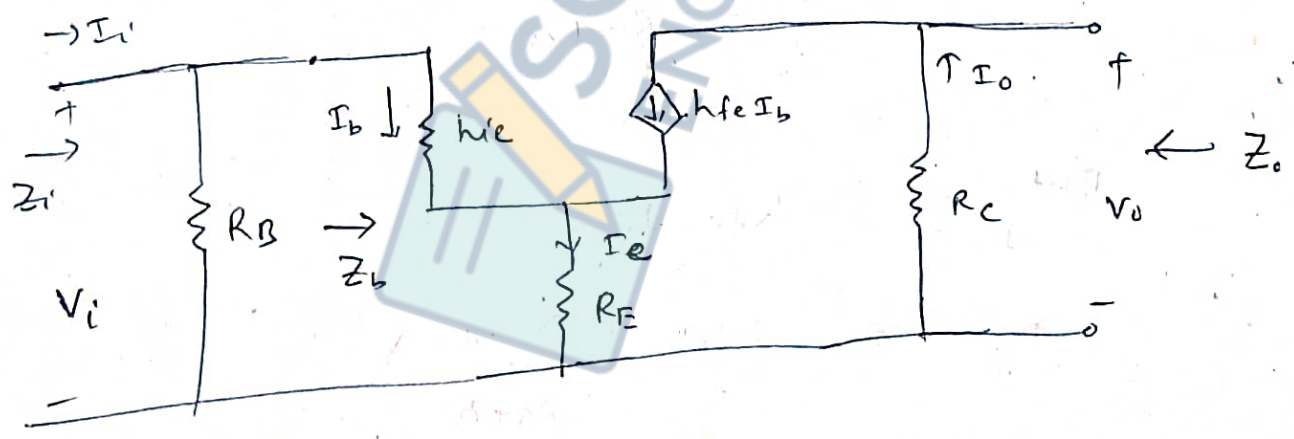
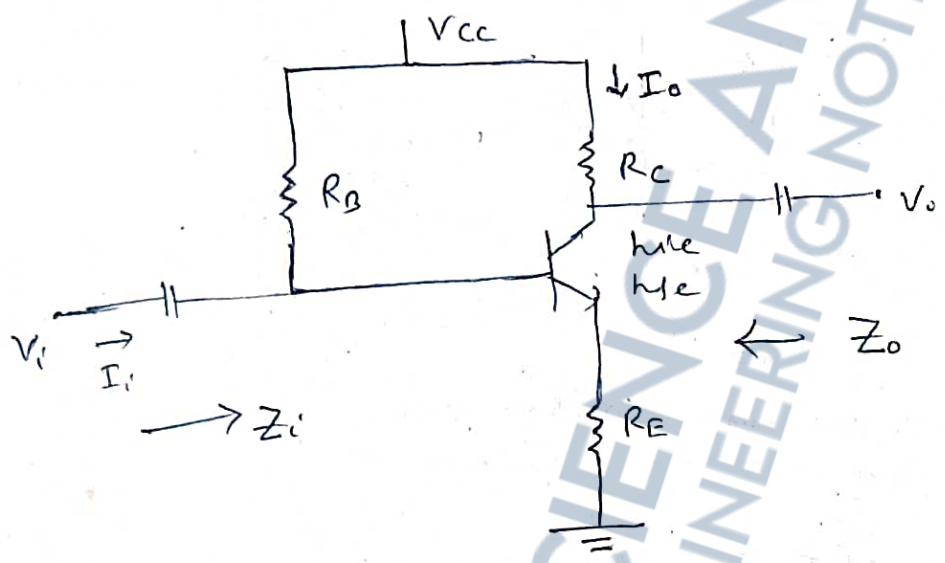
$$I_o = h_{fe} \cdot \left(\frac{I_i \cdot R'}{R' + h_{ie}} \right)$$

$$\Rightarrow \frac{I_o}{I_i} = \frac{h_{fe} \cdot R'}{R' + h_{ie}}$$

$$\Rightarrow A_v = \frac{h_{fe} \cdot R'}{R' + h_{ie}}$$

Where
 $R' = R_1 // R_2$

Unbypassed Emitter-bias Configuration :-



Z_i :-

$$Z_b = h_{fe} R_E$$

(\because In the model,
 $Z_b = \beta R_E$)

$$Z_i = R_B // Z_b$$

Z_o

$$Z_o = R_c$$

A_v

In the model,
Voltage divider

$$A_v = \frac{V_o}{V_i} = - \frac{h_{fe} R_c}{Z_b} \quad \left(\because A_v = -\frac{\beta R_c}{Z_b} \right)$$

$$= - \frac{h_{fe} R_c}{h_{fe} R_E}$$

$$A_v = - \frac{R_c}{R_E}$$

A_i

By Current division rule

$$I_b = I_r \times \frac{R_B}{R_B + Z_b}$$

But $I_o = h_{fe} \cdot I_b$

$$I_o = h_{fe} \cdot \left(\frac{I_r R_B}{R_B + Z_b} \right)$$

$$\Rightarrow \frac{I_o}{I_r} = \frac{h_{fe} R_B}{R_B + Z_b}$$

$$\Rightarrow A_i = \frac{h_{fe} R_B}{R_B + Z_b}$$

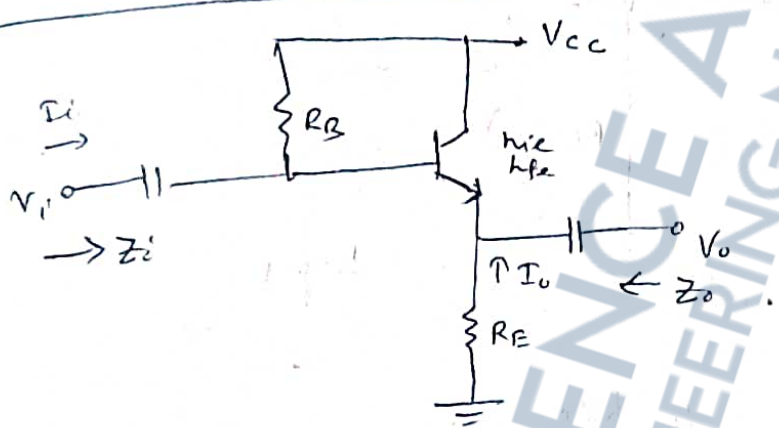
$$A_i = -A_v \cdot \frac{Z_i}{R_c}$$

$$\left(\therefore A_v = -A_v \cdot \frac{Z_i}{R_c} \right)$$

$$\left[\therefore A_i = - \left(\frac{-h_{fe} R_c}{Z_b} \right) \cdot \frac{R_B \times Z_b}{R_B + Z_b} \cdot \frac{1}{R_c} \right]$$

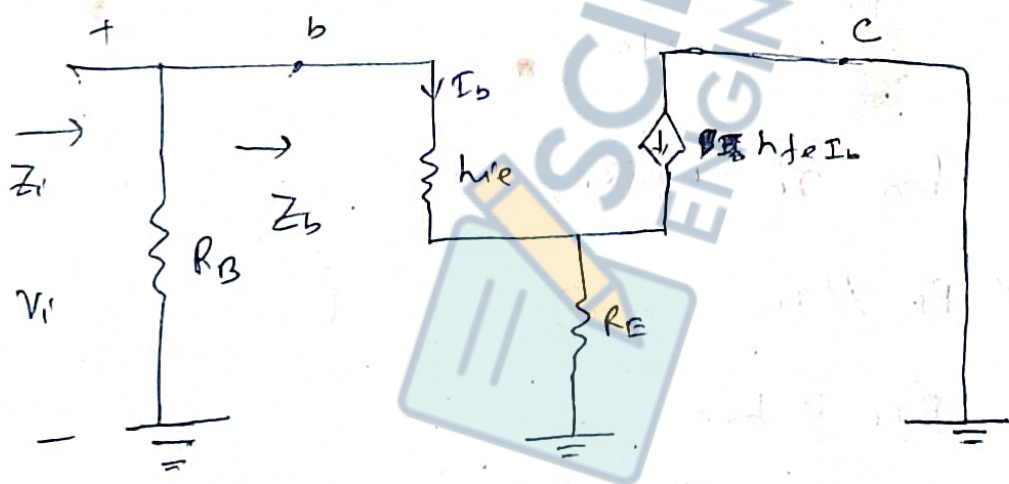
$$A_i = + \frac{h_{fe} R_B}{R_B + Z_b}$$

Emitter - follower Configuration :-



8. Voltage divider without Bypass

- 1) $Z_i = R' \parallel Z_b = R' \parallel h_{fe} R_E$
- 2) $Z_o = R_c$
- 3) $A_v = -\frac{R_c}{R_E}$
- 4) $A_i = \frac{h_{fe} R'}{R' + Z_b}$



$\rightarrow \beta R_E = h_{ie}, \quad h_{fe} = \beta$ (Same as that of \tilde{r}_e' model)

$$Z_i = R_B \parallel Z_b$$

With $Z_b \approx h_{fe} R_E$

$$\left(\therefore Z_b = \beta (r_e + R_E) \approx \beta R_E \right)$$

Z_o [Same approach as in r_e model]

$$I_b = \frac{V_i}{Z_o} = \frac{V_i}{\beta(r_e + R_E)}$$

$$\Rightarrow (\beta + 1) I_b = \frac{(\beta + 1) V_i}{\beta(r_e + R_E)} \quad \left[\begin{array}{l} \text{multiplying } (\beta + 1) \text{ on L.H.S} \\ \text{R.H.S} \end{array} \right]$$

$$\Rightarrow I_E \approx \frac{\beta V_i}{\beta r_e + \beta R_E} = \frac{h_{fe} V_i}{h_{ie} + \beta h_{fe} R_E} = \frac{V_i}{\frac{h_{ie}}{h_{fe}} + R_E}$$

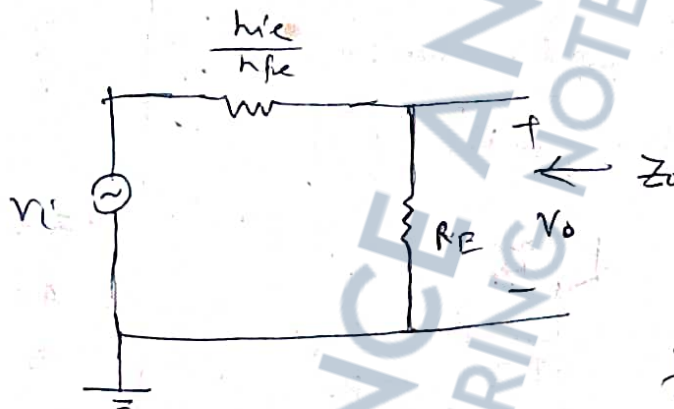


Fig 2.

$$Z_o = R_E \parallel \frac{h_{ie}}{h_{fe}}$$

or Directly from r_e model,

$$Z_o \approx R_E \parallel r_e \quad \text{--- (1)}$$

$$\text{But } \beta r_e = h_{ie}$$

$$\Rightarrow r_e = \frac{h_{ie}}{\beta} = \frac{h_{ie}}{h_{fe}}$$

$$\therefore Z_o \approx R_E \parallel \frac{h_{ie}}{h_{fe}}$$

A_v : From fig 1, (Previous page)

$$V_o = \frac{V_i}{\frac{h_{ie}}{h_{fe}} + R_E} \times R_E$$

$$\Rightarrow \frac{V_o}{V_i} = \frac{R_E}{R_E + \frac{h_{ie}}{h_{fe}}}$$

$$\Rightarrow \boxed{A_v = \frac{R_E}{R_E + \frac{h_{ie}}{h_{fe}}}}$$

or r_e model

$$A_v = \frac{R_E}{r_e + R_E} = \frac{R_E}{\frac{h_{ie}}{h_{fe}} + R_E}$$

($\because \beta r_e = h_{ie}$
 $\Rightarrow r_e = \frac{h_{ie}}{\beta} = \frac{h_{ie}}{h_{fe}}$)

A_i

$$\boxed{A_i = \frac{h_{fe} R_B}{R_B + Z_s}}$$

derivation Same as unbypassed Emitter-bias

$$w. \quad \boxed{A_i = -A_v \cdot \frac{Z_i'}{R_E}}$$

Common-Base Configuration :-

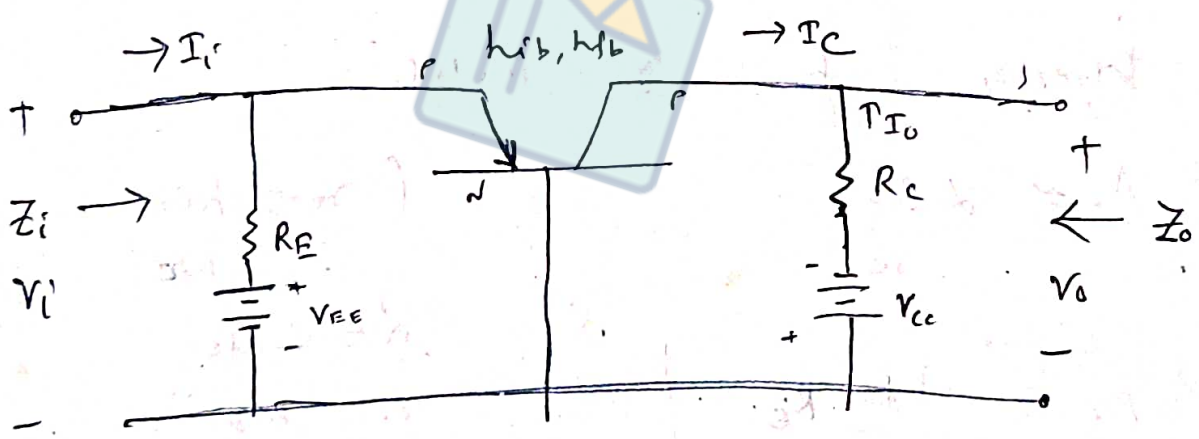
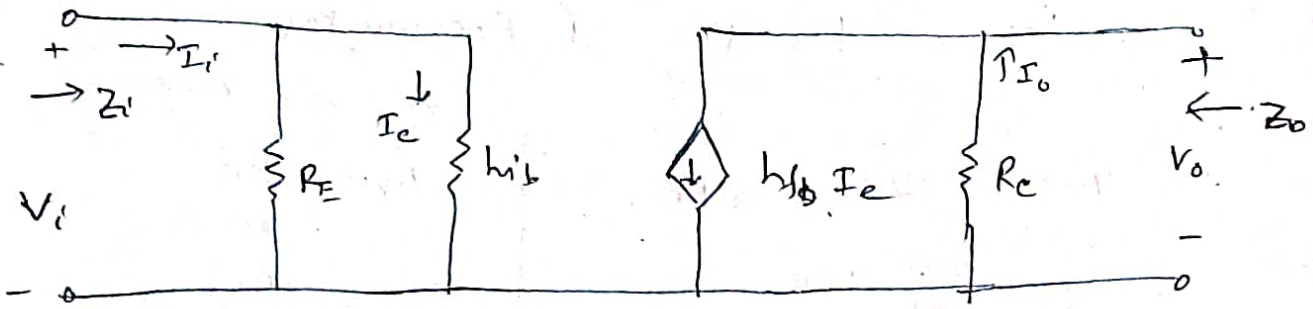


fig:- Common base Configuration



Zi

$$Z_i = R_E \parallel h_{ie}$$

Zo

$$Z_o = R_c \quad \left[\begin{aligned} Z_o &= R_c \parallel \frac{1}{h_{ob}} \\ &\approx R_c \quad \because \frac{1}{h_{ob}} \gg R_c \end{aligned} \right]$$

Av

$$V_o = -I_o R_c = -(h_{fb} I_e) R_c$$

W.A.H $I_e = \frac{V_i}{h_{ie}}, V_o = -h_{fb} \cdot \frac{V_i}{h_{ie}} \cdot R_c$

$$\Rightarrow \frac{V_o}{V_i} = -\frac{h_{fb}}{h_{ie}} \cdot R_c$$

$$A_v = -\frac{h_{fb}}{h_{ie}} \cdot R_c$$

12

Directly from r_e model

$$A_v \approx \frac{\alpha R_c}{r_e}$$

$$\left(\begin{aligned} r_e &= \frac{h_{ie}}{h_{fb}} \end{aligned} \right)$$

$$\begin{aligned} \alpha &= -h_{fb} \\ r_e &= h_{ie} \end{aligned}$$

$$A_v = \frac{-h_{fb} R_c}{h_{ie}}$$

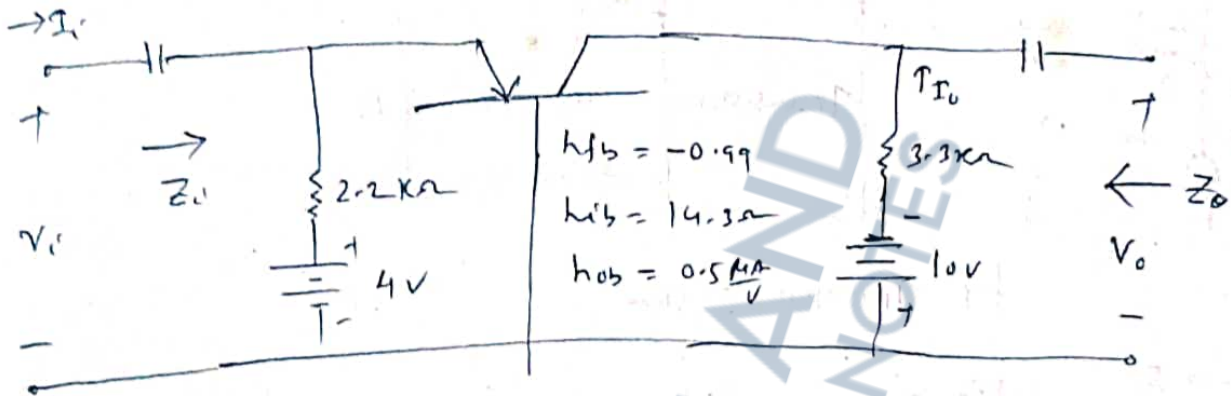
$$\underline{A_i} \quad A_i = \frac{I_o}{I_i} = h_{fb} \approx -1 \quad (\text{if } R_E \gg h_{ib})$$

$$\begin{aligned} I_o &= h_{fb} I_c \\ I_i &= I_e \end{aligned}$$

Ex-19

Determine

- (a) Z_i (b) Z_o (c) A_v (d) A_i



Solⁿ : (a) $Z_i = R_E \parallel h_{ib} = 2.2 \text{ k}\Omega \parallel 14.3 \Omega = 14.1 \Omega \approx h_{ib}$

(b) $Z_o \approx R_c \approx 3.3 \text{ k}\Omega$

Actually $Z_o = R_c \parallel \frac{1}{h_{ob}} = 3.3 \text{ k}\Omega \parallel \frac{1}{0.5 \text{ mA/V}} = 3.3 \text{ k}\Omega \parallel 2 \text{ M}\Omega \approx 3.3 \text{ k}\Omega$

(c) $A_v = -\frac{h_{fb}}{h_{ib}} \cdot R_c = -\frac{(-0.99)}{14.3} \times 3.3 \text{ k}\Omega = 229.91$

(d) $A_i \approx h_{fb} \approx -0.99 \approx -1$

Note :-

$$I_e = \frac{I_i \times R_E}{R_E + h_{ib}} \Rightarrow I_r = \frac{I_c (R_E + h_{ib})}{R_E}$$

(Previous figure)

$$I_o = h_{fb} I_c \Rightarrow \frac{I_o}{I_r} = \frac{h_{fb} I_c \times R_E}{I_c (R_E + h_{ib})} = \frac{h_{fb} R_E}{(R_E + h_{ib})}$$

$$\Rightarrow A_i \approx \frac{h_{fb} R_E}{R_E} \approx h_{fb}$$

Hybrid Equivalent Model

- Effect of h_r & h_{oe} also taken into account.
- Consider the general configuration

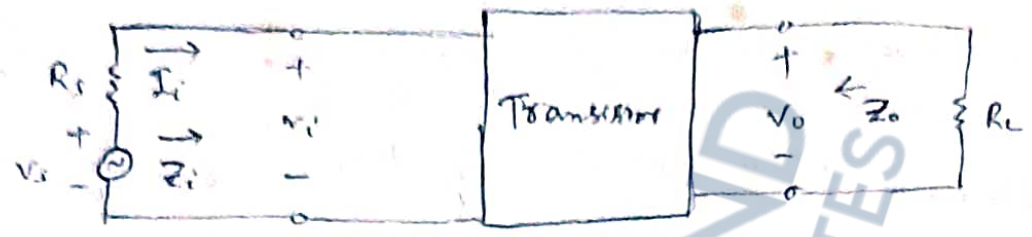


Fig. - Two-port System

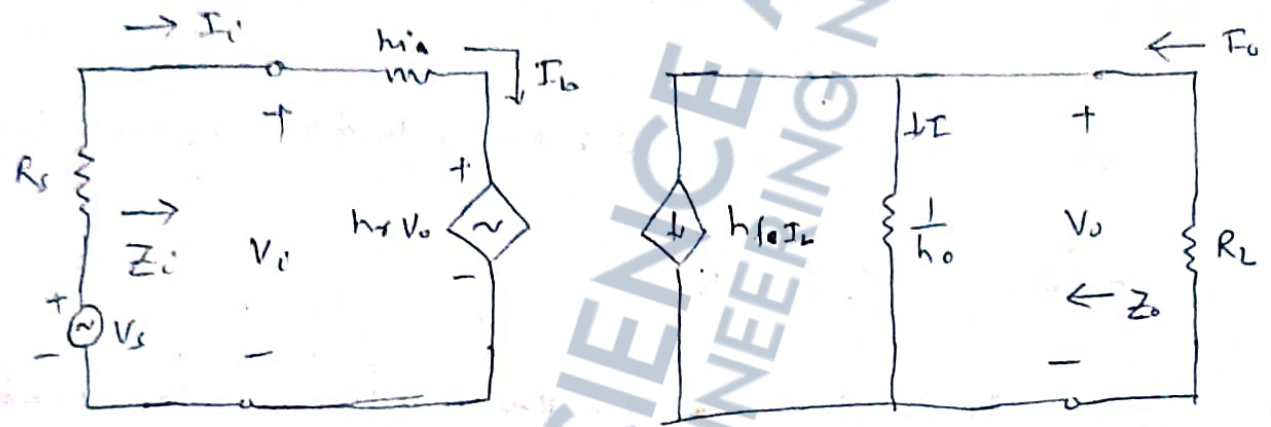


Fig :- Substituting the complete hybrid equivalent circuit into the two-port system.

Current gain :- $(A_i = \frac{I_o}{I_i})$

Applying KCL to the O/P Ckt

$$I_o = h_f I_i + I = h_f I_i + \frac{V_o}{(1/h_o)} = h_f I_i + h_o V_o$$

Substituting $V_o = -I_o R_L$ in the above equation,

$$\Rightarrow I_o = h_f I_i + h_o (-I_o R_L)$$

$$\Rightarrow I_o (1 + h_o R_L) = h_f I_i$$

$$\Rightarrow \frac{I_o}{I_c} = \frac{h_f}{1+h_o R_L} \quad \text{--- (1)}$$

$$\Rightarrow \boxed{A_c = \frac{h_f}{1+h_o R_L}}$$

Note:- if $h_o R_L \ll 1$, $\boxed{A_c = h_f}$ (Approximate model)
CB

Voltage gain ($A_v = \frac{V_o}{V_c}$)

KVL on the i/p ckt

$$V_c - I_c h_i - h_r V_o = 0$$

$$\Rightarrow V_c = I_c h_i + h_r V_o \quad \text{--- (2)}$$

From eqn (1), $I_c = \frac{I_o (1+h_o R_L)}{h_f}$
(Given above)

Putting I_c value, in eqn (2), we have

$$\Rightarrow V_c = \frac{I_o (1+h_o R_L) h_i + h_r V_o}{h_f} \quad \text{--- (3)}$$

Again $I_o = \frac{-V_o}{R_L}$

$$\therefore V_c = \frac{-V_o}{R_L} \cdot \frac{(1+h_o R_L) h_i + h_r V_o}{h_f} = V_o \left(h_r - \frac{(1+h_o R_L) h_i}{R_L h_f} \right)$$

$$\Rightarrow \frac{V_c}{V_o} = \frac{h_r \cdot R_L h_f - h_i - h_o R_L h_i}{R_L h_f} = \frac{-h_i - R_L (h_o h_i - h_r h_f)}{R_L h_f}$$

$$\Rightarrow \frac{V_o}{V_i} = \frac{-h_f R_L}{h_i + (h_i h_o - h_f h_r) R_L}$$

$$\Rightarrow \boxed{A_v = \frac{-h_f R_L}{h_i + (h_i h_o - h_f h_r) R_L}}$$

Note :- If $(h_i h_o - h_f h_r) R_L \ll h_i$, $A_v = \frac{-h_f R_L}{h_r}$

Input Impedance (Z_i) = V_i / I_i

Applying KVL in the T/P ckt,

$$V_i = h_i I_i + h_r V_o \quad \text{--- (A)}$$

Substituting $V_o = -I_o R_L$, we have

$$V_i = h_i I_i - h_r (I_o R_L) \quad \text{--- (B)}$$

But $A_i = \frac{I_o}{I_i} \Rightarrow I_o = A_i I_i$

$$\Rightarrow V_i = h_i I_i - h_r (A_i I_i) R_L$$

$$\Rightarrow V_i = I_i (h_i - h_r A_i R_L)$$

$$\Rightarrow \frac{V_i}{I_i} = h_i - h_r A_i R_L$$

But $A_i = \frac{h_f}{h_o R_L}$

$$\Rightarrow Z_i = \frac{V_i}{I_i} = h_i + h_r \left(\frac{h_f}{1+h_o R_L} \right) R_L$$

$$\Rightarrow \frac{V_i}{I_i} = h_i + h_r \left(\frac{h_f}{1+h_o R_L} \right) R_L$$

$$\Rightarrow Z_i = h_i + \frac{h_r h_f R_L}{1+h_o R_L}$$

Note: If $h_o R_L \ll 1$, $Z_i \approx h_i$

O/P Impedance Z_o (V_o/I_o)

O/P Impedance of an amplifier is defined to be ratio of o/p voltage to o/p current with signal V_s set to zero. For o/p ckt with $V_s = 0$, [Short circuited]

$$-I_i R_s - I_i h_i - h_r V_o = 0$$

$$\Rightarrow I_i (R_s + h_i) = -h_r V_o$$

$$\Rightarrow I_i = \frac{-h_r V_o}{R_s + h_i} \quad \text{--- (A')}$$

KCL in o/p load

$$I_o = h_f I_i + I = h_f I_i + \frac{V_o}{R_L} = h_f I_i + h_o V_o$$

Putting (A'), I_i value from eqn (A'),

$$I_o = h_f \left(\frac{-h_r V_o}{R_s + h_i} \right) + h_o V_o = V_o \left(h_o - \frac{h_r h_f}{h_i + R_s} \right)$$

$$\Rightarrow \frac{I_o}{V_o} = \left[h_o - \frac{h_r h_f}{h_i + R_s} \right] \frac{1}{h_i + R_s}$$

$$\Rightarrow \frac{V_o}{I_o} = \frac{1}{h_o - \frac{h_r h_f}{h_i + R_s}}$$

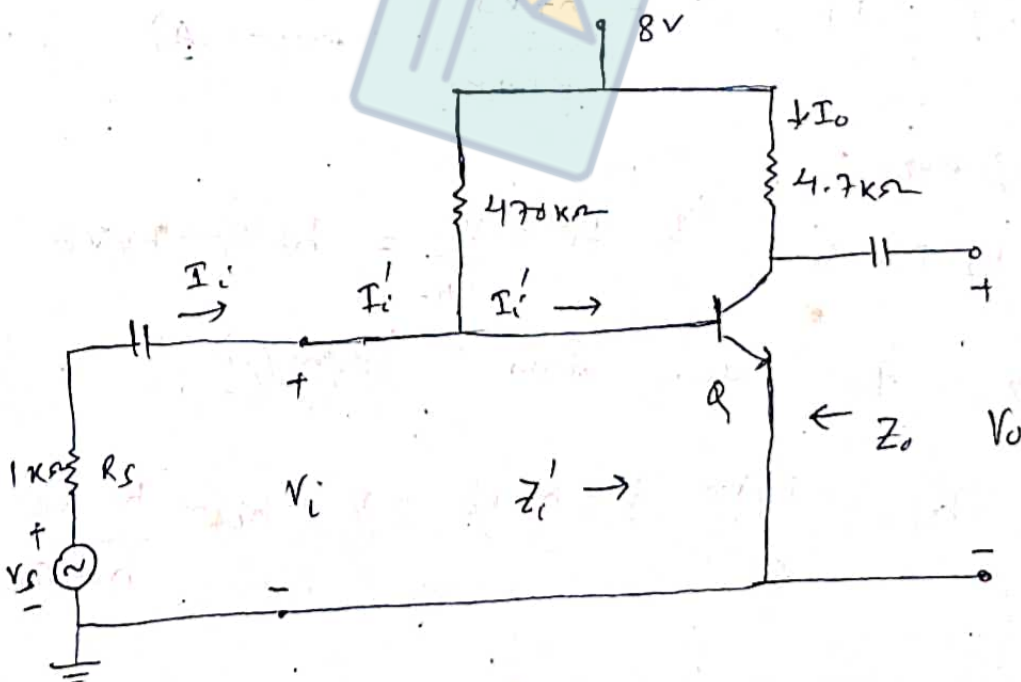
$$\Rightarrow \frac{V_o}{I_o} = \frac{1}{h_o - \frac{h_r h_f}{h_i + R_s}}$$

$$\Rightarrow Z_o = \frac{1}{\left[h_o - \left[\frac{h_r h_f}{h_i + R_s} \right] \right]}$$

Note: - $Z_o = \frac{1}{h_o}$ when $\frac{h_r h_f}{h_i + R_s} \ll h_o$

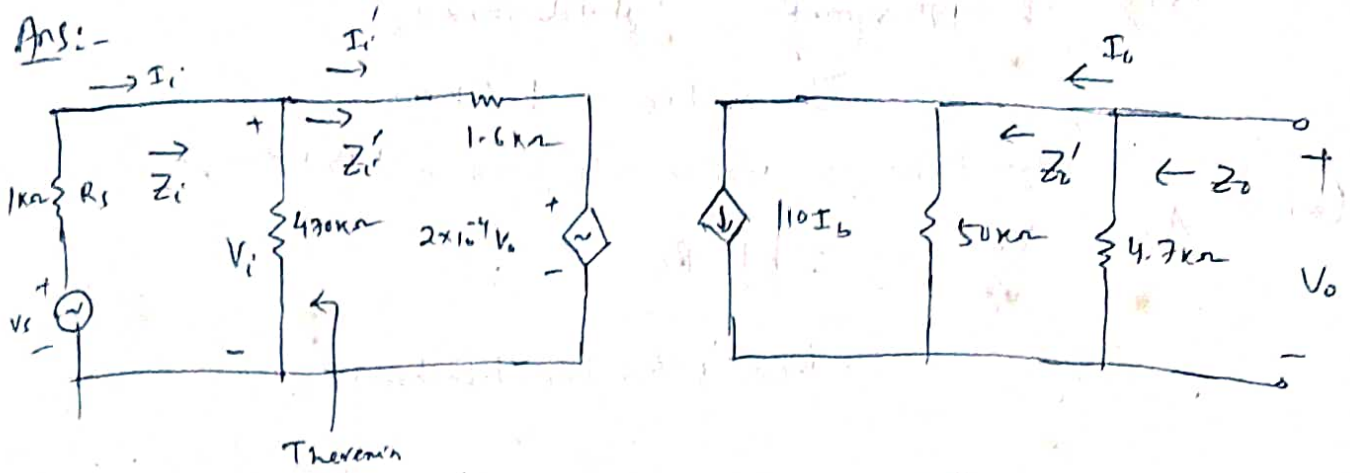
EX-20 :- Determine the following parameters using complete hybrid equivalent model.

- (a) Z_i & Z_i'
- (b) A_v
- (c) $A_i = \frac{I_o}{I_i}$
- (d) Z_o' (within R_c) and Z_o (including R_c)



Given
 $h_{fe} = 110$
 $h_{re} = 1.6\text{ k}\Omega$
 $h_{se} = 2 \times 10^7$
 $h_{oe} = 20\text{ }\mu\text{A/V}$

Ans:-

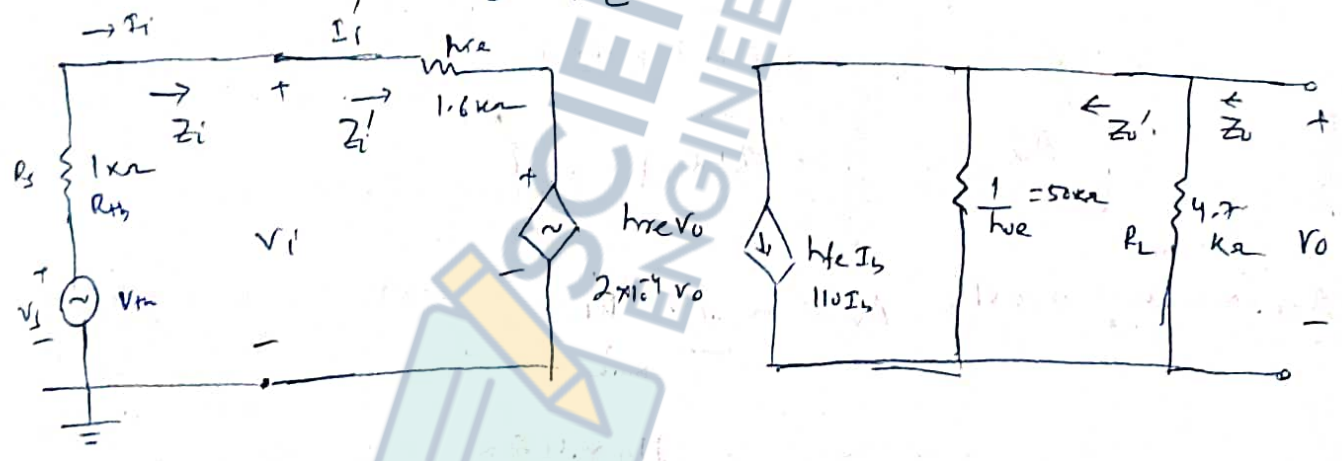


To apply the formulas, we have derived for generalized complete hybrid model, the ckt need to be converted to 'complete hybrid equivalent ckt model'.

For that $E_{Th} \approx V_s$ [Open ckt voltage] $R_{Th} \rightarrow$ Thevenin

$R_{Th} = R_s // 470k\Omega \approx R_s$

$R_L = R_C$



(a) $Z_i' = \frac{V_i'}{I_i'} = h_{ie} - \frac{h_{fe} h_{re} R_L}{1 + h_{ve} R_L}$

$= 1.6k\Omega - \frac{(110) \times (2 \times 10^{-4}) \times (4.7k\Omega)}{1 + 20 \times 10^{-6} \times 4.7k\Omega}$

$\approx 1.6k\Omega - 94.51\Omega$

$Z_i' \approx 1.51k\Omega$

Using an approximate hybrid model,

$$Z_i \approx h_{ie} = 1.6 \text{ k}\Omega$$

$$Z_i = 470 \text{ k}\Omega \parallel Z_i' = 470 \text{ k}\Omega \parallel 1.6 \text{ k}\Omega = 1.51 \text{ k}\Omega \approx Z_i'$$

$$(b) \quad A_v = \frac{V_o}{V_i} = \frac{-h_{fe} R_L}{h_{ie} + (h_{ie} h_{oe} - h_{fe} h_{re}) R_L}$$

$$= \frac{-110 \times 4.7 \text{ k}\Omega}{1.6 \text{ k}\Omega + [1.6 \text{ k}\Omega \cdot 20 \times 10^{-6} - 110 \times 2 \times 10^{-7}] 4.7 \times 10^3}$$

$$= \frac{-517 \times 10^3}{1.6 \times 10^3 + [0.032 - 0.0220] \times 4.7 \times 10^3}$$

$$= \frac{-517 \times 10^3}{1.6 \times 10^3 + 470}$$

$$A_v = -313.9$$

Approximate model

$$A_v \approx -\frac{h_{fe} R_L}{h_{ie}}$$

$$\approx -\frac{110 \times 4.7 \text{ k}\Omega}{1.6 \text{ k}\Omega}$$

$$A_v = -323.125$$

$$(c) \quad A_{v_i}' = \frac{I_o}{I_i'} = \frac{h_{fe}}{1 + h_{oe} R_L} = \frac{110}{1 + 20 \times 10^{-6} \times 4.7 \times 10^3}$$

$$= \frac{110}{1.094} = 100.55$$

Approximate model, $A_v = h_{fe} = 110$

Since $470k\Omega \gg Z_i'$, $I_i \approx I_i'$

$$A_v \approx A_v' = 100 - 55$$

$$\begin{aligned}
 \text{cd) } Z_o' &= \frac{V_o}{I_o} = \frac{1}{h_{oe} - [h_{fe} h_{re} / (h_{ie} + R_s)]} \\
 &= \frac{1}{20 \times 10^{-6} - [110 \times 2 \times 10^{-4} / (1.6k\Omega + 1k\Omega)]} \\
 &= \frac{1}{20 \times 10^{-6} - 8.46 \times 10^{-6}} \\
 &= \frac{1}{11.54 \times 10^{-6}}
 \end{aligned}$$

$$Z_o' = 86.66k\Omega$$

Approximate model, $Z_i' = \frac{1}{h_{ie}} = 50k\Omega$

$$Z_o = Z_o' \parallel R_c = 86.66k\Omega \parallel 4.7k\Omega$$

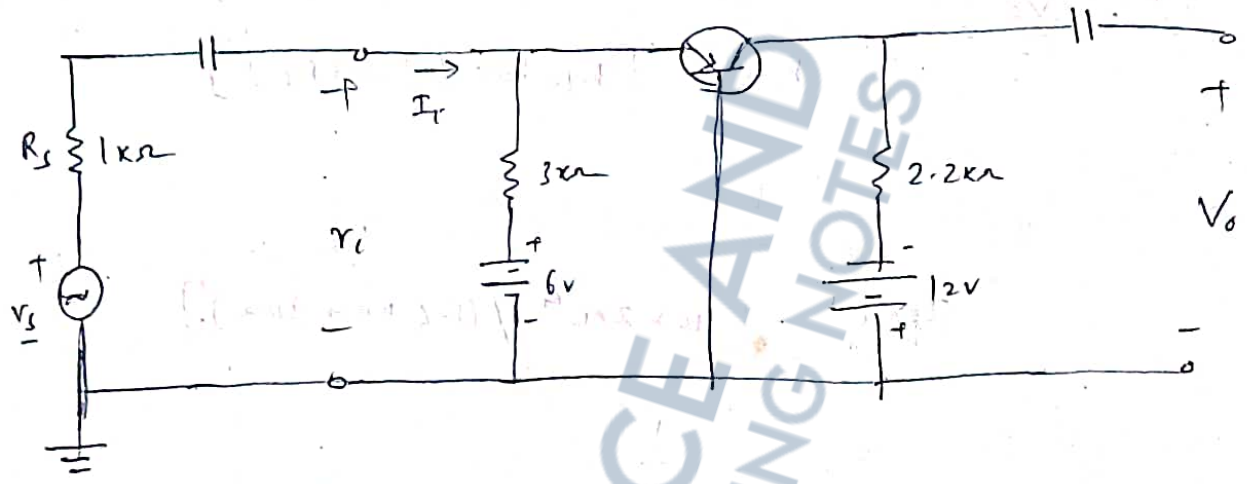
$Z_o = 4.46k\Omega$

Approximate model

$$Z_o = R_c = 4.7k\Omega$$

Ex-21 :- For the Common base amplifier, determine the following parameters using complete hybrid equivalent model & compare the results to those obtained using the approximate model.

- (a) Z_i , A_i , A_v (d) Z_o



Given

$h_{ie} = 1.6 k\Omega$, $h_{fe} = 110$
 $h_{re} = 2 \times 10^{-4}$, $h_{oe} = 20 \mu S$

Note :- Typical Parameter values of CE, CC, CB Transistor Configurations

Parameter	CE	CC	CB
h_i	$1 k\Omega$	$1 k\Omega$	20Ω
h_r	2.5×10^{-4}	≈ 1	3.0×10^{-4}
h_f	50	-50	-0.98
h_o	$25 \frac{mA}{V}$	$25 \frac{mA}{V}$	$0.5 \frac{mA}{V}$
$1/h_o$	$40 k\Omega$	$40 k\Omega$	$2 M\Omega$

Approximate Conversion Equations:

CE \leftrightarrow CB

$$h_{ie} \approx \frac{h_{ib}}{1+h_{ib}} \approx \beta r_e$$

$$h_{re} \approx \frac{h_{ib} h_{ob}}{1+h_{ib}} - h_{rb}$$

$$h_{fe} \approx \frac{-h_{fb}}{1+h_{fb}} \approx \beta$$

$$h_{oe} = \frac{h_{ob}}{1+h_{fb}}$$

CB \leftrightarrow CE \leftrightarrow CC

$$h_{ib} \approx \frac{h_{ie}}{1+h_{fe}} \approx \frac{-h_{ic}}{h_{fc}} \approx r_e$$

$$h_{rb} = \frac{h_{re} h_{oe}}{1+h_{fe}} - h_{re} \approx h_{re} - 1 - \frac{h_{ic} h_{oc}}{h_{fc}}$$

$$h_{fb} = \frac{-h_{fe}}{1+h_{fe}} \approx \frac{-(1+h_{fc})}{h_{fc}} \approx -\alpha$$

$$h_{ob} \approx \frac{h_{oe}}{1+h_{fe}} \approx -\frac{h_{oc}}{h_{fc}}$$

CC

$$h_{rc} \approx \frac{h_{ib}}{1+h_{fb}} \approx \beta r_e$$

$$h_{re} \approx 1$$

$$h_{fc} \approx \frac{-1}{1+h_{fb}} \approx -\beta$$

$$h_{oc} = \frac{h_{ob}}{1+h_{fb}}$$

Soln :- The h-parameters are given for 'CE' configuration, for 'CB' we have to convert it using the conversion formula given on last page.

$$h_{ib} = \frac{h_{ie}}{1+h_{fe}} = \frac{1.6 \text{ k}\Omega}{1+110} = 14.41 \Omega$$

or By approximate hybrid model

$$h_{ib} = r_e = \frac{h_{ie}}{\beta} = \frac{1.6 \times 10^3}{110} \approx \frac{1.6 \times 10^3}{110} = 14.55 \Omega$$

$$h_{rb} = \frac{h_{ie}h_{oe} - h_{re}}{1+h_{fe}} = \frac{1.6 \times 10^3 \cdot 20 \times 10^{-6} - 2 \times 10^{-4}}{1+110}$$

$$= \frac{2.883 \times 10^{-4} - 2 \times 10^{-4}}{1+110}$$

$$h_{rb} = 0.883 \times 10^{-4} \checkmark$$

$$h_{fb} = \frac{-h_{fe}}{1+h_{fe}} = \frac{-110}{1+110} = -0.991 \checkmark$$

$$h_{ob} = \frac{h_{oe}}{1+h_{fe}} = \frac{20 \times 10^{-6}}{1+110} = 0.18 \text{ M}\Omega \checkmark$$

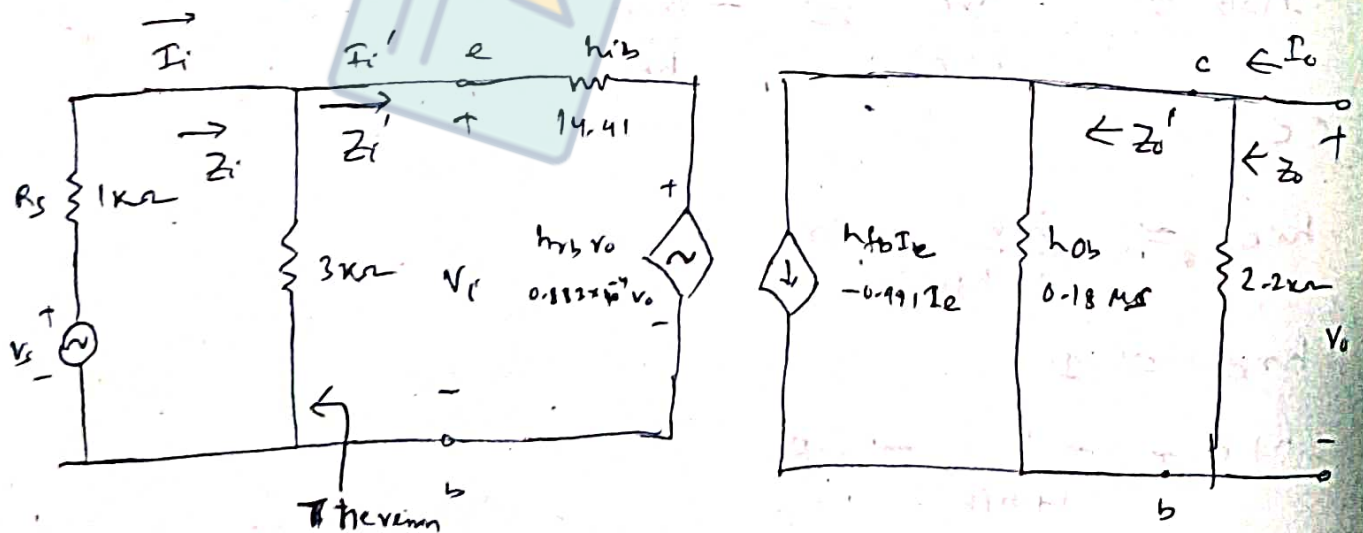


Fig:- CB hybrid equivalent ckt

~~P_{Th}~~ The Thevenin equivalent n/w for

np ckt results in $R_{Th} = 3k\Omega \parallel 1k\Omega = \underline{0.75k\Omega}$

$$\begin{aligned} (a) \underline{Z_i'} &= \frac{v_i'}{I_i'} = \frac{h_{ib} - \frac{h_{fb} h_{rb} R_L}{1 + h_{ob} R_L}}{1 + h_{ob} R_L} \\ &= \frac{14.41 - (-0.991) \cdot (0.883 \times 10^{-4}) \times 2.2 \times 10^3}{1 + 0.18 \times 10^{-6} \times 2.2 \times 10^3} \\ &= \left[14.41 + \frac{19.25}{1.000398} \right] \\ Z_i' &= 14.60 \Omega \end{aligned}$$

Note: - Approximate model, $Z_i' = h_{ib} = 14.41 \Omega$

$$\text{and } Z_i = Z_i' \parallel 3k\Omega = 14.60 \parallel 3k\Omega \approx 14.60 \Omega$$

$$(b) \underline{A_i'} = \frac{I_o}{I_i'} = \frac{h_{fb}}{1 + h_{ob} R_L} = \frac{-0.991}{1 + 0.18 \times 10^{-6} \times 2.2 \times 10^3} = -0.991$$

Because, $3k\Omega \gg Z_i'$ i.e. $3k\Omega \gg 14.60\Omega$,

$$I_i' \approx I_i, \quad A_i' = \frac{I_o}{I_i} \approx -1$$

$$\begin{aligned} (c) \underline{A_v} &= \frac{v_o}{v_i} = \frac{-h_{fb} R_L}{h_{ib} + (h_{ib} h_{ob} - h_{fb} h_{rb}) R_L} \\ &= \frac{-(-0.991) \times 2.2 \times 10^3}{14.41 + [14.41 \times 0.18 \times 10^{-6} - (-0.991) \cdot 0.883 \times 10^{-4}] 2.2 \times 10^3} \end{aligned}$$

$$\therefore A_v = 149.25$$

By approximate model

$$A_v = \frac{-h_{fb} R_L}{h_{ib}} = \frac{-(-0.991) \times 2.2 \times 10^3}{14.41}$$

$$A_v = 157.3$$

$$(a) Z_o' = \frac{1}{h_{ob} - \left[\frac{h_{fb} h_{rb}}{h_{ib} + R_s} \right]}$$

$$= \frac{1}{0.18 \times 10^{-6} - \left[\frac{(-0.991) \cdot (0.883 \times 10^{-4})}{(14.41 + 0.75 \times 10^3)} \right]}$$

NOTE: $R_s = 0.75 \times 10^3$ (Rth.) [1kΩ // 3kΩ]

$$\Rightarrow Z_o' = 3.39 \text{ M}\Omega$$

By approximate model,

$$Z_o' = \frac{1}{h_{ob}} = \frac{1}{0.18 \times 10^{-6}} = 5.56 \text{ M}\Omega$$

$$Z_o = R_c \parallel Z_o' = 2.2 \text{ k}\Omega \parallel 3.39 \text{ M}\Omega = \underline{2.199 \text{ k}\Omega}$$

versus

~~the~~

$$Z_o \approx R_c = \underline{2.2 \text{ k}\Omega}$$

Graphical Determination of Hybrid Parameters

Using Partial derivatives, magnitude of h-Parameters can be found using the following equations,

$$h_{ie} = \frac{\partial v_i}{\partial i_b} = \frac{\partial V_{be}}{\partial i_b} \approx \frac{\Delta V_{be}}{\Delta i_b} \Big|_{V_{ce} = \text{constant}} \quad (\text{Ohms})$$

$$h_{re} = \frac{\partial v_i}{\partial v_o} = \frac{\partial V_{be}}{\partial V_{ce}} \approx \frac{\Delta V_{be}}{\Delta V_{ce}} \Big|_{I_b = \text{constant}} \quad (\text{unitless})$$

[NOTE: -

$$V_i = h_{11} I_i + h_{12} V_o$$

$$I_o = h_{21} I_i + h_{22} V_o$$

v.e

$$V_{be} = h_{ie} I_b + h_{re} V_{ce}$$

$$I_e = h_{fe} I_b + h_{oe} V_{ce} \quad]$$

$$h_{fe} = \frac{\partial i_o}{\partial i_i} = \frac{\partial i_c}{\partial i_b} \approx \frac{\Delta i_c}{\Delta i_b} \Big|_{V_{ce} = \text{constant}} \quad (\text{unitless})$$

$$h_{oe} = \frac{\partial i_o}{\partial v_o} = \frac{\partial i_c}{\partial V_{ce}} \approx \frac{\Delta i_c}{\Delta V_{ce}} \Big|_{I_b = \text{constant}} \quad (\text{Siemens})$$

→ Δ refers to a small change in that quantity around the quiescent point of operation

→ h_{ie} & h_{re} are determined from IP or base

Characteristics.

→ h_{fe} & h_{oe} are obtained from output or collector
Characteristics.

(A) h_{fe} determination :-

1) First find the Quiescent point of operation.

2) From the o/p characteristics V_{CE} , V_{CE} , V_{CE} , V_{CE} .

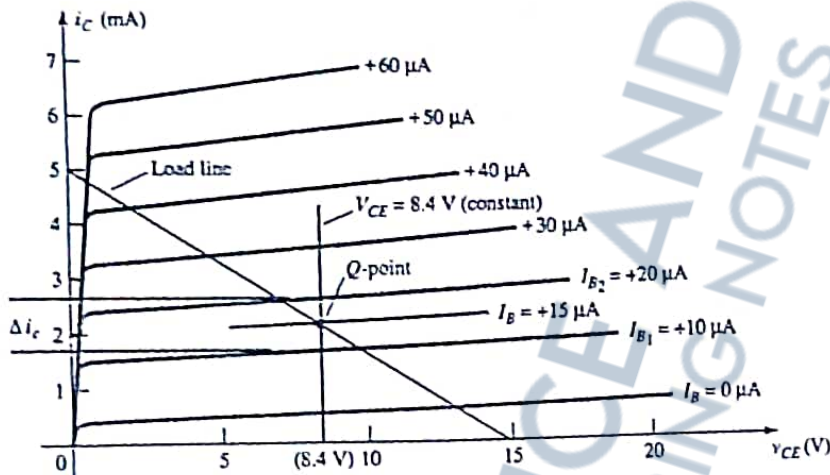


FIG. A.1
 h_{fe} determination.

fig 1: - h_{fe} determination

2/ $h_{fe} = \frac{\Delta I_C}{\Delta I_B} \Big|_{V_{CE} = \text{const}}$ — (1)

So at a constant V_{CE} (Given by Q point), changes in base current and collector current can be obtained by the vertical line drawn through the Q-point.

3/ Eqn (1), show small change in I_C to small change in I_B , So in fig 1, the change in I_B is chosen to extend from I_{B1} to I_{B2} along the perpendicular straight line at V_{CE} . The

Corresponding change in i_c is then found by drawing the horizontal lines from the intersection of I_{B1} & I_{B2} , with $V_{CE} = \text{const}$ to vertical axis. Then substitute the value of Δi_c & Δi_b in eqn (1),

$$|h_{fe}| = \left. \frac{\Delta i_c}{\Delta i_b} \right|_{V_{CE} = \text{constant}}$$

$$= \frac{(2.7 - 1.7) \text{ mA}}{(20 - 10) \text{ } \mu\text{A}} \Big|_{V_{CE} = 8.4 \text{ V}}$$

$$= \frac{1}{10} \times 10^3$$

$$|h_{fe}| = 100$$

(B) h_{oe} determination :-

$$h_{oe} = \left. \frac{\Delta i_c}{\Delta V_{CE}} \right|_{I_B = \text{const}}$$

(i) First on the O/P Characteristics, Q point is determined.

(ii) Then a straight line is drawn tangent to the curve I_B through the Q-point to establish a line $I_B = \text{constant}$.

(iii) A change in V_{CE} was then chosen, and the corresponding change in i_c determined by drawing the horizontal lines to the vertical

axis at the intersection on the $I_B = \text{constant}$, 273 line.

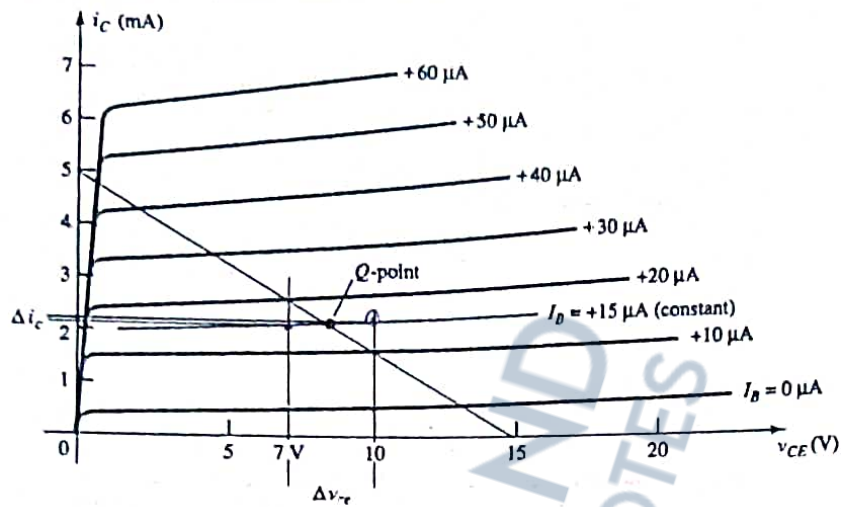


FIG. A.2
 h_{oe} determination.

$$\therefore |h_{oe}| = \left. \frac{\Delta i_c}{\Delta V_{CE}} \right|_{I_B = \text{const}} = \left. \frac{(2.2 - 2.1) \text{ mA}}{(10 - 7) \text{ V}} \right|_{I_B = 15 \mu A}$$

$$= \frac{0.1 \times 10^{-3}}{3} = 33 \frac{\mu A}{V}$$

$$h_{oe} = 33 \mu S$$

(c) h_{ie} determination

(i) To the Q point must be first found on the I_B or base characteristic curve [For Q point, we must have a particular I_B]

$$(ii) h_{ie} = \left. \frac{\Delta V_{be}}{\Delta i_b} \right|_{CE = \text{const}}$$

(iii) At $V_{CE} = 8.4$ V, a line is drawn tangent to the curve

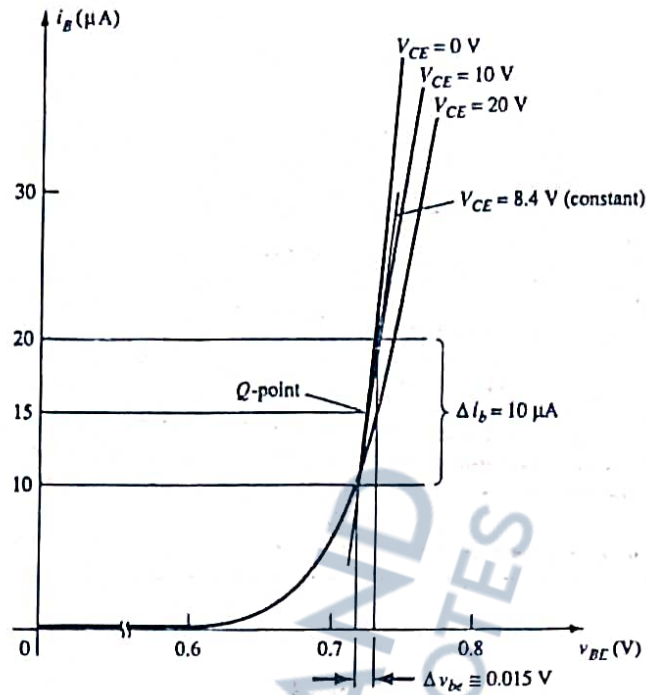


FIG. A.3
 h_{ie} determination.

to the curve through the Q-point to establish a line $V_{CE} = \text{const}$.
 (iii) A small change in V_{be} is then chosen, resulting in a corresponding change in i_b .

$$\therefore |h_{ie}| = \left. \frac{\Delta V_{be}}{\Delta i_b} \right|_{V_{CE} = \text{const}} = \left. \frac{(733 - 718) \text{ mV}}{(20 - 10) \mu\text{A}} \right|_{V_{CE} = 8.4 \text{ V}}$$

$$= \frac{15 \times 10^{-3}}{10 \times 10^{-6}}$$

$h_{ie} = 1.5 \text{ k}\Omega$

(D) h_{re} determination:

(i) First Q-point is found out in r/p characteristics. [From a particular I_B corresponds to Q-point.]

(ii) $h_{re} = \left. \frac{\Delta V_{be}}{\Delta V_{ce}} \right|_{I_B = \text{constant}}$

(ii) A horizontal line is drawn through the Q-point at $I_B = 15 \mu A$.

(iii) Then where ever the line cuts the corresponding V_{CE} curves, change in V_{CE} & change in V_{BE} is determined.

$$\begin{aligned}
 (iv) \quad |h_{re}| &= \left. \frac{\Delta V_{be}}{\Delta V_{ce}} \right|_{I_B = \text{constant}} \\
 &= \frac{(733 - 725) \text{ mV}}{(20 - 0) \text{ V}} \\
 &= \frac{8 \times 10^{-3}}{20} \\
 |h_{re}| &= 4 \times 10^{-4}
 \end{aligned}$$

~~The~~

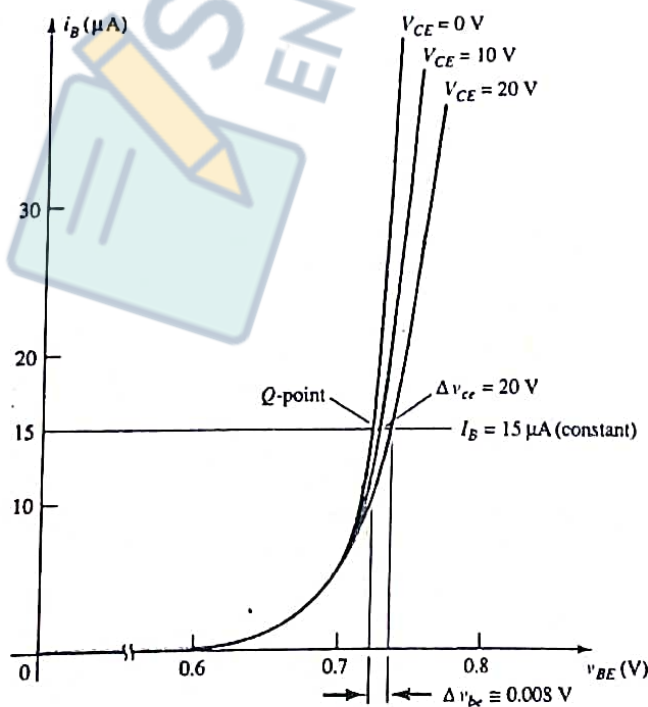


FIG. A.4
 h_{re} determination.

The resultant ckt,

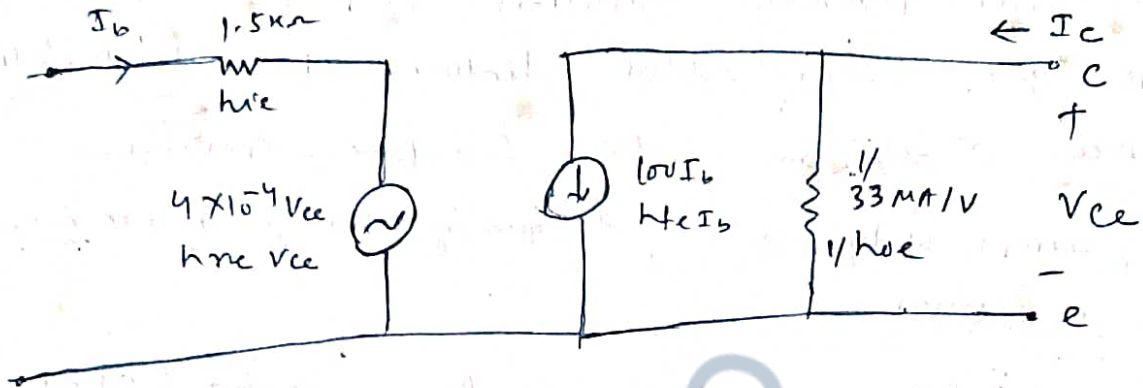


fig: 5:- Complete hybrid equivalent ckt for a transistor having characteristics appears in fig 1-4.

