

Biasing of FET & MOSFET

In case of BJT, i_s is Current (I_B), whereas as for the FET, Controlling variable is the voltage ($V_{gs})$.

→ The general relationship that can be applied to d.c analysis of all FET amplifiers are

$$I_G \approx 0 \text{ Amp.}$$

$$I_D = I_S$$

→ For JFET and depletion type MOSFETS, Shockey's eqⁿ is applied to relate the r/p and other quantities.

$$I_D = I_{DSS} \left(1 - \frac{V_{gs}}{V_P} \right)^2$$

→ For enhancement type MOSFET, the following eqⁿ is applicable

$$I_D = K (V_{gs} - V_T)^2$$

Fixed-Bias Configuration:-

The simplest of biasing arrangement for n-channel JFET appears below.

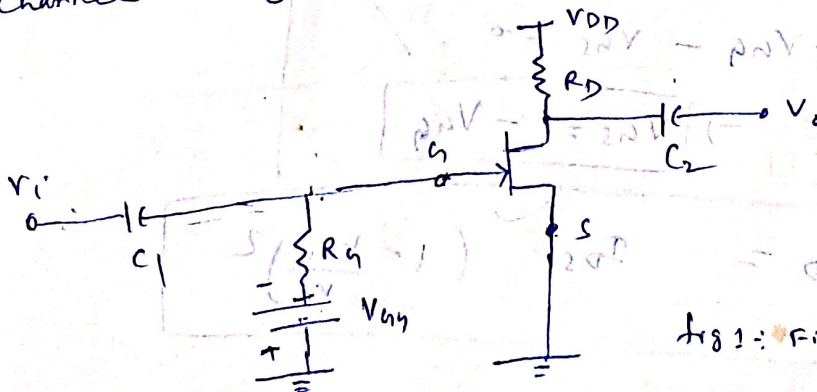


Fig 1: Fixed bias Configuration

It can be solved using mathematical or graphical approach.

For d.c analysis,

Capacitors C_1 & C_2 are open circuited. $V_i = 0$.

The resistor R_g is present to ensure V_i appears at the i/p to FET amplifier.

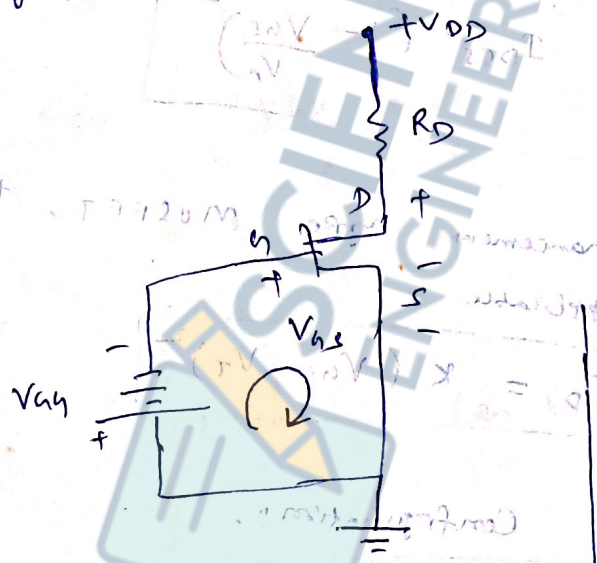
But for d.c analysis, $V_i = 0$.

$$I_{R_g} = 0$$

$$V_{R_g} = I_{R_g} R_g = 0 \text{ V}$$

The zero-voltage drop across R_g permits replacing R_g by a short-circuit equivalent.

So fig(2) can be redrawn as shown below.



Since V_{g1} is a fixed dc supply, V_{gs} is fixed in magnitude, resulting in the designation

'fixed-bias configuration'

Applying KVL,

$$-V_{g1} - V_{gs} = 0$$

$$\Rightarrow V_{gs} = -V_{g1}$$

$$I_D = I_{DSS} \left(1 - \frac{V_{gs}}{V_P} \right)^2$$

Graphical method

If I_{DSS} , V_p is given for a FET

For $V_{GS} = \frac{V_p}{2}$, $I_D = I_{DSS} \left(1 - \frac{V_p/2}{V_p}\right)^2$
 $= I_{DSS} \left(1 - \frac{1}{2}\right)^2$

$I_D = \frac{I_{DSS}}{4}$

So the point is $\left(\frac{V_p}{2}, \frac{I_{DSS}}{4}\right)$.

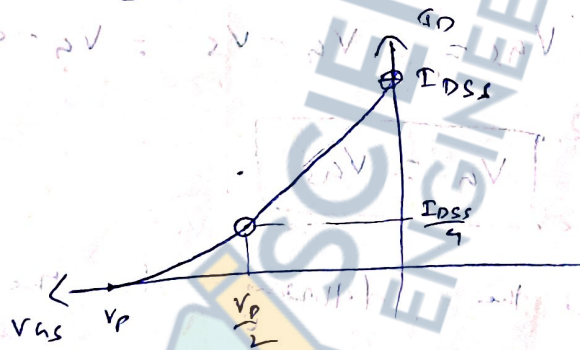
So we have 2 points: (I_{DSS}, V_p)

$(0, I_{DSS})$

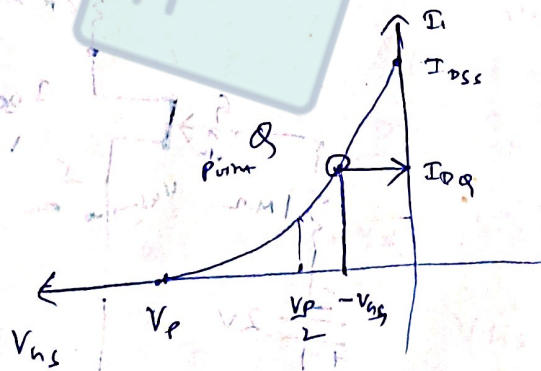
$(V_p, 0)$

$\left(\frac{V_p}{2}, \frac{I_{DSS}}{4}\right)$

We can plot the graph.



Then at $V_{GS} = -V_{GS}$, we can get the I_D .



$V_{GS} = \text{const}$
 $n = \text{const}$
 i.e. line parallel to y-axis

Intersection of 2 lines gives the soln of 2 unknown V_{GS} & I_D .

→ Then drain-to-source voltage of the o/p section can be determined by applying Kirchhoff's Voltage law.

$$V_{DD} - I_D R_D - V_{DS} = 0$$

$$\Rightarrow \boxed{V_{DS} = V_{DD} - I_D R_D}$$

$V_S =$ Source voltage with ground $= 0$

$$\boxed{V_S = 0}$$

$$V_{DS} = V_D - V_S = V_D - 0$$

$$\Rightarrow \boxed{V_D = V_{DS}}$$

Similarly, $V_{GS} = V_G - V_S = V_G - 0$

$$\Rightarrow \boxed{V_G = V_{GS}}$$

Ex:-1) Determine the following for the n/w

(a) V_{GS}

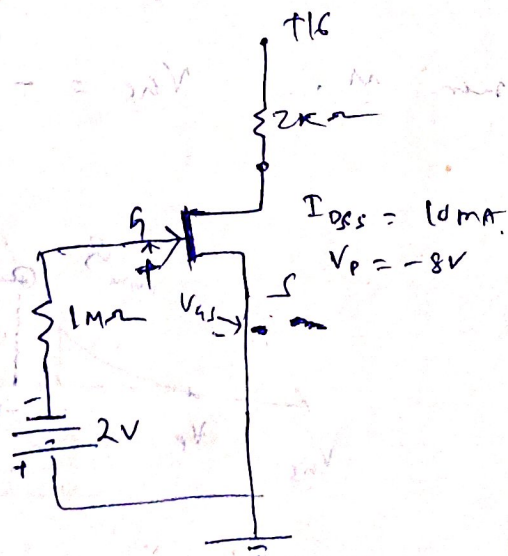
(b) I_{DQ}

(c) V_{DS}

(d) V_D

(e) V_G

(f) V_S



(g) Mathematical approach :-

Applying KVL on the o/p loop,

$$-2 - V_{GS} = 0$$

$$(a) \Rightarrow \boxed{V_{GS} = -2V}$$

$$I_{DQ} = (I_{DSS}) \left(1 - \frac{V_{GS}}{V_P} \right)^2$$

$$= (10 \times 10^{-3}) \left(1 - \frac{(-2)}{(-8)} \right)^2$$

$$= 10 \times 10^{-3} \left(\frac{3}{4} \right)^2$$

$$= \frac{10 \times 10^{-3} \times 9}{16}$$

$$= \frac{45}{8} \text{ mA}$$

$$(b) \boxed{I_{DQ} = 5.625 \text{ mA}}$$

(c)

$$V_{DS} = V_{DD} - I_D R_D$$

$$= 16 - 5.625 \times 10^{-3} \times 2 \times 10^3$$

$$\boxed{V_{DS} = 4.75 \text{ V}}$$

(d)

$$V_{DS} = V_D - V_S = V_D - 0$$

$$\Rightarrow V_D = V_{DS} = 4.75 \text{ V}$$

$$\boxed{V_D = 4.75 \text{ V}}$$

(e)

$$V_{GS} = V_G - V_S = V_G - 0$$

$$\Rightarrow V_G = V_{GS} = -2 \text{ V}$$

$$\boxed{V_{GS} = -2 \text{ V}}$$

(f)

$$V_S = 0 \quad (\text{Grounded})$$

Graphical approach:-

Given

$I_{DSS} = 10 \text{ mA}$,

$V_P = -8 \text{ V}$.

At

$V_{GS} = \frac{V_P}{2}$,

$I_{DQ} = \frac{I_{DSS}}{4}$.

So

We have

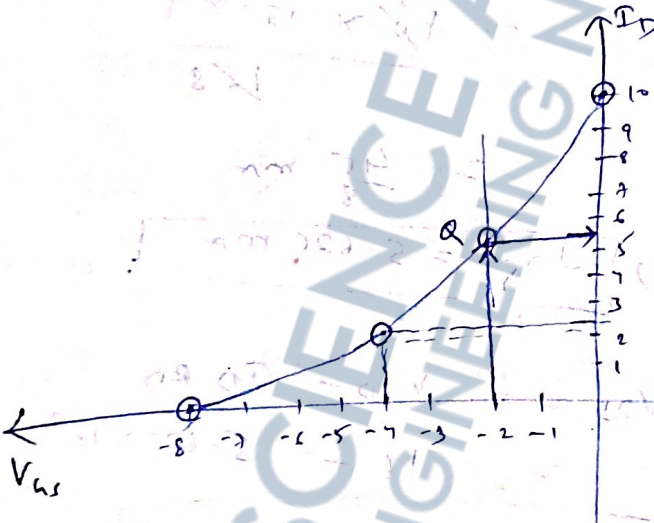
3 points,

$I_{DSS} \rightarrow (0, 10 \text{ mA})$

$V_P \rightarrow (-8, 0)$

$(\frac{V_P}{2}, \frac{I_{DSS}}{4}) \rightarrow (-4, 2.5 \text{ mA})$

So we can plot the graph



→ Then

$V_{GS} = -V_{GS} = -2 \text{ V}$

Eq of line parallel to y-axis at $x = -2$.

→ At

$V_{GS} = -2 \text{ V}$,

draw a vertical line,

where it

cuts the

graph,

draw a

horizontal

line,

it

cuts I_D i.e

I_{DQ}

ie

5.6 mA

→

(a) $V_{GS} = -2 \text{ V}$

(b) $I_{DQ} = 5.6 \text{ mA}$.

(c)

$V_{DS} = V_{DD} - I_{DQ} R_D = 16 - 5.6 \times 2 \text{ (mA)}$
 $= 4.8 \text{ V}$

(d) $V_{DS} = V_D - V_S = V_D - 0$

$\Rightarrow V_D = V_{DS} = 4.8 \text{ V}$

(e) $V_{GS} = V_G - V_S = V_G - 0$

$\Rightarrow V_G = V_{GS} = -2 \text{ V}$

(f) $V_S = 0 \text{ V}$

\therefore The mathematical and graphical approaches generate solutions that are quite close.

Self-bias Configuration :-

Self-bias Configuration eliminates the need of 2 d.c. supplies. The controlling gate-to-source voltage is now determined by the voltage across a resistor R_S introduced on the source leg of the configuration as shown below.

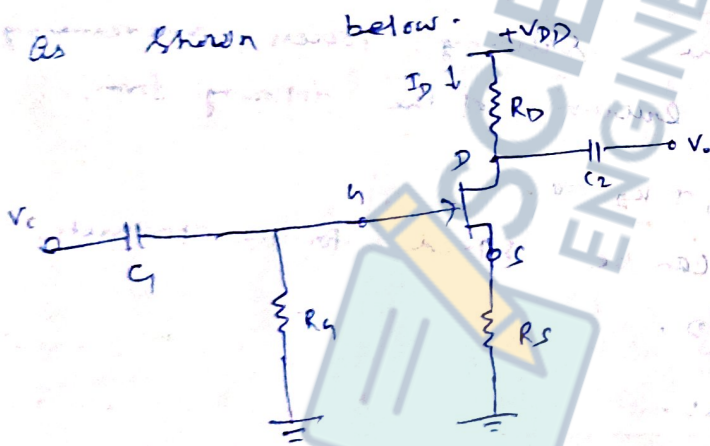


fig 11- JFET self bias configuration.

For d.c. analysis, the capacitors can again be replaced by 'open circuits' and resistor R_g replaced by short-circuit equivalent.

Since $I_g = 0 \text{ A}$.

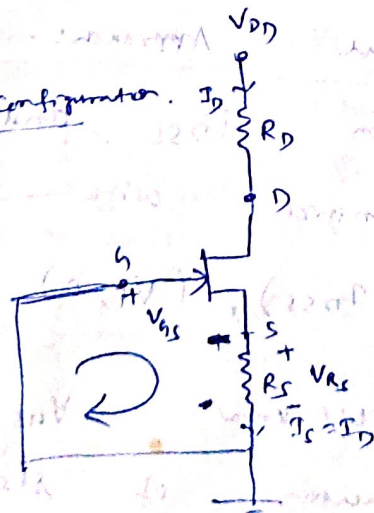


fig 12:-

D.C. analysis of the self bias configuration.

Applying KVL

$$-V_{GS} - V_{RS} = 0$$

$$\Rightarrow \boxed{V_{GS} = -V_{RS}}$$

And

$$\boxed{V_{RS} = I_D R_S}$$

$$\therefore \boxed{V_{GS} = -I_D R_S}$$

Note:- In this case V_{GS} is a function of the output current I_D and not fixed in magnitude as occurred for the fixed-bias configuration.

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2$$

$$= I_{DSS} \left(1 - \frac{-I_D R_S}{V_P}\right)^2$$

$$I_D = I_{DSS} \left(1 + \frac{I_D R_S}{V_P}\right)^2$$

By performing the following process and rearranging, we obtain an equation of the following form.

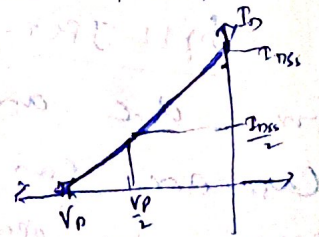
$$I_D^2 + K_1 I_D + K_2 = 0$$

Quadratic eqn can be solved for the approximate solution for I_D .

Graphical Approach:-

For given I_{DSS} and V_P , plot the transfer characteristic with 3 points.

$$(0, I_{DSS}), (V_P, 0), \left(\frac{V_P}{2}, \frac{I_{DSS}}{4}\right)$$



→ We know $V_{GS} = -I_D R_S$, which is a equation of straight line on the same graph.

→ To draw straight line we need 2 points.

First point

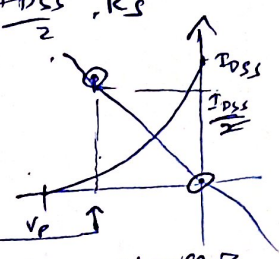
$$I_D = 0, \quad V_{GS} = 0 \cdot R_S = 0$$

$$(0, 0)$$

2nd point

$$I_D = \frac{I_{DSS}}{2}, \quad V_{GS} = -\frac{I_{DSS}}{2} \cdot R_S$$

$$\left(-\frac{I_{DSS}}{2} \cdot R_S, \frac{I_{DSS}}{2} \right)$$



The intersection of transfer characteristic and straight line gives Q point.

Applying KVL on OP loop.

$$V_{DD} - I_D R_D - V_{DS} - I_D R_S = 0 \quad (\because I_D = I_S)$$

$$\Rightarrow V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$V_S = I_D R_S$$

$$V_G = 0$$

$$V_D = V_{DD} - I_D R_D$$

Assume

$$V_{GS} = V_G - V_S$$

$$V_{GS} = 0 - V_S$$

$$\Rightarrow V_S = -V_{GS}$$

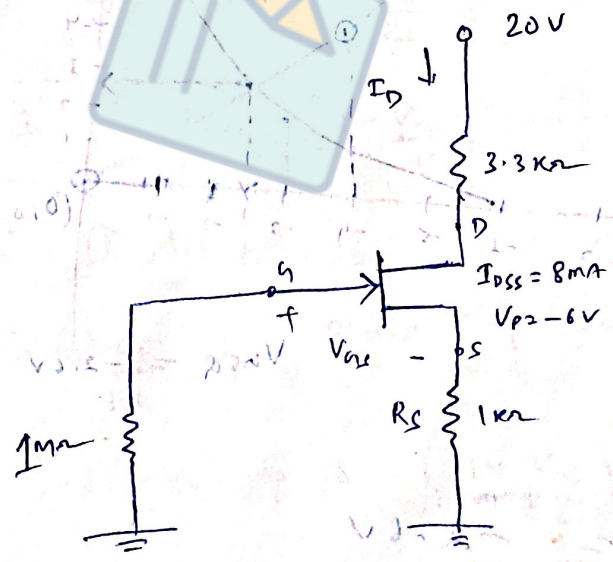
True

$$V_{GS} = -I_D R_S$$

$$V_S = +I_D R_S$$

$$\Rightarrow V_{GS} = -V_S$$

Ex-2 :-



Determine

- (a) V_{GSQ}
- (b) I_{DQ}
- (c) V_{DS}
- (d) V_S
- (e) V_G
- (f) V_D

Ans: - Graphical method: -
 $V_{GS} = -I_D R_S$

Let At $I_D = \frac{I_{DSS}}{2} = \frac{8}{2} = 4 \text{ mA}$,

$V_{GS} = -4 \times (1 \text{ k}\Omega)$

$V_{GS} = -4 \text{ V}$

on the straight line

Point $(-4, 4)$

To plot transfer characteristics: -

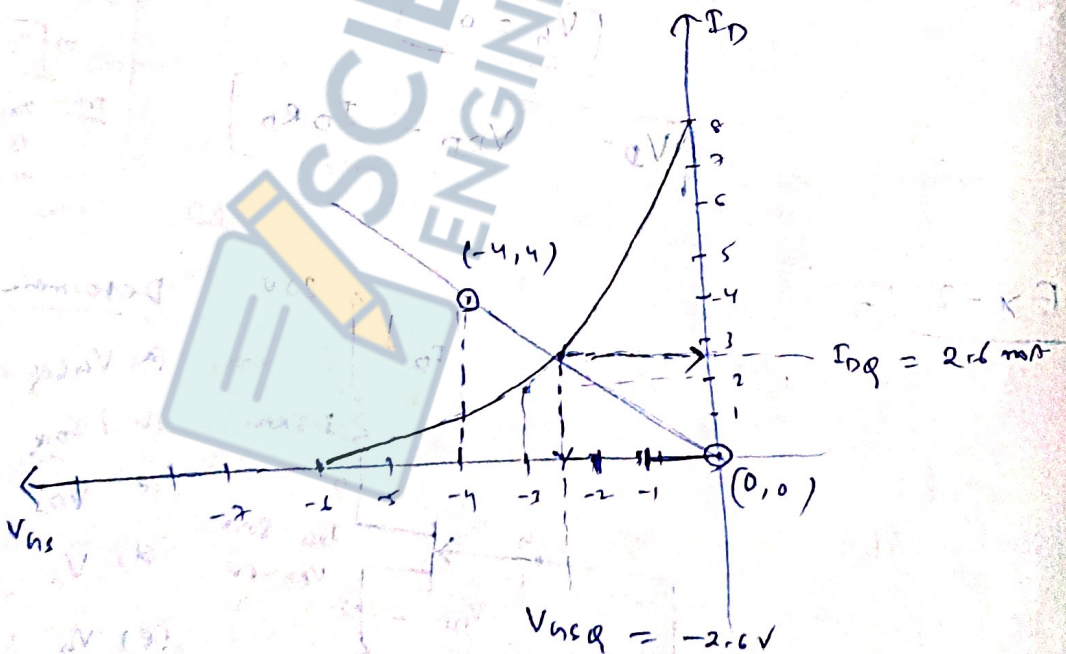
At $V_{GS} = \frac{V_P}{2}$, $I_D = \frac{I_{DSS}}{4}$

$V_{GS} = -\frac{6}{2} = -3 \text{ V}$, $I_D = \frac{8}{4} = 2 \text{ mA}$

1) $(-3, 2)$

2) $(0, I_{DSS}) = (0, 8)$

3) $(V_P, 0) = (-6, 0)$



(a) $V_{GSQ} = -2.6 \text{ V}$

(b) $I_{DQ} = 2.6 \text{ mA}$

(c)

$$V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$= 20 - 2.6 \times 10^{-3} (3.3 + 1) \times 10^3$$

$$= 20 - 2.6 \times 4.3$$

$$V_{DS} = 8.82 \text{ V}$$

(d)

$$V_S = I_D \times R_S$$

$$= 2.6 \times 10^{-3} \times 1 \times 10^3$$

$$= 2.6 \text{ V}$$

$$V_G = 0 \text{ V} \quad (\text{from } I_G = 0)$$

(e)

(f)

$$V_D = V_{DD} - I_D R_D$$

$$= 20 - 2.6 \times 10^{-3} \times 3.3 \times 10^3$$

$$= 20 - 8.58$$

$$= 11.42 \text{ V}$$

Mathematical Approach:

Approach:

$$V_{GS} = -I_D R_S$$

$$I_D = -\frac{V_{GS}}{R_S} \quad \text{--- (1)}$$

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2 \quad \text{--- (2)}$$

Solving eq (1) & (2)

$$-\frac{V_{GS}}{1k} = 8 \text{ mA} \left(1 - \frac{V_{GS}}{-6}\right)^2$$

$$\Rightarrow -V_{GS} = 8 \left(\frac{6 + V_{GS}}{6}\right)^2$$

$$= 8 \left(\frac{6 + V_{GS}}{6}\right)^2 = \frac{8}{9} (36 + V_{GS}^2 + 12V_{GS})$$

$$\Rightarrow -18 V_{GS} = 144 + 4 V_{GS}^2 + 48 V_{GS} \quad (1)$$

$$\Rightarrow 4 V_{GS}^2 + 66 V_{GS} + 144 = 0$$

$$V_{GS} = -2.58 \text{ V} \quad \text{or} \quad -13.9$$

-13.9 is not possible (greater than V_p)

$$V_{GS} = -2.58 \text{ V}$$

$$I_D = \frac{-V_{GS}}{R_S} = \frac{-(-2.58 \text{ V})}{1 \text{ k}\Omega} = +2.58 \text{ mA}$$

$$I_D = +2.58 \text{ mA}$$

$$(c) \quad V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$= 20 - 2.58 (3.3 + 1)$$

$$V_{DS} = 8.906 \text{ V}$$

$$(d) \quad V_S = I_D \times R_S = +2.58 \times 10^{-3} \times 1 \times 10^3 = 2.58 \text{ V}$$

$$(e) \quad V_G = 0 \text{ V} \quad (- \text{at } I_G = 0)$$

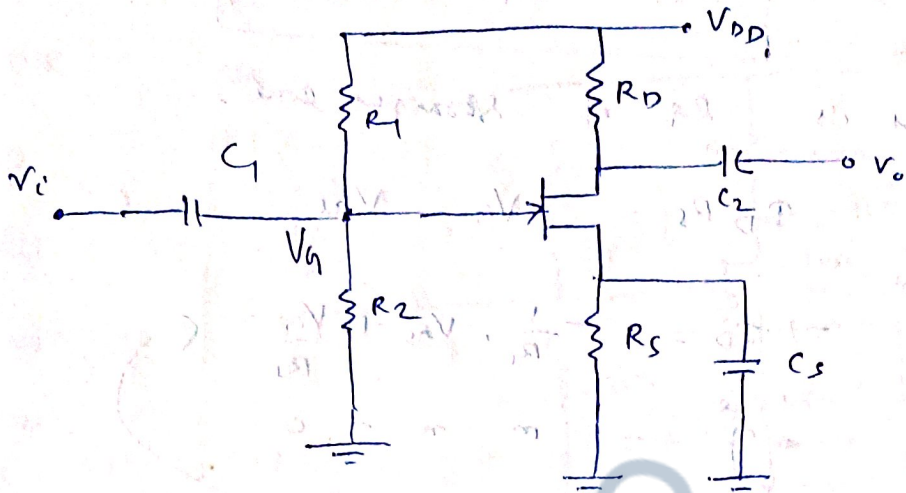
$$(f) \quad V_D = V_{DD} - I_D R_D$$

$$= 20 - 2.58 \times 10^{-3} \times 3.3 \times 10^3$$

$$= 20 - 8.514$$

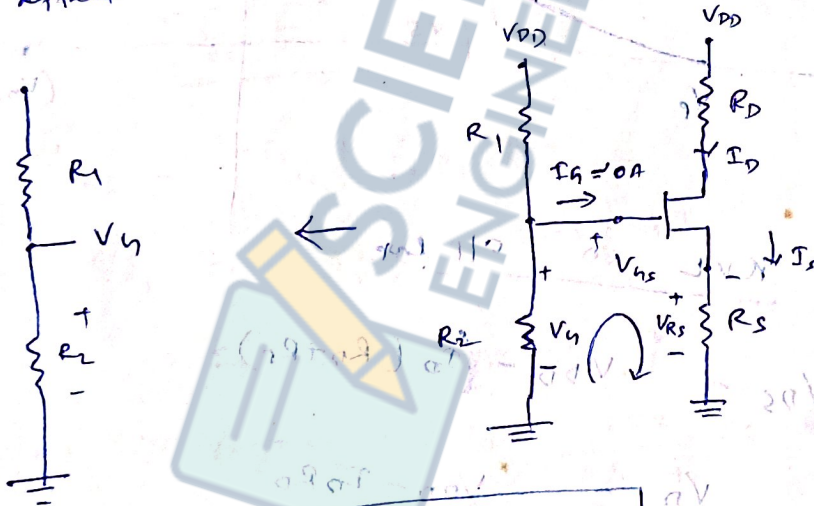
$$V_D = 11.486 \text{ V}$$

Voltage - divider biasing 2 -



(voltage divider bias arrangement)

The basic construction for voltage divider bias for FET is exactly same as that for BJT, but d-c analysis of each is quite different. $I_G = 0$ for FET amplifiers, but the magnitude of I_B for common-emitter BJT amplifiers can affect the d-c level of current and voltage.



$$V_g = \frac{V_{DD} \cdot R_2}{R_1 + R_2}$$

Applying KVL

$$V_g - V_{gs} - V_{rs} = 0$$

$$\Rightarrow V_g = V_{gs} + V_{rs}$$

$$\Rightarrow V_{gs} = V_g - V_{rs}$$

$$\Rightarrow V_{GS} = V_G - I_D R_S$$

This is eqⁿ of straight line.

$$I_D R_S = V_G - V_{GS}$$

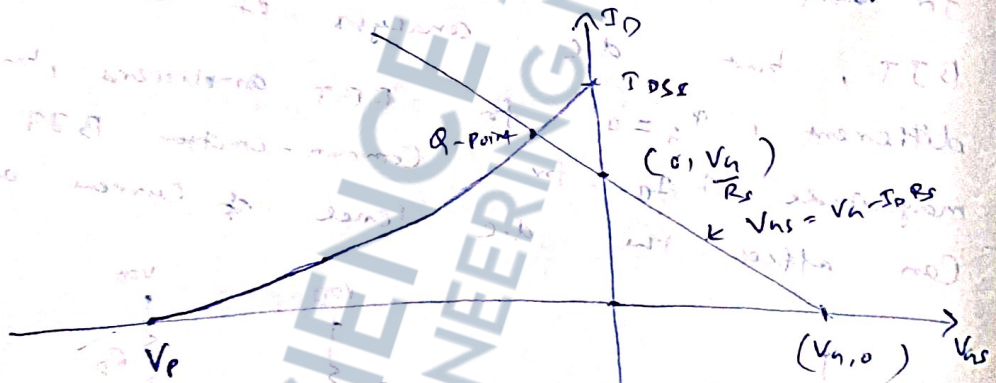
$$\Rightarrow I_D = -\frac{1}{R_S} V_{GS} + \frac{V_G}{R_S}$$

$$y = m x + c$$

To get the points

At $V_{GS} = 0$, $I_D = \frac{V_G}{R_S}$, $(0, \frac{V_G}{R_S})$

At $I_D = 0$, $V_{GS} = V_G$, $(V_G, 0)$



Applying KVL in O/P loop

$$V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$V_D = V_{DD} - I_D R_D$$

$$V_S = I_D R_S$$

$$I_{R_1} = I_{R_2} = \frac{V_{DD}}{R_1 + R_2}$$

Ex-3 :-

Determine the following for the o/w

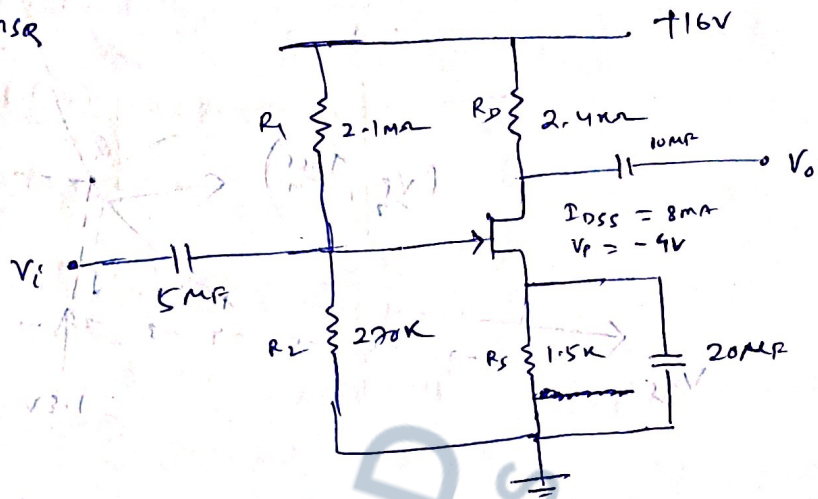
(a) FDR & V_{DS}

b. V_D

c. V_S

d. V_{GS}

e. V_{DQ}



Ans:

$$V_G = \frac{V_{DD} \cdot R_2}{R_1 + R_2}$$

$$= \frac{16 \times 270 \times 10^3}{2.1 \times 10^6 + 270 \times 10^3}$$

$$= \frac{16 \times 270}{2100 + 270}$$

$$V_G = 1.82V$$

$$V_{GS} = V_G - I_D R_S$$

$$V_{GS} = 1.82 - I_D (1.5 \times 10^3)$$

At $I_D = 0$, $V_{GS} = 1.82V$

At $V_{GS} = 0$, $I_D = \frac{1.82}{1.5 \times 10^3} = 0.121 \text{ mA}$

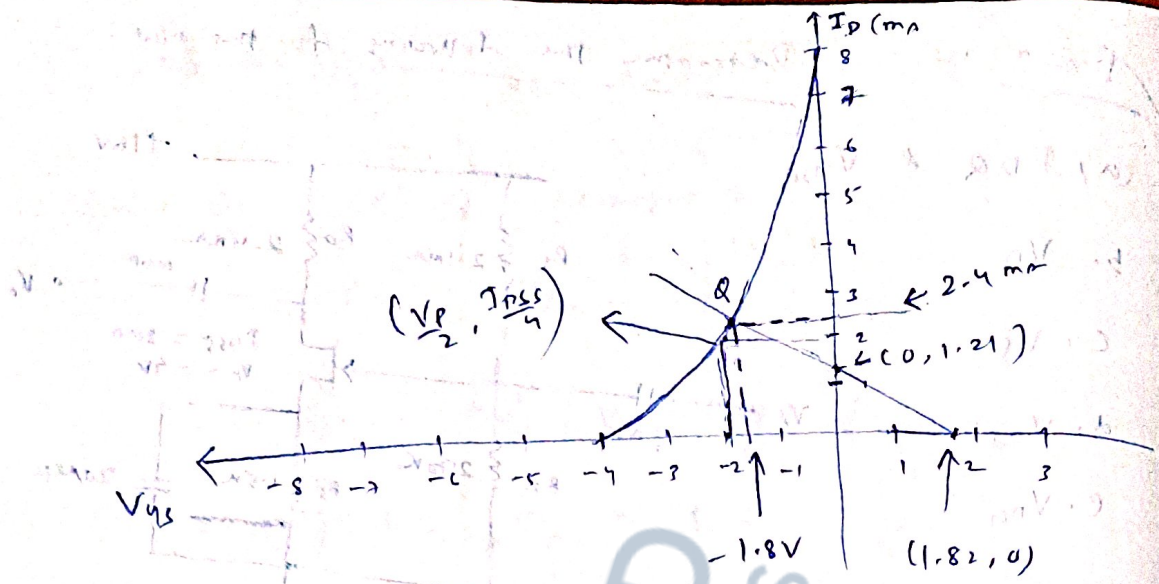
To draw the transfer characteristics.

1) $(V_{P1}, 0) = (-4, 0)$

2) $(0, I_{DSS}) = (0, 8)$

3) $(\frac{V_P}{2}, \frac{I_{DSS}}{4}) = (-2, 2)$

After drawing the transfer characteristics. The straight line is drawn which has



the end points $(V_G, 0)$, $(0, \frac{V_G}{R_S})$
 $= (-1.82, 0)$, $(0, 1.21 \text{ mA})$

where the straight line intersects the transfer characteristics, we get the Q-point.

(a) $(-1.8 \text{ V}, 2.4 \text{ mA})$
 $\uparrow \quad \uparrow$
 $V_{GSQ} \quad I_{DQ}$

(b) $V_D = V_{DD} - I_D R_D$
 $= 16 - 2.4 \times 2.4 \times 10^3 \times 10^{-3}$
 $V_D = 10.24 \text{ V}$

(c) $V_S = I_D \cdot R_S = 2.4 \times 1.5 \text{ V} = 3.6 \text{ V}$
 $V_S = 3.6 \text{ V}$

(d) $V_{DS} = V_D - V_S$
 $= 10.24 - 3.6$
 $V_{DS} = 6.64 \text{ V}$

(e) $V_{DG} = V_D - V_G$

$$\Rightarrow V_{D4} = 10.24 - 1.82$$

$$V_{D4} = 8.42 \text{ V}$$

Mathematical Approach :-

$$V_{th} = \frac{V_{D0} \times R_2}{R_1 + R_2}$$

$$= \frac{16 \times 2.7 \times 10^3}{2.1 \times 10^6 + 2.7 \times 10^3} = 1.82 \text{ V}$$

$$V_{th} = V_{th} - I_D R_s$$

$$V_{th} = 1.82 - I_D \times 1.5 \times 10^3 \quad \text{--- (1)}$$

$$\text{But } I_D = I_{DSS} \left(1 - \frac{V_{th}}{V_{P}}\right)^2$$

$$= 8 \times 10^{-3} \left(1 - \frac{V_{th}}{4}\right)^2$$

$$I_D = 8 \times 10^{-3} \left(1 + \frac{V_{th}}{4}\right)^2 \quad \text{--- (2)}$$

Equating (1) & (2)

$$\frac{1.82 - V_{th}}{1.5 \times 10^3} = \frac{8 \times 10^{-3} (1 + \frac{V_{th}}{4})^2}{16}$$

$$\Rightarrow 1.82 - V_{th} = \frac{12}{16} (16 + V_{th}^2 + 8V_{th})$$

$$\Rightarrow 7.28 - 4V_{th} = 48 + 3V_{th}^2 + 24V_{th}$$

$$\Rightarrow 3V_{th}^2 + 28V_{th} + 40.72 = 0$$

$$\text{Solving } V_{th} = -1.80 \text{ or } -7.53$$

-7.53 is not possible since it is less than V_p .

$$V_{th} = -1.8 \text{ V}$$

From eqn (1), $I_D = \frac{1.82 - V_{GS}}{1.5 \times 10^3}$

$$= \frac{1.82 - (-1.8)}{1.5 \times 10^3}$$

$$I_D = 2.41 \text{ mA}$$

Then Rest Process is same as that done by graphical method.

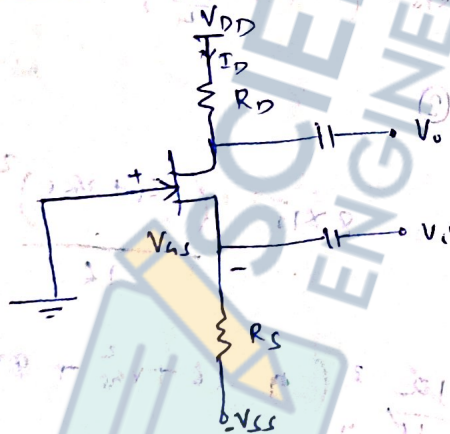
(b) $V_D = V_{DD} - I_D R_D$

(c) $V_S = I_D R_S$

(d) $V_{DS} = V_D - V_S$

(e) $V_{DG} = V_D - V_G$

Common - Gate Configuration:



$\rightarrow -V_{GS} - I_S R_S + V_{SS} = 0$

$\Rightarrow V_{GS} = V_{SS} - I_S R_S$

$I_S = I_D$

$\Rightarrow V_{GS} = V_{SS} - I_D R_S$

At $I_D = 0$, $V_{GS} = V_{GS}$
 $(V_{GS} = 0)$, $I_D = \frac{V_{GS}}{R_S}$ (9V, 0V)

$V_{DD} - I_D R_D - V_{DS} - I_D R_S = 0$ (2-1)

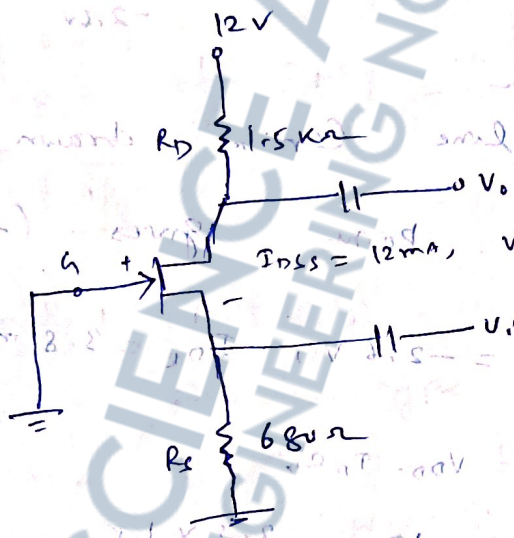
$\Rightarrow V_{DS} = V_{DD} + V_{GS} - I_D (R_D + R_S)$

$V_D = V_{DD} - I_D R_D$

$V_S - I_D R_S + V_{GS} = 0$

$\Rightarrow V_{GS} = -V_{GS} + I_D R_S$

Ex: - 4



Ans: $V_{GS} = V_{GS} - I_D R_S = 0 - I_D R_S$

Eqn of line passing through origin (0,0).
 one point is (0,0).
 For any arbitrary point, $I_D = 6mA$, $V_{GS} = -6 \times 680 \times 10^{-3}$
 $V_{GS} = -4.08V$

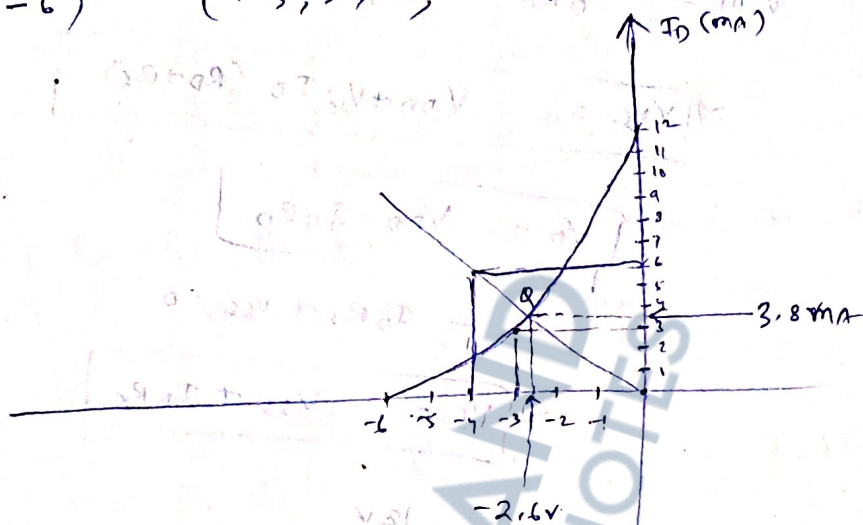
To draw V_{GS} transfer characteristics,

At $V_{GS} = \frac{V_P}{2}$, $I_D = \frac{I_{DSS}}{4}$
 $\Rightarrow V_{GS} = -\frac{6}{2}$, $I_D = \frac{12mA}{4}$
 $\Rightarrow V_{GS} = -3V$, $I_D = 3mA$

3 points are

$$(V_0, V_P), (-3, 3), (I_{DSS}, 0)$$

$$(0, -6), (-3, 3), (12, 0)$$



(Bias) eqn
 Straight line can be drawn $(0, 0), (-4.08, 6)$
 Intersecting point gives $(-2.6, 3.8)$

(a) $V_{GS} = -2.6 \text{ V}, I_{DQ} = 3.8 \text{ mA}$

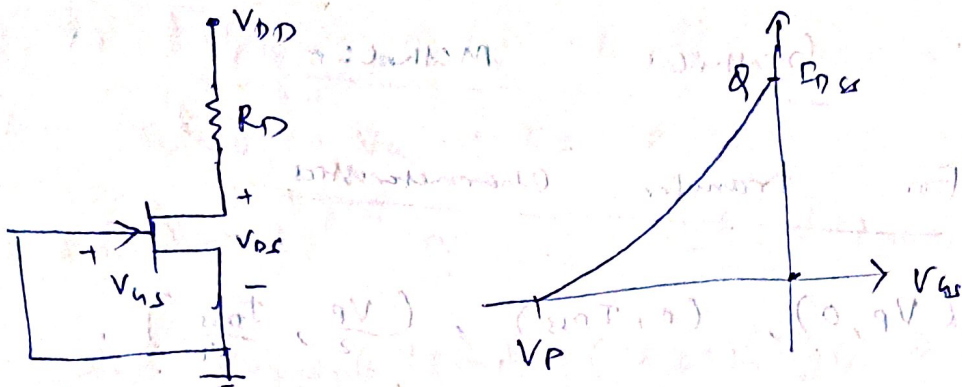
(b) $V_D = V_{DD} - I_{DQ} R_D$
 $= 12 - 3.8 \times 1.5$
 $V_D = 6.3 \text{ V}$

(c) $V_G = 0$

(d) $V_S = I_D \times R_S = 3.8 \times 10^{-3} \times 680 \Omega = 2.584$

(e) $V_{DS} = V_D - V_S = 6.3 - 2.58 = 3.72 \text{ V}$

Special case $V_{GS} = 0 \text{ V}$



$$I_{DQ} = I_{DSS} \quad \text{at } V_{GS} = 0V$$

$$V_{DD} - I_D R_D - V_{DS} = 0$$

$$\Rightarrow V_{DS} = V_{DD} - I_D R_D$$

$$V_D = V_{DD} - I_D R_D = V_{DS}$$

$$V_L = 0V$$

Depletion-type MOSFET

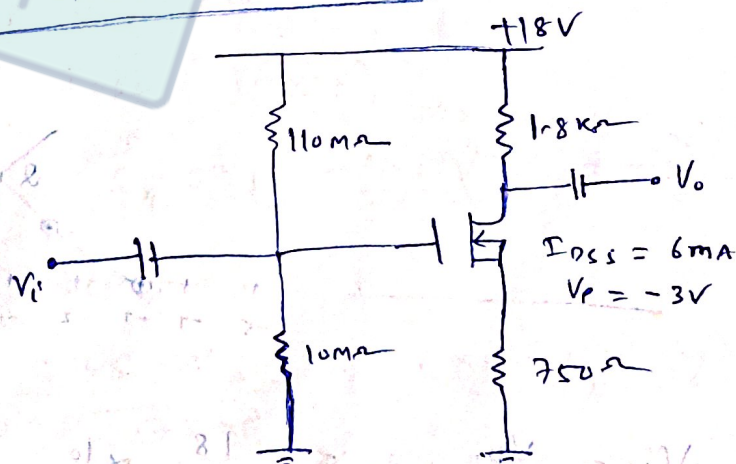
The analysis of depletion type MOSFET is similar to that of JFET. The primary difference between the two is the fact that depletion type MOSFET, in addition, permit operating with +ve value of V_{GS} and levels of I_D that exceeds I_{DSS} .

EX-5:-

Determine

(a) I_{DQ} , V_{GSQ}

(b) V_{DS}



Ans: - Graphical Method: -

For transfer characteristics

$$(V_p, 0), (0, I_{DSS}), \left(\frac{V_p}{2}, \frac{I_{DSS}}{4}\right)$$

Since transfer characteristics lies on the side of V_{GS} , to find a point,

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_p}\right)^2$$

For an arbitrary, +ve value of V_{GS} (+1V)

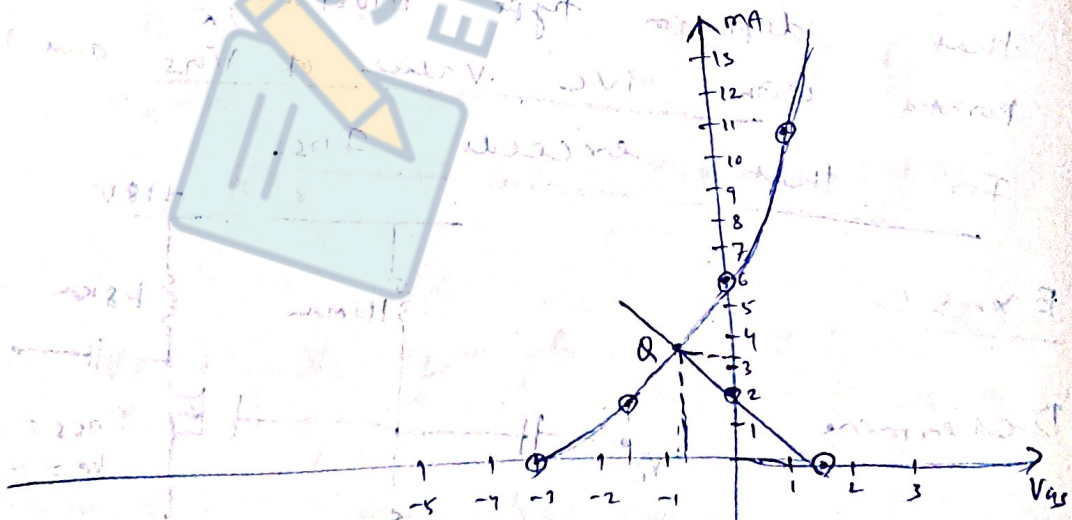
$$I_D = I_{DSS} \left(1 - \frac{1}{3}\right)^2 = \left(1 - \frac{1}{3}\right)^2 \times 6 \times 10^{-3}$$

$$I_D = 10.67 \text{ mA}$$

∴ 4th point (1, 10.67)

3 points are

$$(-3, 0), (0, 6), (-1.5, 1.5), (1, 10.67)$$



$$V_G = \frac{V_{DD} \times R_2}{R_1 + R_2} = \frac{18}{10 + 10} \times 10 = 9 \text{ V}$$

$$V_{GS} = V_G - I_D R_S$$

At $I_D = 0$, $V_{GS} = V_G = 1.5V$

At $V_{GS} = 0$, $I_D = \frac{V_G}{R_S} = \frac{1.5}{750\Omega} = 2\text{mA}$

Points for straight line (bias) $(1.5, 0)$ $(0, 2)$
 Intersection of 2 curves given, (V_{GS}, I_D)

$(-0.8V, 3.1\text{mA})$

$$V_{DS} = V_{DD} - I_D(R_D + R_S)$$

$$= 18 - 3.1(1.8 + 0.75)$$

$$= 10.095$$

$V_{DS} \approx 10.1V$

Mathematical approach:-

$$V_G = \frac{V_{DD} \times R_2}{R_1 + R_2} = \frac{18 \times 10}{10 + 10} = 1.5V$$

$$V_{GS} = V_G - I_D R_S$$

$$V_{GS} = 1.5 - I_D \times 750$$

$$\Rightarrow I_D = \frac{1.5 - V_{GS}}{750} \quad \text{--- (1)}$$

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2$$

$$I_D = 6 \times 10^{-3} \left(1 - \frac{V_{GS}}{-3}\right)^2 \quad \text{--- (2)}$$

equating (1) & (2),

$$\frac{1.5 - V_{GS}}{750 \times 10^{-3}} = 6 \times 10^{-3} \left(1 + \frac{V_{GS}}{3}\right)^2$$

$$1.5 - V_{GS} = 4.5 \times 10^{-3} (3 + V_{GS})^2$$

$$\Rightarrow 1.5 - V_{GS} = \frac{2 \times 2}{1} (9 + V_{GS}^2 + 6V_{GS})$$

$$\Rightarrow 3 - 2V_{GS} = 9 + V_{GS}^2 + 6V_{GS}$$

$$\Rightarrow V_{GS}^2 + 8V_{GS} + 6 = 0$$

$$V_{GS} = -0.83, -7.16 \text{ V}$$

-7.16 V is not possible

$$\therefore V_{GSQ} = -0.83 \text{ V}$$

$$I_D = \frac{1.5 - V_{GS}}{750} = \frac{1.5 + 0.83}{750} = 3.1 \text{ mA}$$

$$I_{DQ} = 3.1 \text{ mA}$$

$$V_{DS} = V_{DD} - I_D (R_D + R_S) = 18 - 3.1 \times 10^{-3} (1.8 + 0.75) \times 10^3$$

$$V_{DS} = 10.0 \text{ V}$$

Ex-6 :- Repeat Example 5, with $R_S = 150 \Omega$

The transfer characteristic remains same,

For bias line

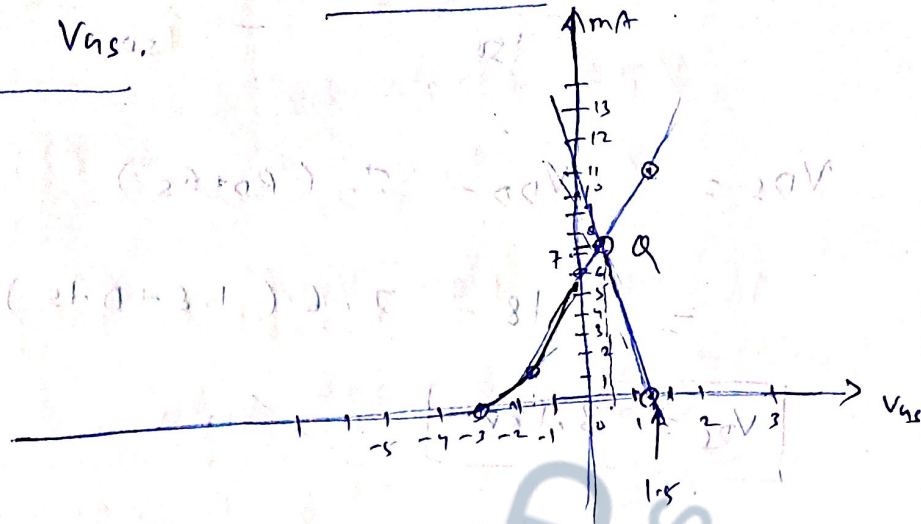
$$V_{GS} = V_G - I_D R_S$$

$$I_D = 0, \quad V_{GS} = V_G = 1.5 \text{ V} \quad (1)$$

$$V_{GS} = 0, \quad I_D = \frac{V_G}{R_S} = \frac{1.5}{150} = \frac{1}{100} = 10 \text{ mA}$$

Note :- In this case Q-Point results in a

drain current exceeds I_{DSS} with +ve value for V_{GS} .



Intersection of bias line, the transfer curve

gives $(1.5, 0)$ & $(0, 10)$

$V_{GSQ}, I_{DQ} = (0.35V, 7.6mA)$

Mathematical approach:-

$$I_D = \frac{1.5 - V_{GS}}{150} \quad \text{--- (1)}$$

$$I_D = 6 \times 10^{-3} \left(1 + \frac{V_{GS}}{3}\right)^2 \quad \text{--- (2)}$$

$$\frac{1.5 - V_{GS}}{150} = \frac{6 \times 10^{-3} (3 + V_{GS})^2}{9}$$

$$\Rightarrow 1.5 - V_{GS} = \frac{0.15 \times 10^{-3} \times 6 \times 10^{-3} (9 + V_{GS}^2 + 6V_{GS})}{9}$$

$$\Rightarrow (1.5 - V_{GS}) \times 10 = 9 + V_{GS}^2 + 6V_{GS}$$

$$\Rightarrow 15 - 10V_{GS} = 9 + V_{GS}^2 + 6V_{GS}$$

$$\Rightarrow V_{GS}^2 + 16V_{GS} - 6 = 0$$

$$V_{GS} = 0.36V, -16.36V$$

$$\therefore V_{GS} = 0.36V$$

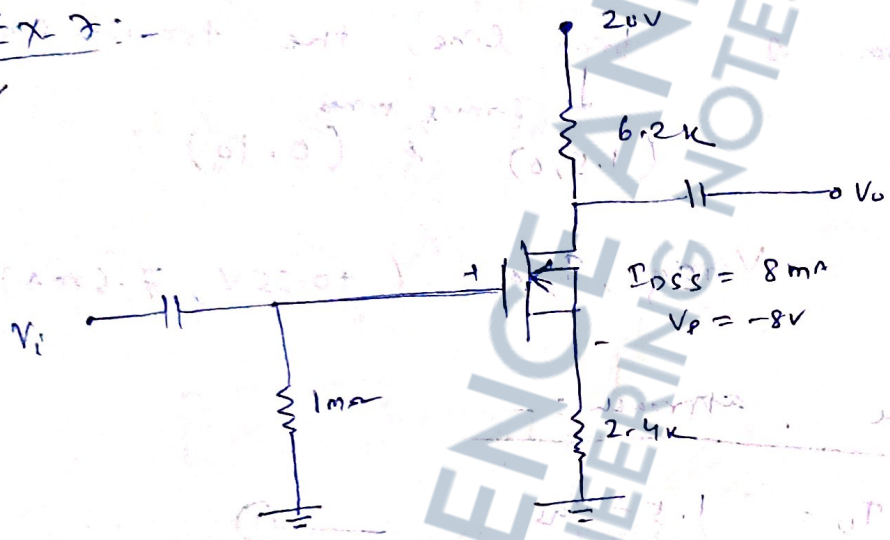
$$I_D = \frac{1.5 - V_{GS}}{150} = \frac{1.5 - 0.34}{150} = 7.6 \text{ mA}$$

$$V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$= 18 - 7.6 (1.8 + 0.15)$$

$$V_{DS} = 3.18 \text{ V}$$

Ex 7: ✓



Ans: The self-bias configuration results in,

$$-V_{GS} - I_D R_S = 0$$

$$\Rightarrow V_{GS} = -I_D R_S$$

$$\text{At } I_D = 0, V_{GS} = 0$$

At $V_{GS} = -6 \text{ V}$, (Any arbitrary point)

$$I_D = \frac{-V_{GS}}{R_S} = \frac{+6}{2.4 \times 10^3} = 2.5 \text{ mA}$$

$(0, 0)$ and $(-6, 2.5)$ are the points of the bias line.

To draw the transfer characteristics.

$$(V_P, 0), (0, I_{DSS}) \text{ i.e. } (-8, 0), (0, 8)$$

At $V_{GS} = \frac{V_P}{2}, \quad I_{DSS} \text{ i.e. } (-4, 2 \text{ mA})$

For a true PNM,

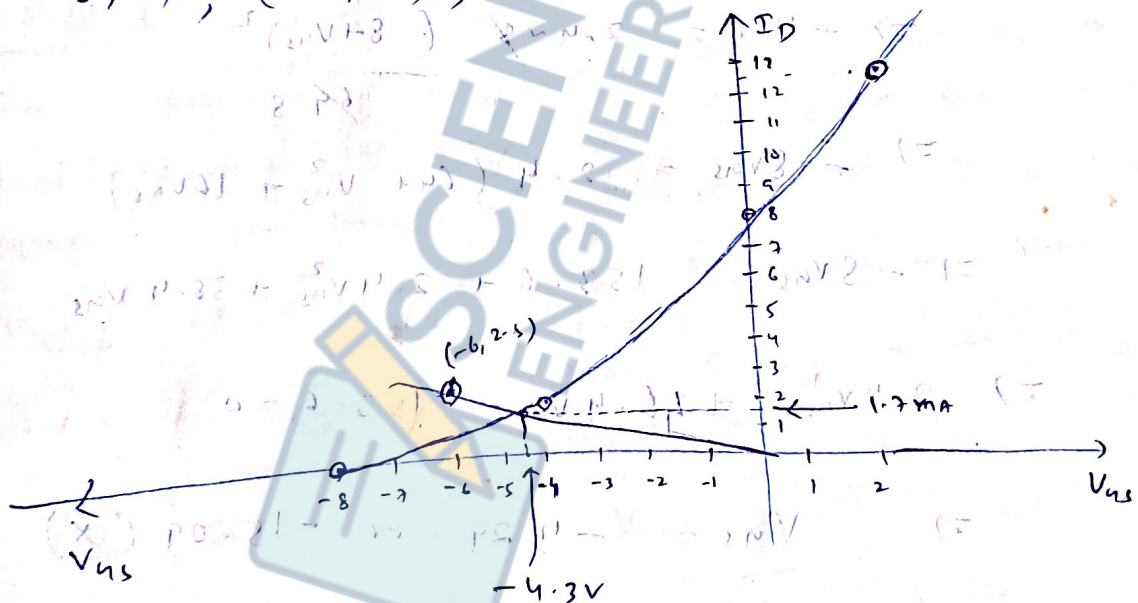
At $V_{GS} = +2 \text{ V}$

$$\begin{aligned} I_{DQ} &= I_{DSS} \left(1 - \frac{V_{GS}}{V_P}\right)^2 \\ &= 8 \times 10^{-3} \left(1 - \frac{2}{-8}\right)^2 \\ &= 8 \times 10^{-3} \left(1 + \frac{1}{4}\right)^2 \\ &= 8 \times 10^{-3} \left(\frac{25}{16}\right) \end{aligned}$$

$I_{DQ} = 12.5 \text{ mA}$

(4 points are)

$$(-8, 0), (-4, 2), (0, 8), (2, 12.5)$$



Intersection of 2 curves, gives the Q-Point

$(-4.3 \text{ V}, 1.7 \text{ mA})$

Mathematical approach

$$V_{GS} = -I_D R_S$$

$$V_{GS} = -I_D \cdot 2.4 \times 10^3 \quad \text{--- (1)}$$

$$I_D = I_{DSS} \left(1 - \frac{V_{GS}}{V_P} \right)^2$$

$$= 8 \times 10^{-3} \left(1 - \frac{V_{GS}}{-8} \right)^2$$

$$I_D = 8 \times 10^{-3} \left(1 + \frac{V_{GS}}{8} \right)^2 \quad \text{--- (2)}$$

Equating (1) & (2),

$$\frac{-V_{GS}}{2.4 \times 10^3} = 8 \times 10^{-3} \left(1 + \frac{V_{GS}}{8} \right)^2$$

$$\Rightarrow -V_{GS} = 2.4 \times 8 \frac{(8 + V_{GS})^2}{64}$$

$$\Rightarrow -8V_{GS} = 2.4 (64 + V_{GS}^2 + 16V_{GS})$$

$$\Rightarrow -8V_{GS} = 153.6 + 2.4V_{GS}^2 + 38.4V_{GS}$$

$$\Rightarrow 2.4V_{GS}^2 + 46.4V_{GS} + 153.6 = 0$$

$$\Rightarrow V_{GS} = -4.24 \text{ V} \quad \text{or} \quad -15.09 \text{ (X)}$$

$$V_{GS} = -4.24 \text{ V}$$

$$I_D = -\frac{V_{GS}}{R_S} = \frac{-(-4.24)}{2.4 \times 10^3} = 1.76 \text{ mA}$$

$$Q \quad (-4.24 \text{ V}, 1.76 \text{ mA})$$

EX-8 : - Determine V_{DS}

✓ 2 marks

for the n/w

Ans : $V_{GS} = 0V$

Since $V_{GS} = 0$,

$I_D = I_{DSS} = 10mA$

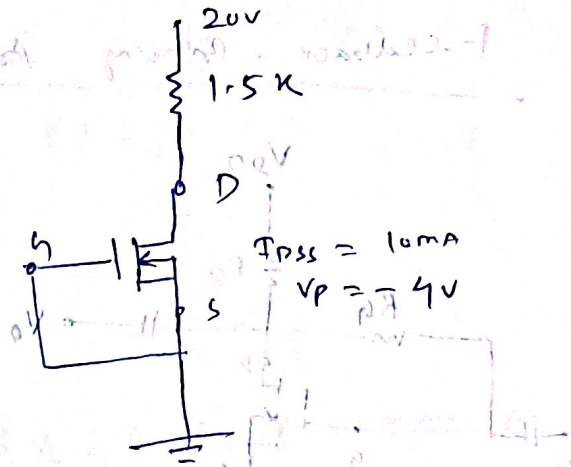
$I_D (0, 10mA)$

$V_{DS} = V_{DD} - I_D R_D$

$= 20 - 10 \times 10^{-3} \times 1.5 \times 10^3$

$= 20 - 15$

$V_{DS} = 5V$



Enhancement - type MOSFETS

For ~~n-channel~~ ^{n-channel} enhancement type MOSFET, the drain current is zero for levels of gate-to-source voltage less than the threshold level $V_{GS(th)}$.

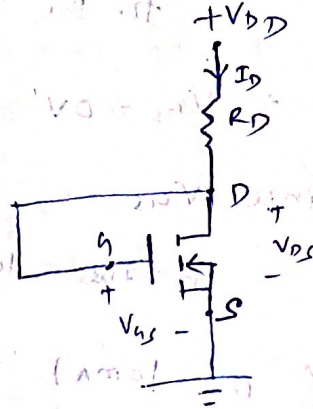
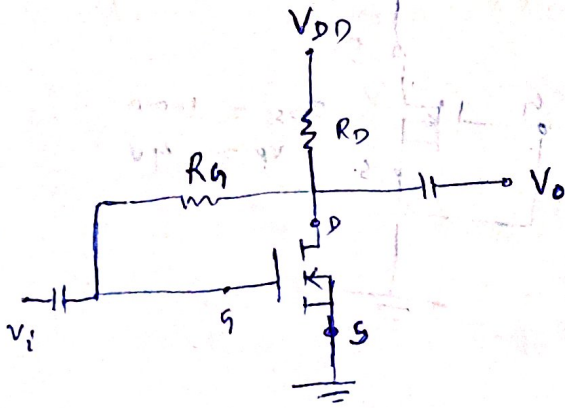
For levels of V_{GS} greater than $V_{GS(th)}$ the drain current is defined by

$I_D = K (V_{GS} - V_{GS(th)})^2$ ①

and $K = \frac{I_{D(on)}}{[V_{GS(on)} - V_{GS(th)}]^2}$ ②

$I_{D(on)}$, $V_{GS(on)}$, $V_{GS(th)}$ Specified in Specification sheet.

Feedback - Biasing Arrangement :-



Feedback Biasing Arrangement

Since $I_D = 0$, $V_{Rg} = 0$ so the d-c equivalent can be drawn as shown above.

$$V_D = V_g$$

(Current zero means D & G are at same potential)

$$V_{DS} = V_{GS}$$

(∵ D = G)

O/P cut

$$V_{GS} = V_{DD} - I_D R_D$$

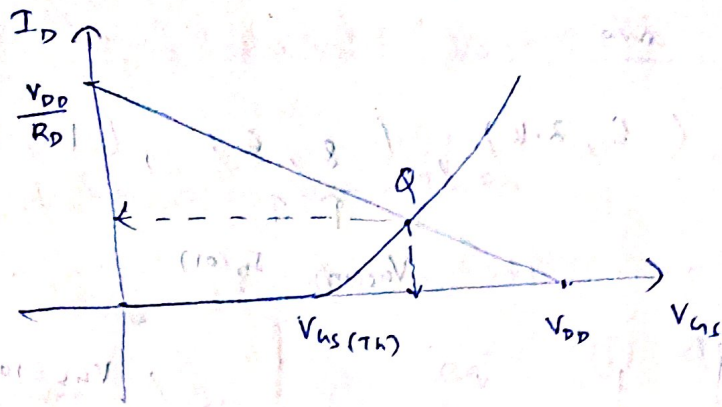
OR
 $V_{DD} - I_D R_D - V_{GS} = 0$
 $\Rightarrow V_{GS} = V_{DD} - I_D R_D$
 also $V_{GS} = V_{DD} - I_D R_D$
 $\therefore V_{GS} = V_{GS}$

$$V_{GS} = V_{DD} - I_D R_D \quad (\because V_{DS} = V_{GS})$$

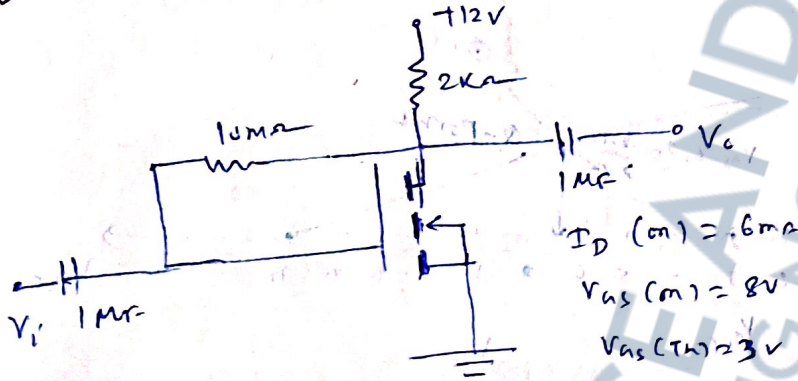
Eqn ① is a straight line

At $I_D = 0$, $V_{GS} = V_{DD}$

At $V_{GS} = 0$, $I_D = \frac{V_{DD}}{R_D}$



Ex: - 9 Determine I_{DQ} & V_{GSQ} for enhancement-type MOSFET



Ans: $I_{DQ} = 2.16 \text{ mA}$, $V_{GSQ} = 6 \text{ V}$

$$I_{DQ} = \frac{K}{2} [V_{GSQ} - V_{GS(TH)}]^2$$

$$6 \times 10^{-3} = \frac{K}{2} [8 - 3]^2$$

$$K = \frac{6 \times 10^{-3} \times 2}{25} = 0.24 \times 10^{-3} \frac{\text{A}}{\text{V}^2}$$

To draw transfer characteristics, we need

Point 1, Arbitrary point

At $V_{GS} = 6 \text{ V}$, $I_D = K (V_{GS} - V_T)^2$

(between 3 to 8V)

$V_{GS(TH)} = 3 \text{ V}$, $V_{GS(on)} = 8 \text{ V}$

$$I_D = 0.24 \times 10^{-3} (6 - 3)^2$$

$$I_D = 2.16 \text{ mA}$$

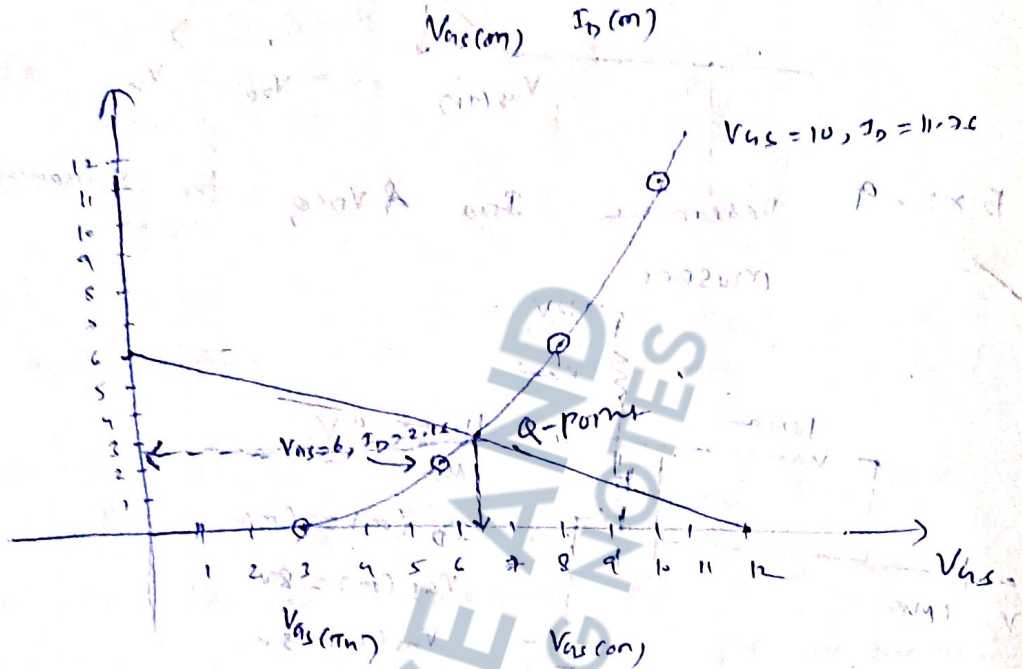
Arbitrary point,

$V_{GS} = 10 \text{ V}$, ($> V_{GS(TH)}$), $I_D = 0.24 \times 10^{-3} (10 - 3)^2$

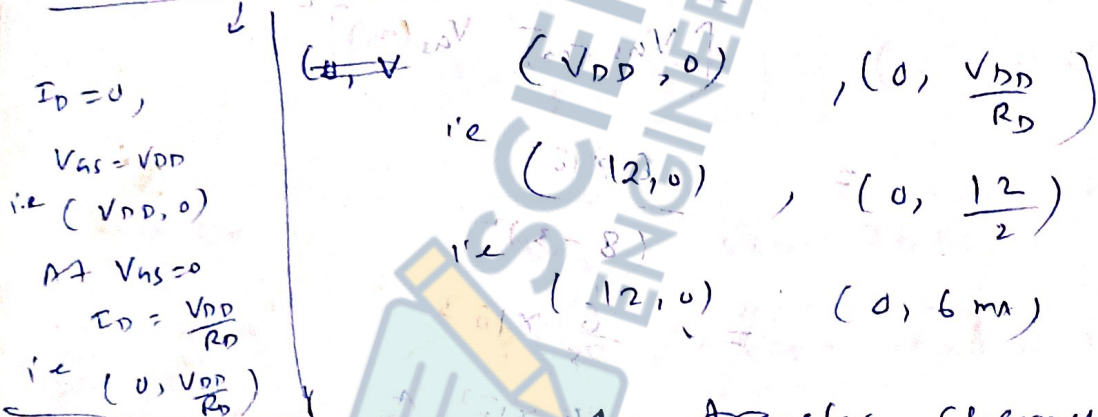
$$I_D = 11.76 \text{ mA}$$

4 points are

$(V_{th}, 0)$, $(6, 2.6)$, $(8, 6)$, $(10, 11.76)$
 $(3, 0)$



To draw the Bias line $(V_{ds} = V_{DD} - I_D R_D)$



The intersection of transfer characteristics & Bias line gives Q-point $(6.4 \text{ V}, 2.75 \text{ mA})$

Mathematical Approach:-

$$K = \frac{I_D (\text{mA})}{[V_{gs} (\text{mV}) - V_{th}]^2} = 0.24 \times 10^{-3} \frac{\text{A}}{\text{V}^2}$$

$$I_D = K (V_{gs} - V_{th})^2$$

$$\Rightarrow I_D = 0.24 \times 10^{-3} (V_{GS} - 3)^2 \quad \text{--- (1)}$$

$$V_{GS} = V_{DD} - I_D R_D$$

$$V_{GS} = 12 - I_D \cdot 2 \times 10^3 \quad \text{--- (2)}$$

Equating I_D of eqⁿ (1) + (2)

$$0.24 \times 10^{-3} (V_{GS} - 3)^2 = \frac{12 - V_{GS}}{2 \times 10^3}$$

$$\Rightarrow 0.48 (V_{GS}^2 + 9 - 6V_{GS}) = 12 - V_{GS}$$

$$\Rightarrow 0.48 V_{GS}^2 + 4.32 - 2.88 V_{GS} = 12 - V_{GS}$$

$$\Rightarrow 0.48 V_{GS}^2 - 1.88 V_{GS} - 7.68 = 0$$

$$V_{GS} = 6.4 \text{ V or } -2.49 \text{ V (Not possible)}$$

$$V_{GS} > V_{th}, \quad \boxed{V_{GS} = 6.4 \text{ V}}$$

$$V_{GS} > 3 \text{ V}$$

$$\therefore I_D = \frac{0.24 \times 10^{-3} (12 - V_{GS})}{2 \times 10^3} \quad \text{(from eqⁿ (2))}$$

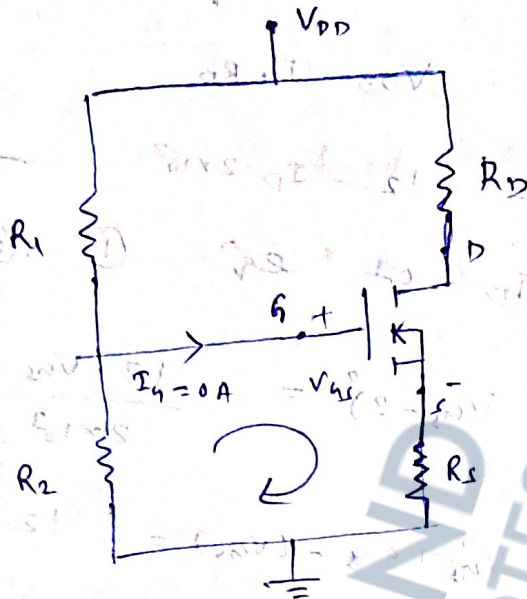
$$= \frac{12 - 6.4}{2 \times 10^3}$$

$$= \frac{5.6}{2} \times 10^{-3}$$

$$\boxed{I_D = 2.8 \text{ mA}}$$

Q point (6.4 V, 2.8 mA)

Voltage - Divider Biasing Arrangement :-



The fact that $I_B = 0 \text{ mA}$, results in the following eqn for V_{BE} as derived from an application of the voltage divider rule,

$$V_{BE} = \frac{V_{DD} \times R_2}{R_1 + R_2} \quad \text{--- (1)}$$

Applying Kirchhoff's voltage law around the loop,

$$V_{BE} - V_{BE} - V_{RE} = 0$$

$$\Rightarrow V_{BE} = V_{BE} - V_{RE} = V_{BE} - I_D R_E$$

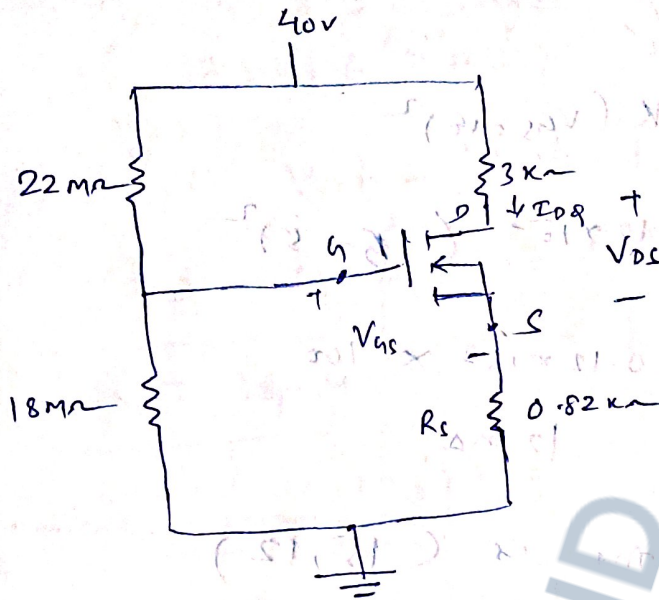
$$V_{BE} = V_{BE} - I_D R_E \quad \text{--- (2)}$$

For O/P section.

$$V_{DD} - I_D R_C - V_{CE} - I_D R_E = 0$$

$$\Rightarrow V_{CE} = V_{DD} - I_D (R_C + R_E)$$

Ex - 10



$V_{GS(TH)} = 5V$
 $I_D(on) = 3mA$
 at $V_{GS(on)} = 10V$

To draw the transfer characteristics Bias line

$$V_{GS} = V_G - I_D R_S$$

When $I_D = 0$, $V_{GS} = V_G$

$$V_G = \frac{V_{DD} \times R_2}{R_1 + R_2} = \frac{40 \times 18}{22 + 18} = 18V$$

when $I_D = 0$, $V_{GS} = V_G = 18V$ $(18V, 0)$

when $V_{GS} = 0$, $I_D = \frac{V_G}{R_S} = \frac{18V}{0.82k\Omega} = 21.95mA$

The points of bias line are

$(0, 21.95mA)$, $(18V, 0)$

To draw transfer characteristics

At $V_{GS} = V_{TH}$, $I_D = 0$, $(5, 0)$

$$K = \frac{I_D(on)}{(V_{GS(on)} - V_{GS(TH)})^2} = \frac{3 \times 10^{-3}}{(10 - 5)^2} = 0.12 \frac{mA}{V^2}$$

At $V_{GS(on)}$, $I_D(on) = 3mA$, $(10, 3mA)$

At $V_{GS} = 15V,$

$$I_D = K (V_{GS} - V_t)^2$$

$$= 0.12 \times 10^{-3} (15 - 5)^2$$

$$= 0.12 \times 10^{-3} \times 100$$

$$I_D = 12 \text{ mA}$$

\therefore Point is $(15, 12)$

At $V_{GS} = 20V,$

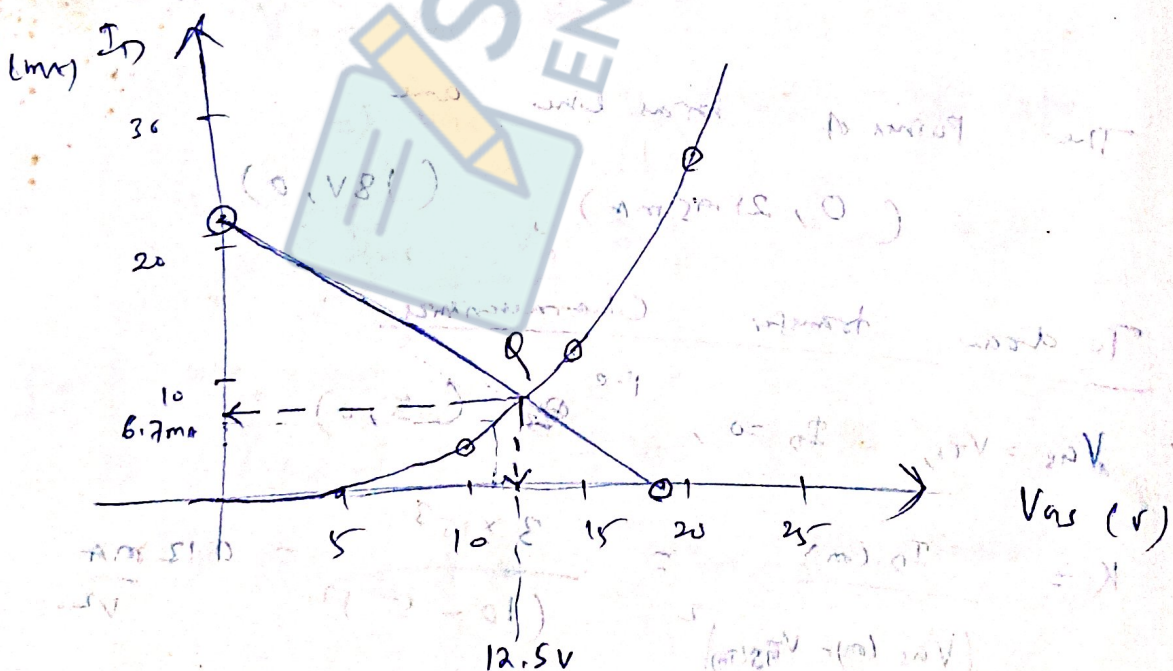
$$I_D = 0.12 \times 10^{-3} (20 - 5)^2$$

$$I_D = 27 \text{ mA}$$

\therefore Point is $(20, 27)$

\therefore 4 points on transfer characteristics are

$(5, 0), (10, 3), (15, 12), (20, 27)$



Now (for the) bias line is drawn. The

Intersection of 2 curves gives the Q point at $(12.5V, 6.7mA)$

$$V_{DS} = V_{DD} - I_D (R_D + R_S)$$

$$= 40 - 6.7 \times 10^{-3} (3 + 0.82) \times 10^3$$

$V_{DS} = 14.4V$

Mathematical Approach:-

$$K = \frac{I_D (mA)}{V_{GS(on)} - V_{GS(th)}} = \frac{0.12 \text{ mA}}{\sqrt{20V}}$$

$$I_D = K (V_{GS} - V_T)^2$$

$$I_D = 0.12 \times 10^{-3} (V_{GS} - 5)^2 \quad \text{--- (1)}$$

$$V_{GS} = V_{G1} - I_D R_S$$

$$V_{G1} = \frac{V_{DD} \times R_2}{R_1 + R_2} = \frac{40 \times 18}{22 + 18} = 18V$$

$$V_{GS} = 18 - I_D \times 0.82 \times 10^3 \quad \text{--- (2)}$$

Equating I_D of eqn (1) & (2), we have

$$0.12 \times 10^{-3} (V_{GS} - 5)^2 = \frac{18 - V_{GS}}{0.82 \times 10^3}$$

$$\Rightarrow 0.0984 (V_{GS}^2 + 25 - 10V_{GS}) = 18 - V_{GS}$$

$$\Rightarrow 0.0984 V_{GS}^2 + 2.46 - 0.984 V_{GS} = 18 - V_{GS}$$

$$\Rightarrow 0.0984 V_{GS}^2 + 0.016 V_{GS} - 15.54 = 0$$

$$\Rightarrow V_{GS} = 12.48V \text{ or } -12.64V \text{ (Not possible)}$$

$$V_{GS} = 12.48V$$

$$I_D = \frac{118 - 12.48}{0.82 \times 10^3}$$

$$I_D = 6.73 \text{ mA}$$

$$V_{DS} = V_{DD} - I_D (R_D + R_S)$$
$$= 40 - 6.73 (3 + 0.82)$$

$$V_{DS} = 14.3V$$

