

①  
(Chapter-I)  
Introduction to Electronics

→ The word electronics derives its name from 'electron' present in all material.

→ So the branch of Engineering which deals with current conduction through a vacuum or gas or semiconductor is known as 'electronics'.

Signals :-

We can define signal as

→ "Any function of one or more independent variables which contains some information is called a signal".

→ Generally signal is a time varying quantity.

→ Signals contains information about variety of things & activities of our physical world.

→ In daily life we come across several electrical signals such as Radio signal, T.V signal, Computer signal etc.

One Dimensional Signal :-

When the function depends on a single variable, the signal is said to be one-dimensional signal.

Multidimensional Signal

When the function depends on two or more variables, the signal is said to be multidimensional

Classification

In electrical sense, the signal can be voltage or current. The voltage & current is the function of time as independent variable

Q Find the fundamental period of the following signals

- (a)  $\sin 15\pi t$  (b)  $\sin 20\pi t$

(a)  $x(t) = \sin 15\pi t$

$x(t) = \sin \omega t$

$\omega = 15\pi$

$2\pi f = 15\pi$

$f = \frac{15}{2} = 7.5 \text{ Hz}$

$T = \frac{1}{f} = \frac{1}{7.5} = 0.133 \text{ sec.}$

(b)  $x(t) = 8 \sin 20\pi t$

$\omega t = 20\pi t$

$\Rightarrow \omega = 20\pi$

$\Rightarrow 2\pi f = 20\pi$

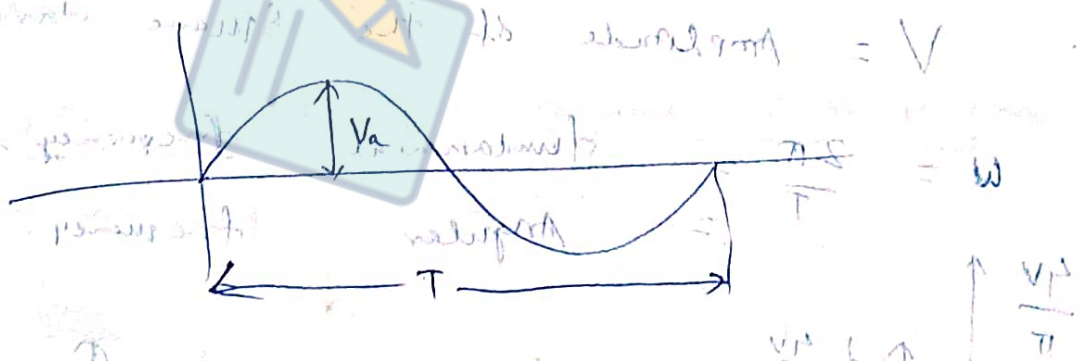
$\Rightarrow f = 10 \text{ Hz}$

$T = \frac{1}{f} = \frac{1}{10} = 0.1 \text{ Second}$

Frequency Spectrum of Signal:-

1) Using Mathematical Tools like Fourier Series & Fourier Transform

We can represent a voltage  $V_s(t)$  or current  $i_s(t)$  as a sum of sine waves of different frequency & amplitude.



$V_a$  = Amplitude.

$\omega$  = Angular frequency.

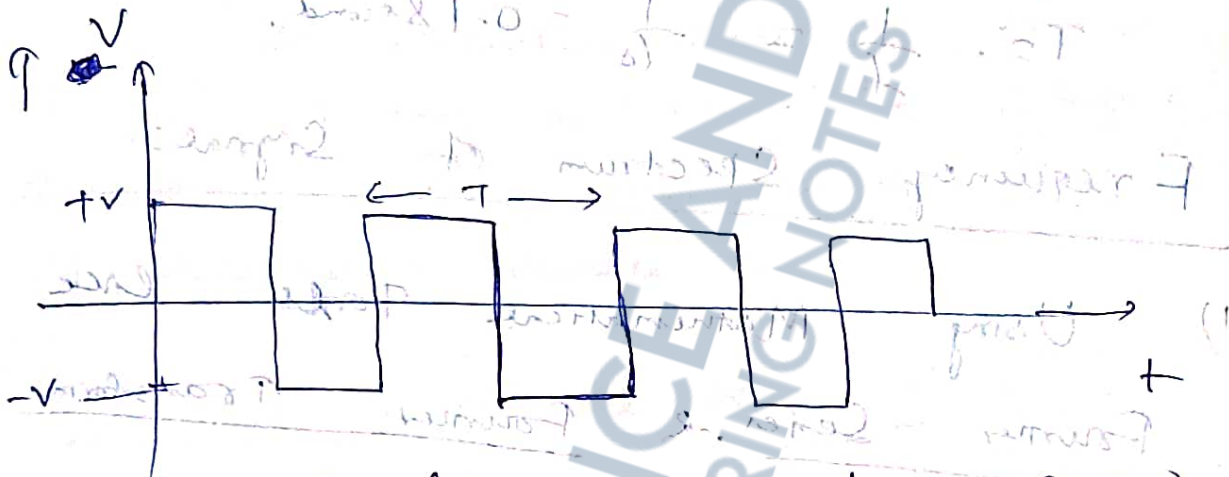
$T$  = Time period of signal.

$\omega = 2\pi f = \frac{2\pi}{T}$  ( $\because f = \frac{1}{T}$ )

→ Fourier series is for periodic signals.

→ Fourier transform is for both periodic and non periodic signals.

→ R.M.S Value =  $\frac{\text{Peak value}}{\sqrt{2}} = \frac{V_a}{\sqrt{2}}$  (Peak Amplitude)   
Here  $V_a =$



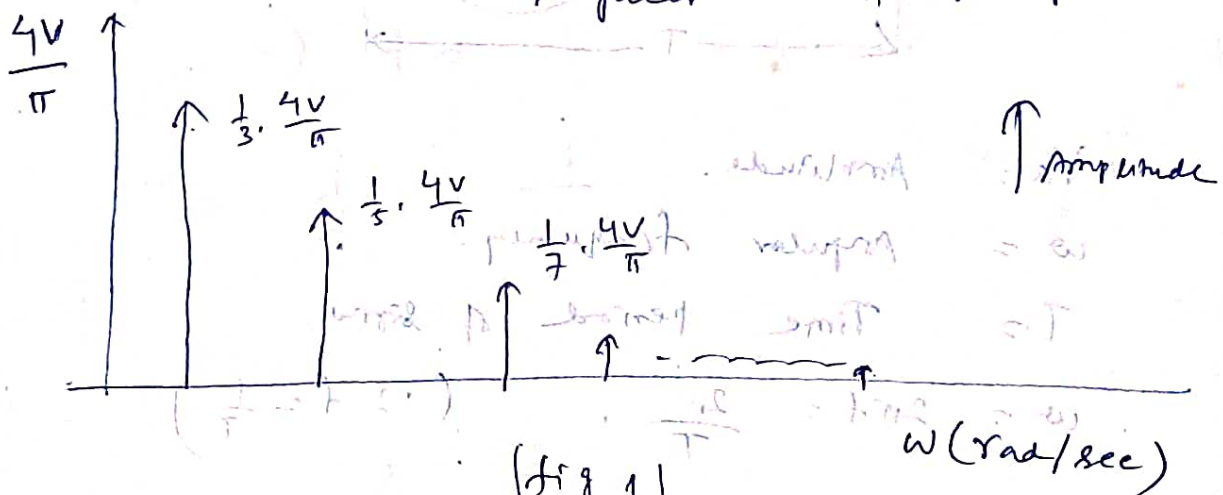
By the help of (Fourier transform) Fourier series

This symmetrical waveform can be represented as

$$V(t) = \frac{4V}{\pi} \left( \sin \omega t + \frac{1}{3} \sin 3\omega t + \frac{1}{5} \sin 5\omega t + \dots \right)$$

$V$  = Amplitude of the square wave

$$\omega = \frac{2\pi}{T} = \text{fundamental frequency} = \text{Angular frequency}$$



(fig 4)

(fig 1: The frequency spectrum of periodic square wave)

→ Fourier transform can be applied to a non periodic signal.

e.g.



Ques 1) Find the frequency  $f$  &  $\omega$  of a sine wave signal with period of 1 ms.

Ans: Data given  $T = 1 \text{ ms}$ .

$$f = \frac{1}{T} = \frac{1}{1 \times 10^{-3}} = 10^3 = 1000 \text{ Hz}$$

$$\omega = 2\pi f = 2\pi \times 1000 = 2\pi \times 10^3 \text{ rad/sec}$$

2) Define Analog, Digital, discrete-time signal

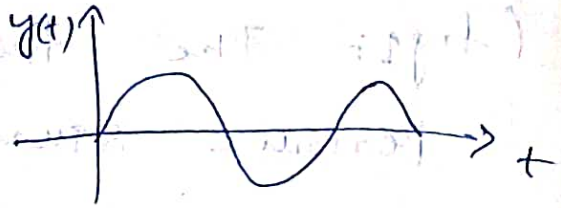
Ans: ~~...~~

Analog signal:-

The analog signal is that type of signal which varies continuously with certain interval of time.

e.g.

$$y(t) = \sin t$$



Note: - 1) The name (analog) derives from the fact that such a signal is analogous to the physical signal that is present.

2) The magnitude or amplitude of an analog signal can take any value.

Digital Signal: -

Digital signal is that type of signal which is represented a sequence of numbers i.e. magnitude at an instant of time.



e.g.: - Pulse train shown.

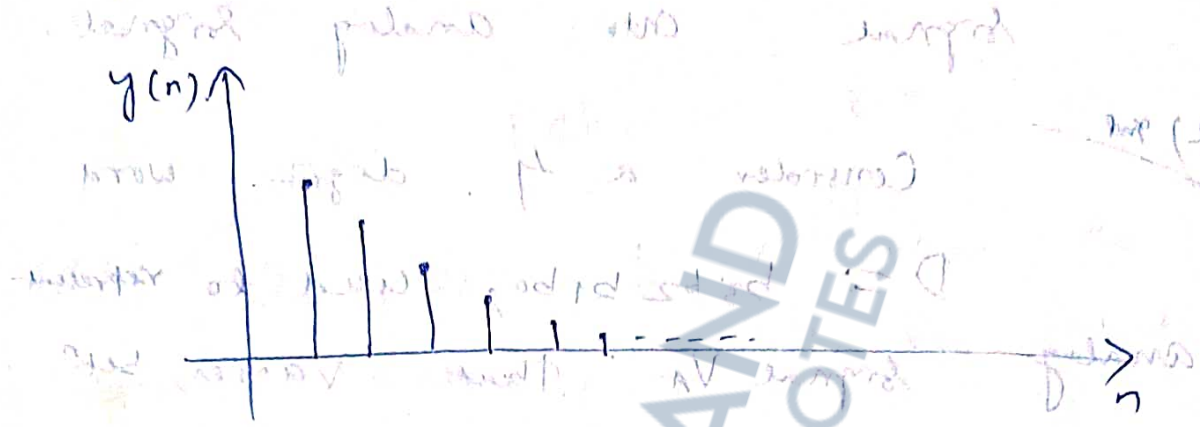
High = +5V (Called logic 1)

Low = 0V (Called logic 0)

Discrete-Time Signal: -

If a signal is defined for certain specific interval of time, then it is called discrete-time signal.

The time instant / interval need not be equidistant. But in practice they are usually taken at equal spaced intervals for computational convenience.



e.g.  $y(n) = \begin{cases} (0.2)^n, & \text{for } n \geq 0 \\ 0, & \text{otherwise} \end{cases}$

where  $n$  is an integer.

Conversion of Analog signal to Digital

Signals :-

Digital signal can be obtained from the analog signal by using an analog-to-digital converter (ADC).

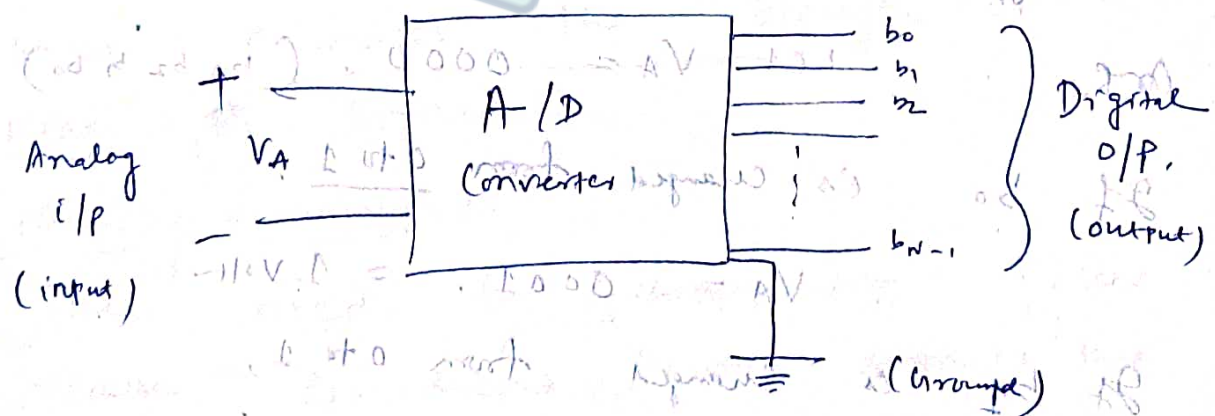


Fig: - Block diagram representation of analog-to-digital converter (ADC)

→ DAC → Digital to Analog Converter

→ DAC → It does the reverse of ADC. It converts digital signal into analog signal.

Q) ans

Consider a 4 digit word

$D = b_3 b_2 b_1 b_0$ , used to represent analog signal  $V_A$  that varies bet<sup>n</sup> 0 to 5V.

(a) Give  $D$  corresponding to

$V_A = 0V, 1V, 2V, 5V$ .

- Ans:
- 0 → 0000
  - 1 → 0001
  - 2 → 0010
  - 5 → 1011

b) Which change in  $V_A$  causes a change from 0 to 1 in  $b_0, b_1, b_2, b_3$ ?

Ans: Let  $V_A = 0000$ . ( $b_3 b_2 b_1 b_0$ )  
 If  $b_0$  is changed from 0 to 1,  
 $V_A = 0001 = 1 \text{ Volt}$

If  $b_1$  is changed from 0 to 1,

$V_A = 0010 = 2 \text{ Volt}$ .



If  $b_2$  is changed from 0 to 1,

$$V_A = 0.100 = 4V$$

If  $b_3$  is changed from 0 to 1,

$$V_A = 1.000 = 8V$$

(c) If  $V_A = 5.2V$ , what do you expect  $D$  to be, what is the error?

Ans:  ~~$D$  will be 5V~~

$$\text{Let } V_A = 5V$$

$$D_{\text{will be}} = 0101$$

$$\text{error} = \frac{5 - 5.2}{5} = \frac{-0.2}{5} = -4\%$$

### Amplifier :-

An amplifier is an electronic device which increases the strength of a weak signal.

### Need of amplification :-

The need for amplification arises because transducers provide signals that are said to be weak in the micro volt ( $\mu V$ ) or milli volt ( $mV$ ) range.

and possessing little energy. Such signals are too small for reliable processing.

→ Linearity in the amplifier should be taken care of, so that information contained in the signal is not changed or no new information is introduced.

→ An amplifier that preserves the details of the signal waveform is characterized by the relationship.

$$V_o(t) = A \cdot V_i(t) \quad \text{--- (1)}$$

→ 'A' is a constant representing the magnitude of amplification, known as amplifier gain.

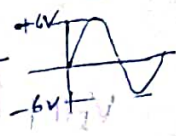
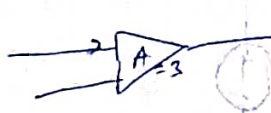
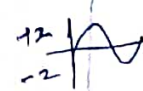
→ The equation (1) is a linear relationship, hence the amplifier describes a linear amplifier.

if  $V_o(t) = A \cdot V_i^2(t)$

it is a non-linear amplifier.

if the power of  $V_i(t)$  is  $\frac{1}{2}$  then it is linear else it is non-linear amplifier.

Ex: The Pre-amplifier in the home stereo system is an example of voltage amplifier.

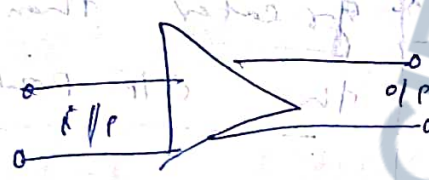


Power Amplifier: - in the home stereo system provides sufficient

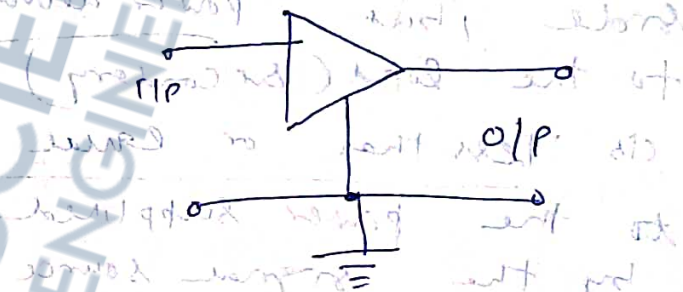
power to drive the loud speaker, which is a power load.

∴ Power Amplifier provide modest amount of voltage gain but substantial current gain. (High current gain)

Amplifier Ckt. Symbol: -



(a) < Ckt symbol for Amplifier >

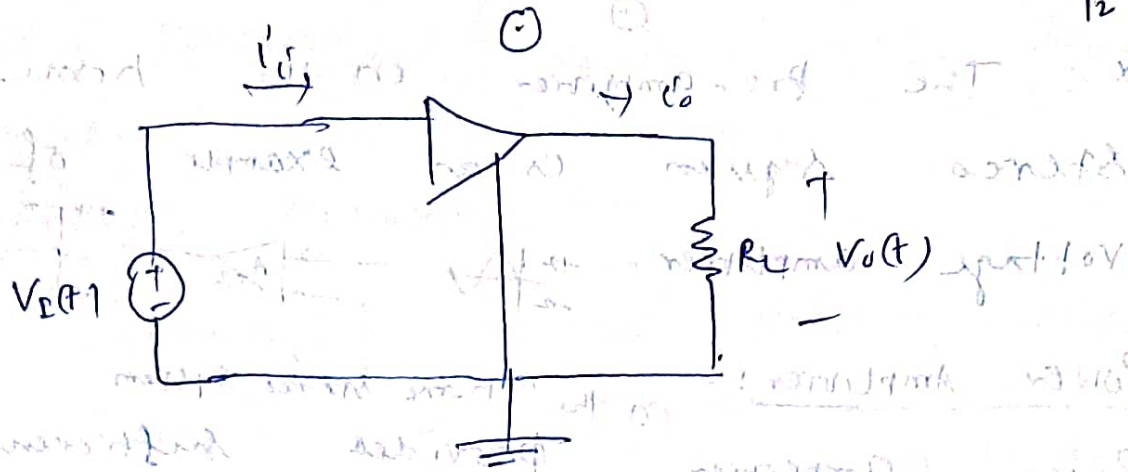


(b) < An amplifier with common terminal grounded between i/p and o/p ports >

Voltage gain = (A<sub>v</sub>)

It is ratio of o/p voltage to the i/p voltage.

$$A_v = \frac{V_o}{V_i}$$



Difference b/w transformer/Amplifier

Transformer

1) In transformer voltage delivered to the load could be greater than voltage feeding side, but power delivered to the load (secondary) is less than or equal to the power supplied by the signal source.  
 i.e. Power remains constant  
 $V_1 I_1 = V_2 I_2$

Amplifier

1) But in power amplifier, the O/P power is always greater than the C/P power.

$$\begin{aligned} \text{Power gain (A_p)} &= \frac{\text{Load power}}{\text{C/P power}} \\ &= \frac{\text{O/P power}}{\text{C/P power}} \\ &= \frac{V_o I_o}{V_i I_i} \end{aligned}$$

$$A_p = \frac{V_o I_o}{V_i I_i}$$

$$= \left( \frac{V_o}{V_i} \right) \cdot \left( \frac{I_o}{I_i} \right)$$

$$A_p = A_v \cdot A_i$$

$$\text{Power gain} = \text{Voltage gain} \times \text{Current gain}$$

Expressing in dB  $\rightarrow$  If  $A_p, A_v, A_i$  not in dB.

$$\text{Voltage gain in dB} = 20 \log |A_v| \text{ dB}$$

$$\text{Current gain in dB} = 20 \log |A_i| \text{ dB}$$

$$\text{Power gain in dB} = 10 \log |A_p| \text{ dB}$$

Amplifier efficiency:

$$\text{Efficiency } (\eta) = \frac{P_L}{P_{dc}} \times 100 = \frac{P_o}{P_{dc}} \times 100$$

$P_L =$  ~~Load~~ Power supplied to the load.

$P_{dc} =$  DC Power supplied to the amplifier.

$P_o =$  O/P power.

$$P = \frac{V \cdot I}{R} = \frac{V_o \cdot I_o}{R}$$

$$P = \frac{V_o \cdot I_o}{R}$$

Ex 1.1 (Sedra/Smith)

Q) Consider an amplifier, operating from  $\pm 10V$  power supplies. It is fed with sinusoidal voltage having  $1V$  peak & delivers a sinusoidal voltage o/p  $9V$  peak to  $1k\Omega$  load. The amplifier

draws a current of  $9.5mA$  from each of its 2 power supplies. The o/p current of the amplifier is found to be sinusoidal with  $0.1mA$  peak. Find the voltage gain, current gain, power gain & power drawn from the d.c. supplies, power dissipated in the amplifier and amplifier efficiency.

Ans:

(a)  $A_v = \text{voltage gain}$ .

$$A_v = \frac{\text{o/p voltage}}{\text{i/p voltage}} = \frac{9V}{1V} = 9$$

$$(A_v)_{dB} = 20 \log 9 = 19.08 \text{ dB}$$

(b)  $A_I = \text{Current gain} = \frac{I_o}{I_i}$

$$I_o = \frac{V_o}{R_L} = \frac{9V}{1k\Omega} = 9mA$$

$$I_i = 0.1mA$$

$$A_I = \frac{9 \text{ mA}}{0.1 \text{ mA}} = 90.$$

$$(A_I)_{dB} = 20 \log 90 = 39.1 \text{ dB}.$$

(c)  $A_P =$  Power gain.

$$A_P = \frac{P_L}{P_I} = \frac{\text{Power at the load}}{\text{I/P Power}}$$

$$P_L = V_{rms} \cdot I_{rms} = \frac{9}{\sqrt{2}} \cdot \frac{9 \text{ mA}}{\sqrt{2}} = 40.5 \text{ mW}$$

$$P_I = V_{rms} \cdot I_{rms} = \frac{1}{\sqrt{2}} \times \frac{0.1 \text{ mA}}{\sqrt{2}} = 0.05 \text{ mW}$$

$$A_P = \frac{P_L}{P_I} = \frac{40.5}{0.05} = 810$$

$$(A_P)_{dB} = 20 \log 810 = 39.1 \text{ dB}.$$

(d) Power drawn by dc supplies

$$P_{dc} = V_1 I_1 + V_2 I_2$$

$$= 10 \times 9.5 + 10 \times 9.5$$

$$= 95 + 95$$

$$= 190 \text{ mWatt}.$$

(e) Power dissipated in the amplifier

$$\begin{aligned}
 P_{\text{dissipated}} &= P_{\text{dc}} + P_{\text{I}} - P_{\text{L}} \\
 &= 190 + 0.05 - 40.5 \\
 &= 149.55 \\
 &\approx 149.6 \text{ m watt.}
 \end{aligned}$$

(f) Amplifier efficiency

$$\eta = \frac{P_{\text{L}}}{P_{\text{dc}}} = \frac{40.5}{190} \times 100 = 21.3\%$$

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Q) An amplifier operating from  $\pm 3\text{V}$  supplies provides a  $2.2\text{V}_{\text{p-p}}$  sine wave across a load, when provided with  $0.2\text{V}_{\text{p-p}}$  input from which  $10\text{mA}$  peak is drawn. The avg. current is measured to be  $20\text{mA}$ . Find the voltage gain, current gain, & power gain expressed in dB. Find the supply power, amplifier dissipation and amplifier efficiency.

Ans: (a)  $A_v = \frac{\text{O/P voltage}}{\text{I/P voltage}}$



$$\Rightarrow A_v = \frac{2.2}{0.2} = 11$$

$$(A_v)_{dB} = 20 \log 11 = \boxed{20.827 \text{ dB}}$$

(b) Current gain =  $\frac{O/P \text{ Current}}{I/P \text{ Current}}$

$$I_o = \frac{V_o}{R_o} = \frac{2.2 \text{ V}}{100} = 22 \text{ mA}$$

$$A_I = \frac{I_o}{I_i} = \frac{22 \text{ mA}}{1.0 \text{ mA}} = 22$$

$$(A_I)_{dB} = 20 \log 22 = \boxed{26.848 \text{ dB}}$$

(c)  ~~$A_p = \frac{V_o I_o}{V_i I_i}$~~

$$A_p = \frac{V_o I_o}{V_i I_i} = \frac{O/P \text{ Power}}{I/P \text{ Power}}$$

$$P_o = O/P \text{ power} = V_{o,rms} \times I_{o,rms} = \frac{2.2}{\sqrt{2}} \times \frac{22 \text{ mA}}{\sqrt{2}} = 24.2 \text{ mW}$$

$$P_i = I/P \text{ power} = V_{i,rms} \times I_{i,rms} = \frac{0.2}{\sqrt{2}} \times \frac{1 \text{ mA}}{\sqrt{2}} = 0.1 \text{ mWatt}$$

$$A_p = \frac{24.2}{0.1} = 242$$

$$(A_p)_{dB} = 20 \log 242 = 23.838$$

(d) Supply Power

$$P_{dc} = V_1 I_1 + V_2 I_2$$

$$= 3 \times 20 \text{ mA} + 3 \times 20 \text{ mA}$$

$$P_{dc} = 120 \text{ mWatt}$$

$$P_{dissipated} = P_{dc} + P_I - P_o$$

$$= 120 + 0.1 - 24.2$$

$$P_{dissipated} = 95.9 \text{ mWatt}$$

(e) Amplifier Efficiency

$$\eta = \frac{P_o}{P_{dc}} = \frac{24.2}{120}$$

$$= \frac{24.2}{120} \times 100\%$$

$$\eta = 20.16\%$$

Q) An amplifier has current gain of 60 dB & power gain of 50 dB. Find its voltage gain.

Ans:  $A_p = A_v \cdot A_i$

$$20 \log A_p = 20 \log (A_v \cdot A_i)$$

$$\Rightarrow 20 \log A_p = 20 (\log A_v + \log A_i)$$

$$\Rightarrow 2 \times 10 \log A_p = 20 \log A_v + 20 \log A_i$$

$$\Rightarrow 2 \times (A_p)_{dB} = (A_v)_{dB} + (A_i)_{dB}$$

$$\Rightarrow 2 \times 50 = (A_v)_{dB} + 60$$

$$\Rightarrow (A_v)_{dB} = 100 - 60 = 40 \text{ dB} \quad (\text{Ans})$$

Q) A signal of 100 mV is fed to an amplifier which produces o/p of 8.5 volt. Find its voltage gain in dB.

Ans:  $V_i = 100 \text{ mV}$

$$V_o = 8.5 \text{ V}$$

$$A_v = \frac{V_o}{V_i} = \frac{8.5}{100 \times 10^{-3}} = \frac{8.5 \times 10^3}{100} = 85$$

$$(A_v)_{dB} = 20 \log 85 = 38.6 \text{ dB}$$

Q. 2.1:-

A periodic digital waveform has pulse width 25 μs & a period of 150 μs. Determine freq & duty cycle.

Ans → Duty Cycle =  $\frac{\text{Pulse width}}{\text{Time period}}$

$$= \frac{25 \mu\text{s}}{150 \mu\text{s}}$$

$$= \frac{1}{6} = 16.66\%$$

$$f = \frac{1}{T} = \frac{1}{150 \times 10^{-6}} = \frac{10^3 \times 10^3}{150} = 6.66 \text{ kHz}$$

Q. 2.2:-

An amplifier has voltage gain 100 V/V & current gain 1000 A/A.

Express current & voltage gain in dB & find the power gain.

Ans →  $(A_v) \text{ in dB} = 20 \log 100 = 20 \times 2 = 40 \text{ dB}$

$(A_i) \text{ in dB} = 20 \log 1000 = 20 \times 3 = 60 \text{ dB}$

~~$A_p = A_v \cdot A_i$~~   $A_p = A_v \cdot A_i$

$A_p = 100 \times 10000 = 10^5$

$(A_p) \text{ in dB} = 10 \log 10^5$   
 $= 10 \times 5 = 50 \text{ dB}$

### Digital inverters:-

It inverts the logic value of its input signal. For logic '0' o/p will be logic 1 & for logic 1 o/p will be logic 0.



V <sub>I</sub>	V <sub>O</sub>
0	1
1	0

I/P → O/P  
 0 → 1  
 1 → 0

Ques:-

Q) Which of the following signals are analog?

- (i) o/p of CE (Amplifier)
- (ii) o/p of NAND gate
- (iii) o/p of a FET amplifier

Ans :- (i)

$$V_{ce} = V_{cc} - I_{ce} R_{ce} = 30V$$

$$0 - 30V = -30V <$$

$$30V = 30V$$

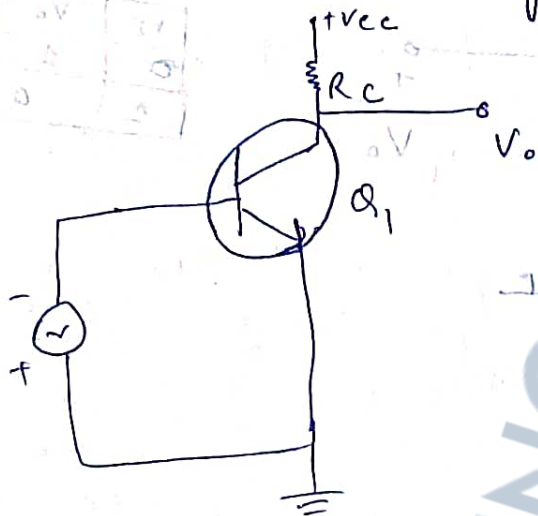
(30V) . A signal is 0/1

P.T.O

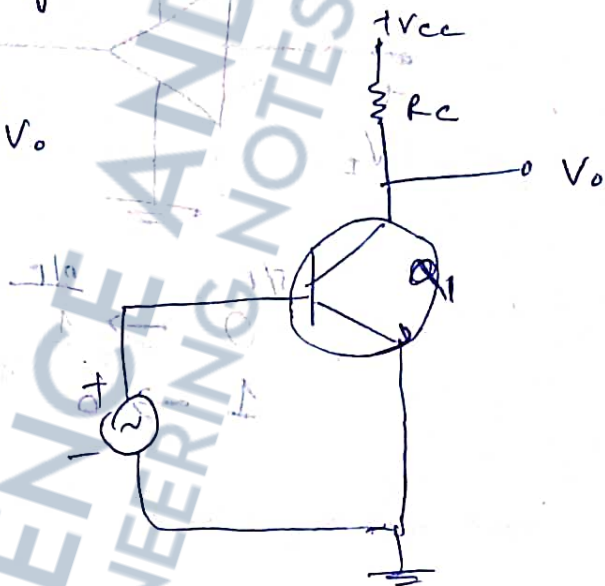
Q. 1) Q. 1) Input

Can you make logic inverter using a transistor? Justify.

Ans: Yes, we can make a logic inverter using a transistor.



(a)



(b)

→ When we give IP as low

(logic 0)

transistor  $Q_1$  is off  $I_C = 0$  (collector current is 0)

$$I_C = 0$$

$$V_{CE} = V_{CC} - I_C R_C$$

$$\Rightarrow V_o = V_{CC} - 0$$

$$\Rightarrow V_o = V_{CC}$$

OTF is logic 1. ( $V_{CC}$ )

-! When I/P is 0, O/P is 1 :

Referring to fig (b)

When we give high I/P logic (1),  
 the transistor  $Q_1$  is on.  $I_c$   
 (Collector Current) is maximum.

$$V_o = V_{cc} - I_c R_c$$

$$V_o \approx 0$$

$\therefore$  O/P is 0.

When I/P is 1, O/P is 0.  
 Thus the transistor can work as a logical inverter.

